



VISION

The Department of Electronics & Communication Engineering shall strive to create engineering technocrats for addressing the global challenges in relevant areas to cater the ever changing needs of society at National and International level.

MISSION

- 1. To ensure dissemination of knowledge through effective teaching and learning in Electronics and Communication Engineering.
- 2. To excel in Research and Development activities in emerging areas.
- 3. To promote industry-institute and institute-institute linkages for sustainable development of academic, research, training and placement activities.
- 4. To establish center of excellence in thrust areas to nurture the spirit of innovation and creativity among faculty and students.



Programme Educational Objectives (PEOs)

The Integrated Certificate and Diploma programme (DEC-CSME) shall produce skilled professionals who are:

- 1. Technically competent in maintenance, service and repair of electronic equipment.
- 2. Effective in communication and capable to work in a team.
- 3. Ethically and socially responsible for the development of country and community.
- 4. Able to demonstrate entrepreneurship skills and lifelong learning for successful career.
- 5. Able to adapt themselves with new technological challenges in relevant field.

Programme Outcomes (POs)

After successful completion of ICD (DEC-CSME) program, student will be able to:

- 1. Apply technical skill to troubleshoot, repair, service & maintenance of electronic equipment.
- 2. Use knowledge of science and humanities for personality development.
- 3. Demonstrate basic electronics engineering principles and conduct related experiments including programming skills.
- 4. Identify and analyze well-defined electronic engineering problems.
- 5. Use appropriate tools and techniques to solve well-defined electronic engineering problems systematically.
- 6. Assist in the design and development of engineering solutions.
- 7. Demonstrate technical skills in utilizing modern electronic engineering tools.
- 8. Communicate effectively with the engineering community and the society at large.
- 9. Demonstrate awareness for societal, health, safety, legal and cultural issues and the consequent responsibilities for sustainable development.
- 10. Develop entrepreneurship skills.
- 11. Understand professional ethics, responsibilities, and norms of electronic engineering practices.
- 12. Function effectively as an individual or in teams with leadership qualities.

INTEGRATED CERTIFICATE AND DIPLOMA (DEC-CSME)

		Semester-I										
S. No	Sub. Code	Subject Name	L	Т	Р	Hrs.	Credits					
1.	AM-111	Mathematics- I	4	1	0	5	5					
2.	PH-111	Physics-I	4	0	2	6	5					
3.	CY-111	Chemistry-I	4	0	2	6	5					
4.	HU-111	Communication Skills-I	2	0	0	2	2					
5.	EE-111	Fundamental of Electrical Engineering	3	0	2	5	4					
6.	WS-122	Workshop Practice	0	0	4	4	2					
7.	EC-112	Electronic Devices	2	0	2	4	3					
		Total	19	1	12	32	26					
	Semester-II											
S. No	Sub.	Subject Name	L	Т	Р	Hrs.	Credits					
	Code											
1	AM-121	Mathematics- II	4	1	0	5	5					
2	PH-121	Physics-II	4	0	2	6	5					
3	CY-121	Chemistry-II	4	0	2	6	5					
4	ME-121	Engineering Drawing	0	0	4	4	2					
		Disitel Electronice	3	0	4	7	5					
5	EC-121	Digital Electronics	- 3	U	4	/	-					
5 6	EC-121 EC-122	Electronic Workshop Practice-I	0	0	4	4	2					
	-		-	-	-	•	2 24					
	-	Electronic Workshop Practice-I	0	0	4	4						
	-	Electronic Workshop Practice-I Total	0	0	4	4						



		Semester-III (B)								
S. No	Sub. Code	Subject Name	L	Τ	Р	Hrs.	Credit s			
1	HU-211	Communication Skills-II	1	0	2	3	2			
2	CS-216	Computer Fundamentals	3	0	2	5	4			
3	EC-213	Electronic Measurements & Instrumentations	3	1	2	6	5			
4	EC-214	Analog Communication	3	1	2	6	5			
5	EC-217	Consumer Electronics	3	1	0	4	4			
6	EC-218	Troubleshooting of Electronics Equipment-I	0	0	4	4	2			
7	MC-211	Moral values and Professional ethics	1	0	0	1	0			
		Total	14	3	12	29	22			
Semester-IV										
S. No	Sub.	Subject Name	L T P			Hrs.	Credit			
	Code						S			
1	AM-221	Applied Mathematics	3	1	0	4	4			
2	EC-223	Fundamentals of Microprocessor & Microcontroller	3	1	4	8	6			
3	EC-226	Computer Programming & Application	3	0	2	5	4			
4	EC-227	Service & Maintenance of Computer	2	0	4	6	4			
5	EC-228	Network Theory	3	1	0	4	4			
6	EC-229	Troubleshooting of Electronic Equipment-II	0	0	4	4	2			
		Total	14	3	14	31	24			
		Semester-V(A)								
	TP301	Four Weeks Industrial Training during summer vacations				160	S/US			



		Semester-V (B)					
S. No	Sub Code	Subject Name	L	Τ	Р	Hrs.	Credits
1	MC-311	Environmental Studies	2	0	0	2	2
2	HU-311	Entrepreneurship	2	0	0	2	2
3	EC-311	Electromagnetic Field Theory	3	0	0	3	3
4	EC-312	Linear IC's & Application	3	1	4	8	6
5	EC-313	Digital Communication	3	1	2	6	5
6	EC-314	Electronic Workshop Practice-II	0	0	4	4	2
7	EC-315	Principles of Microwave Engineering	2	1	0	3	3
8	TP-301E	Industrial Training					S/US
		Total	15	3	10	28	23
		Semester-VI					
S. No	Sub	Subject Name	L	Т	Р	Hrs.	Credits
	Code						
1	EC-321	Industrial Electronics	3	1	4	8	6
2	EC-322	Wireless & Mobile Communication	3	0	0	3	3
3	EC-323	Microprocessor & Microcontroller Applications	3	1	2	6	5
4	EC-324	Antenna Wave Propagation	3	0	0	3	3
5	EC-325	Microelectronics	3	0	2	5	4
7	EC-327	Project	0	0	4	4	2
		Total	15	2	12	29	23
	Total The	ory & Practical Load for Diploma	92	13	76	181	142

Note:		The r	required credits for certificate programme	e				96					
		The r	required credits for ICD programme					142					
		Maxi	mum courses in one semester					7					
		Maxi	Maximum Contact Hrs.										
The common courses and their credits are fixed for all ICD													
		programmes.											
Course	s offered	to oth	er Departments:										
S. No	Sub Cod	le	Subject Name	L	Т	Р	Hrs.	Credits					
1	EC-211		Fundamental of Electronics	3	0	2	5	4					
		Engineering											
2	EC-221		Fundamental of Electronics	3	0	2	5	4					
			Engineering										

						EC-1										
					E	lectroni	<u>c Device</u>	1			<u> </u>					
				L			<u>T</u>		P							
				2			0		2							
			onal Ma													
		End S	Semeste	r Exami	ination	Marks					50					
Course	e	The co	ourse int	ends to p	provide	the basic	concept	t and cha	racteristi	cs of the e	lectronics	devices				
Object	tives:								le the un	derstandi	ng applic	ation of				
						nd simp										
Course		1.			nowledge	e about	semico	nductor	physics	for intrin	sic and e	extrinsic				
Outco	mes:		materi													
 This course gives an overview of various semiconductor devices. Acquired knowledge about active and passive components, voltage and current 																
		3.	-		wledge	about ac	ctive and	d passiv	e compo	nents, vo	ltage and	curren				
sources.																
		4.					ng prin	ciples of	f electror	nic circuit	s e.g., Re	ectifiers				
				, Regula			• / 1									
	DO1	DOA							outcom		DO11	DO10				
001	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12				
<u>CO1</u>		N		N												
<u>CO2</u>	N	N	V	1								<u> </u>				
<u>CO3</u>		N		N												
CO4			\checkmark	N N	√	\checkmark						0.1				
T 7 1 4	1.0		D		<u>nit-I</u>	I.I.C.					x 1 .	8 hrs				
	·									apacitors,						
			-					•	-	onents; fi						
resistar	nces, the	ir vario	us types	fixed an	d variab	le capac	itors, the	eir vario	us types a	and import	ant specif	fications				
and col	lor code	s.														
Voltag	ge and o	current	source	s - Vol	tage an	d Curre	nt sourc	es, Syn	nbols and	I Graphic	al represe	entation				
charact	teristics	of ideal	and pra	ctical so	urces. C) verview	of AC,	DC, Ce	lls and Ba	atteries, E	nergy and	l Power				
			1		nit-II						01	6 hrs				
Introd	uction:	Classif	ication of			conduct	ing and	insulatin	g materia	als through	a brief r					
							0		•	uctor Ma						
	ature on			•			ig iviate	11 u 15, 50		uctor 101u	terrar, Er	10015 0				
temper	ature on	Condu				•				1						
					<u>it-III</u>							10 hrs				
C	onducto	r Diod	es: Ato	mic stru junction		of Germ	anium a	and Sili		i-conducto	ors; intrir	nsic and				



	Unit-IV 8 hrs.												
Transistors: Introduction to a transistor, working of a PNP and NPN transistor, input and output													
characteristics, transistor configurations, biasing of a transistor, amplifying action of a transistor, comparison													
of different configurations, common emitter amplifier circuit, load line, concept, field effect transistor FET,													
JFET, MOSFET, their characterist	tics and applications, unijunction tra	nsistor (UJT).											
RECOMMENDED BOOKS													
Title Author Publisher													
1. Basic Electronics and Linear	N N Bhargaya and Kulshreshta	McGraw Hill											

1. Basic Electronics and Linear	N N Bhargava and Kulshreshta	McGraw Hill
Circuitis		
2. Electronics Devices and	Miliman and Halkias	McGraw Hill
Circuits		

	Electronic Devices Lab Course To reinforce learning through hands-on experience by examining the electrical													
Course	9	To rei	nforce	learning	throug	h hands	s-on ex	perience	by ex	amining	the el	ectrical		
Object	ives:								as diode					
		provide	the stud	ent with	the capal	bility to	measure	and reco	ord the ex	perimen	tal data,	analyze		
				rious ser										
Course	e	1.	To unde	erstand t	he functi	ioning o	f various	s electron	nic instru	iments li	ke CRO	, signal		
Outco	mes:	1. To understand the functioning of various electronic instruments like CRO, signal generator and multimeter.												
		2. To understand the characteristics of diode and BJT and verify their responses.												
3. To construct various electronic circuits on the bread board and analyses their out														
	Mapping of course outcomes with program outcomes													
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1														
CO2														
CO3						\checkmark								
		1. 2. 3. 4. 5. 6. 7. 8. 9.	To calce To obse To obse To obse To obse To verif To cons its rippl To cons its rippl	sure value ulate the erve the f erve the V erve the V fy the NH etruct hall e factor. etruct full e factor.	value of Front pan Front pan VI charace VI charace PN and P f-wave r l-wave re	various el of CR el of sig cteristics cteristics NP tran ectifier,	passive O. nal gener of semic of Zene sistors. wave sha	compone rator. conducto r diode i ape of the ape of the	the color ents usin or diode i n reverse e electric e electric age regul	g multim in forwar e bias. cal signa cal signal	eter. d bias. l and cal			



						EC-121						
		[L	Digit	al Elect	ronics T		Р		Cr	edits
				3			0		4			5
		Session	nal Mar	-			U		-			50
					ation M	arks						50
		This c	ourse w	ill provi	ide the i	introduc	tion of	the basi	c princij	oles, cha	racterist	ics and
				•								
Course	e	operations of a digital system. Next focus is to give the detail description about Boolean algebra and the various methods of Boolean function reduction, designing of combinational										
Object	ives:	circuits by using logic gates, design and analyses of asynchronous and synchronous										
		sequential Circuits using flip flops and at last to understand principle of operation of shift										
		-			D conve	-				-p-• •1 •]		01 0111
1. Learn to apply Boolean laws/K-Map-method method to reduce a given Boolean funct												inction
		2. Able to design & realize combinational logic circuits using logic gates for various										
Course	e	practical applications.										
Outcor	mes:	3. Able to demonstrate the operation of flip-flops, counters, and shift registers.										
					arious A				is, and si	int regist		
					ourse out				tcomes			
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1					 √	√						
CO2		V										
CO3				\checkmark								
CO4												
					Unit-I	<u> </u>	1	1				10 hrs
Signals Reviev convers	s, digital v of nu sions Si	systems, nber sy gned and	, and the s tem: D d unsign	ir applicates in applicates in a constant of the second seco	ation. Binary,	Octal, a ary oper	nd hexa	decimal	number	nd Advan system, ion, Mul	and the	ir inter
			,,	,	Unit-I							12 hrs
										OR, XOF		
	•				•			•	s; realiza	tion of s	simple I	Boolear
-	-	-	0		tion to k							
					al circuit	design,	adders,	subtracto	or, code o	converter	s, multi	plexers
demult	iplexer,	encoders	and dec	oders.	TI:4 TT	т						16 has
Secular	ntial Cir	enite. Ir	troducti	on Logi	Unit-II		table tir	ning dia	aram an	d operati	on of fo	16 hrs
-				•	•			•	•	JK flip f		
					T, D, an							
										ters, Div	ide by N	N ripple
					r with tir							
Shift F		s: Introd	uction, S	Serial in	parallel	out, ser	ial in se	rial out,	parallel	in serial	out, par	allel in
	Lout											

Page 9





Unit-I	V	10 hrs.								
A/D and D/A converters: Binary Weighted D/										
A/D converter, Dual Slope A/D converter, Successive Approximation A/D Converter.										
RECOMMENDED BOOKS										
Title	Author	Publisher								
1. Fundamentals of Digital Electronics	A. Anand Kumar	PHI 2 nd Edition								
2. Digital Electronics	R P Jain	McGraw Hill Education 4 th Edition								
3. Digital Logic Designs	Morris Mano	PHI 5 th Edition								
4. Digital Systems: Principles and Applications	R J Tocci	PHI 10 th Edition								

					Digital	Electro	nics Lab)						
Course	e	To mal	ke stude	nts fami	liar with	n differe	ent types	s of desi	gns as se	equentia	l logic c	ircuits,		
Object	ives:	combir	national	logic ci	rcuits, t	rouble s	shooting	g of vari	ous digi	tal syste	ms & st	tudy of		
		various	s digital	system	is. Kno	wledge	of basi	c electr	onics &	z digital	technic	ques is		
			in under							U				
Course	9									ke deco	ders, en	coders,		
Outcon	nes:	multi	plexers,	and de-n	nultiplex	ers as w	ell as ari	thmetic o	circuits (l	half adde	r, full ad	der and		
		multi	plier).											
2. To analyses and design sequential digital circuits like flip-flops, registers, cou														
3. Understand the importance and need for verification, testing of digital logic an														
for testability.														
Mapping of course outcomes with program outcomes														
	PO1	PO2	PO2 PO3 PO4 PO5 PO6 PO7 PO8 PO9 PO10 PO11											
CO1				\checkmark	\checkmark									
CO2														
CO3														
		 List of Experiments: 1. Verification and interpretation of truth tables for AND, OR, NOT NAND, NOE Exclusive OR (EXOR) and Exclusive NOR (EXNOR) gates. 2. Realization of logic functions with the help of NAND or NOR gates. 3. To design a half adder using XOR and NAND gates and verification of its operation. 4. Construction of a full adder circuit using XOR and NAND gates and verify its oper 5. To design a NOR Gate Latch and verification of its operation. 6. Verification of truth table for positive edge triggered, negative edge triggered, triggered IC flip-flops (At least one IC each of D latch, D flip-flop, JK flip-flops). 7. Verification of truth table for encoder and decoder ICs, Mux and Demux. 8. To design a 4 bit SISO, SIPO, PISO, PIPO shift registers using JK/D flip flop verification of their operation. 9. To design a 4 bit ring counter and verify its operation. 												
		Use	10. Asynchronous Counter ICs Use of IC 7490 or equivalent TTL (a) divide by 2 (b) divide by 10 Counter											



				Elec	tronics	EC-122 worksho	e op Pract	ice-I					
			L	1		Т		Р			Credits		
	·		0			0)	4			2		
Course Objectives	s:	calibrat		onitor a	variety					logy and to appl			
Course Outcomes:	:	2. To	recogniz	ze and te	est vario	us active				timeter, omponen			
			pacitors,				· ·,	DCD					
		3. To	design a										
	01	DO2		_		1	vith prog	-	r	DO10	DO11	PO12	
	PO1PO2PO3PO4PO5PO6PO7PO8PO9PO10PO11CO1 $$ $$ $$ $$ $$ $$ $$ $$ $$ $$												
	$\sqrt[n]{}$	$\frac{N}{}$	$\sqrt{1}$										
	$\sqrt[n]{}$	v	 √										
		 2. 3. 4. 5. 6. 7. 8. 9. 10. 11. 	This top various to check Study of basic el Transist Testing compon To study Solderin electron To solde To use a To find To study To study To study for asser a. Half y b. Full y c. Simpl	vic cover electronic the frequent of electronic ors, IC's of elect ents usir y and vis ng pract ic compose er the IC a Zener of the Q poor y the inp y the inp y the am bling of s mbly of wave rec	s the use a compo- juency a onic con compor s etc.) an ctronic on s etc.) an ctronic on a g multin sualize th tice: Cir onents. base on liode as bint for c ut and on ut and o plifying imple el the follo tifier cir lashing of	e of mult onents an and ampli mponent d identif compon meters (<i>A</i> ne solder cuit asse a genera a voltage ommon utput V-1 utput V-2 characte lectronic wing cir cuit with cuit with	imeter to d (Study itude of a ts: This d circuit fication o ents: Th Active an ing kit an embling al purpos e regulato emitter of I characto I characto c circuits	o check v of CRO i signal v topic co symbols f compo his topic d passiv d variou practice e PCB. or. onfigura eristics o eristics o eristics o : This to hout filte	voltage, c vaveform vers the covers the covers e compo us solder using pr tion. f commo of commo of commo d PNP t pic cove	familiar ors, Cap ues. how to	nd also to rs the pro- ization of acitors, T test ele autions. reuit boa er config configur.	o check ocedure of some Diodes, ectronic rd with uration.	

Page 13

						TP-2	01							
			Two W	eeks Pr	actical	Trainin	g durin	g summ	ner vaca	tions				
Course	e	Studer	nts have	to unde	ergo two	o – weel	c practio	cal train	ing in D	Departmen	t of Elect	ronics &		
Object	tives:	Comm	unicatio	on Engii	neering	so that	they be	come av	ware of	the pract	ical appli	cation of		
		theoretical concepts studied in the classrooms.												
Course	e	1. Gai	1. Gain experience in various domains such as hardware, software, maintenance, and											
Outco	mes:	testing	testing.											
			Mapping of course outcomes with program outcomes											
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1														



	EC-213 Electronic Measurements and Instrumentations L T P Credits														
			_	-	Measure						<u>C1!4.</u>				
				և 3				_							
		Gaustan		-			1	2	2		5				
			al Mark		·						50				
			mester I								50				
Course							unit, dir								
Object	ives:	-					oridges. I	t discuss	ses the C	CRO in d	etail. Fi	nally, it			
			ces signa	-											
Course	e					• •	f errors in			suremen	ts.				
Outco	mes:						ents and								
		3. Able	to under	stand the	e CRO, s	signal ge	nerators	and anal	ysers.						
	I	I		<u> </u>			vith prog			1		1			
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12			
CO1															
CO2															
CO3	$\begin{array}{c c c c c c c c c c c c c c c c c c c $														
				Un								12 hrs.			
Unit, dimensions, and standards: Scientific notations and metric prefixes. SI electrical units, SI temperature scales, dimension, and standards. Measurement Errors: Gross error, systematic error, absolute error and relative error, accuracy, precision, resolution and significant figures, Measurement error combination, basics of statistical analysis.												nt error			
				Uni						.~		12 hrs.			
		nent, galv													
		er system													
		surement		esistance	e measu	ring inst	ruments.	AC bric	ige theo	ry, capa	citance I	oridges,			
Inducta	ince brid	lges, Q m	ieter.	T I •	TTT							10 1			
CDO.	CDT	C	11	Unit			11			6 14		12 hrs.			
		ave form								voitage	, rrequen	icy, and			
phase t	by CRU	, oscilloso	cope proi			specific	ations an	a perior	mance.			12 hrs.			
Signal generator and analyzer: Signal generator: Sine wave, non-sinusoidal signal, and function generators. Spectrum analyzer and distortion.										unction					
RECOMMENDED BOOKS															
Title Author Publisher															
1. Elec	tronic Ir	nstrument	tation and	d Dav	id A. Be	11		2	nd Ed., I	PHI, Nev	v Delhi				
Measur	rements								008.						
2. Elec	tronic N	leasurem	ents and	Oliv	er and C	lage		Т	MH, 20	09.					
Instrum	nentatio	n													
				Alar	n S. Mor	ris					orth				
3. Measurement and Instrumentation PrinciplesAlan S. MorrisElsevier (Butterworth Heinmann), 2008										n), 2008					



			Electronic Measurements and Instrumentations Lab											
Course Object				-		suremen d commu				entation r	requirem	ent as a		
Course						rement c		U	0					
Outcor	nes:						-	. resistar	nce and c	apacitan	ce using	various		
			bridges					,		1	0			
		3.	0	operate	signal ge	enerators	and sign	nal analy	zer for e	lectronic	measure	ements.		
			3. Able to operate signal generators and signal analyzer for electronic measurements. Mapping of course outcomes with program outcomes											
	PO1	DO3	PO2PO3PO4PO5PO6PO7PO8PO9PO10PO11PO12 $$ $$ $$ $$ $$ $$ $$ $$ $$											
CO1	roi	FU2												
CO1														
CO3	Ń			,	Ń									
		2. 3. 4. 5. 6. 7. 8. 9.	To obse Measure Measure Measure To find To find To study recorder To study data acq Displace To mea	rve the c ement of ement of ement of Q of a c y & obse c. y & obse uisition ement m asuring	lynamic Finducta Small re Capacit medium oil by a s rve the re erve the system. easurem the temp	nce by N esistance ance of t resistar series res ecording acquisiti ent using	g of diffe faxwell' by the K he Scher ice with conance r of differ on of da g LVDT, of sold	erent sig s bridge Celvin's tring Bric the help method a rent sign ta from Inductiv ering by	nals on o Bridge. Ige. of Whea and verif als from strain ga	oscillogra at stone b y it by us sensors o uge-base p and cap thermoc	ridge. sing Q-m on magne ed transd pacitive j	neter. etic tape ucer on pick up.		



						EC-214						
					Analog	Commu	nication	1				
				L			Т]	P		Credits	
				3			1	,	2		5	
		Sessio	nal Ma	rks							50	
		End S	emester	r Examiı	nation M	larks					50	
Course	2	The f	ocus of	the cours	se is on u	nderstan	ding the	importa	nce and	theories (of analog	g
Object	ives:	comn	nunicatio	on syster	ns. The s	tudents	will unde	erstand t	he variou	is analog		
				•	iques, AN					0		ption
					modulat	. 0		-,	,			F
Course					e about th		-	onconta	ofvoriou	a analog		
Course Outcon	-		•	ation system		ie iuliua		oncepts	or variou	is analog		
Outcol	<u>IIC.</u>			•	ods of ge	neration	and dete	ection of	AM and	FM		
					e about A							
					ulse com					puon.		
					urse out				tcomes			
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	101	$\sqrt{1-\frac{1}{1-\frac{1}{\sqrt{1-\frac{1}{1-\frac{1}{\sqrt{1-\frac{1}}{1-\frac{1}}}}}}}}}}$	100			200		100	2.07	1010	1 0 11	
CO2			V									
CO3		V	V									
CO4												
				Un	it-I							12 hrs.
Introd	uction:	Comm	unicatio	n, infor	mation,	Messag	ge and	Signal	s, Elec	tromagne	etic Sp	ectrum,
Classif	ication of	f signals	, Period	lic and n	on-perio	dic signa	als, Anal			gnals, De	etermini	stic and
random	n signals,	the eler	ments of	f a comr	nunicatio	on syster	n, Modu	ilation, I	Definition	n, Types	of mod	ulation,
Need for	or modula	ation.										
Amplit	tude Mo	dulation	n: Defi	nition, E	Expressio	n of AN	/I wave,	modulat	ion inde	x, freque	ency, sp	ectrum,
bandwi	dth, pow	er conte	nts of si	debands	and carri	ier.						
				Uni	it-II							14 hrs.
Freque	ency Mod	lulation	: Modul	lation inc	lex, frequ	lency de	viation, f	frequenc	y spectru	ım and ba	andwidtl	n of FM
	Power con											
	ation of									-		
· •	tions, Ba	<u> </u>	-	•	eration, C	Beneratio	on of DSI	B and SS	B signal	s. Basic	principle	e of FM
generat	ion, Vara	ctor dio	de modu									
				Uni								12 hrs.
	Transmi				•		and FM	transmit	ter, Worl	king prin	ciple wit	h block
	n of AM					ne).						
Domod	Inlation				1 -						1 -	
	ed slope d			ction, en	velope d	letector,	FM dete	ection, b	asic prin	ciple of	slope de	tection,



Unit-IV		10 hrs.
Pulse Modulation: Sampling process, Sam		t PAM, PWM and PPM and
typical applications, Reconstruction of messa	age, Pulse code modulation.	
RECO	OMMENDED BOOKS	
Title	Author	Publisher
1. Electronic communication systems	Kennedy	Tata McGraw Hill
2. Electronic Communications System:	by Wayne Tomasi	5 th Edition, Pearson
Fundamentals Through Advanced,		Education
3. Electronic communications	Roddy and Coolen	Prentice Hall of India
4. Principles of communication systems	Taub and Schilling	Tata McGraw Hill
5.Communication system (Analog and	Sanjay Sharma	Katson Books
Digital)		



		Analog Communication Lab This lab aims to provide basic practical knowledge about different AM and FM modulation													
Course	9								lifferent	AM and	FM mod	lulation			
Object	ives:		ues by ol												
Course	e	1.	Calibrat	e modul	ated as v	vell as de	emodulat	ted wave	forms of	n CRO.					
Outcon	nes:	2.	Generat												
		3.	sensitivity and selectivity.												
		Mapping of course outcomes with program outcomes													
	PO1	PO2													
CO1															
CO2															
CO3															
		1. 2. 3. 4. 5. 6. 7. 8. 9.	modula To obse Genera To obse To gene To stud parame To obse detecto To obse	erve amp tion Amp tion. erve env tion of I tion of s erve free erate a H ly super ters viz. erve the r	litude n velop de DSB-SC single si quency r FM Sign heterod sensitiv wavefo	nodulate tector fo S signal de band modulat al and r lyne AM vity and rm of d	ed Enve or demo using ba signal. ion and neasure I receiv selectiv emodula	lop and dulation alanced its wav depth o er and r vity. ated FM	determin n of AM modula eform co of modu neasures	ine dept [signal. tor. on CRO.	receiver	ratio			



End SCourseThe cObjectives:micropprincipTV isgadgetCourse1.Outcome:2.3.				EC-217								
End SCourseThe cObjectives:The cDijectives:The cprincipTV is gadgetCourse1.Outcome:2.3.4.PO1PO2CO1 $$ CO2 $$ CO3 $$ CO4 $$ Audio System: Micromoving coil. velocity, loaded, woofer, tweeteSound Recorder:Sound Recorder:Sound Recorder:Sound Recorder:Sound Recorder:Sound Recorder:			Consu	mer Ele	ctronics							
End SCourseThe cObjectives:The cDijectives:The cprincipTV is gadgetCourse1.Outcome:2.3.4.PO1PO2CO1 $$ CO2 $$ CO3 $$ CO4 $$ Audio System: Micromoving coil. velocity, loaded, woofer, tweeteSound Recorder: Somechanism, Digital so		L			Т	I			Credits			
End SCourseThe cObjectives:micropprincipTV is gadgetCourse1.Outcome:2.3.4.PO1PO2CO1 $1000000000000000000000000000000000000$		3			1	()		4			
Course Objectives:The c microp princip TV is gadgedCourse Outcome:1.Outcome:2.3.4.PO1PO2 CO1 $\sqrt{\sqrt{2}}$ CO2CO1 $\sqrt{\sqrt{2}}$ CO3 $\sqrt{\sqrt{2}}$ CO3Audio System: Micromoving coil. velocity, loaded, woofer, tweetedSound Recorder:So mechanism, Digital so	nal Marl								50			
Objectives: microprincip $princip princip TV is gadget princip Course 1. Outcome: 2. 3. 3. 4. 3. CO1 CO2 CO3 CO4 Audio System: Micromoving coil. velocity, loaded, woofer, tweetee Sound Recorder: Somechanism, Digital so $	emester	Examina	ation Ma	rks					50			
Course1.Outcome:2.Outcome:2.3.3.4.3.CO1 $$ CO2 $$ CO3 $$ CO4 $$ Audio System: Micromoving coil. velocity, loaded, woofer, tweeteeSound Recorder:Sound Recorder:	ourse ain phones an ples of au given. At	d loudsp dio and last work	eakers. F video rec ing princ	urther th ording s ciple, blo	is subjec ystems. ck diagra	t will int An intro	roduce the duction	ne studen about sat	ts with v ellite an	vorking d cable		
CO1 $$ CO2 $$ CO3 $$ CO4 $$ Audio System: Micro moving coil. velocity, loaded, woofer, tweeteSound Recorder:So mechanism, Digital so	1. Required knowledge of various types of microphones and foudspeakers.											
CO1 $$ CO2 $$ CO3 $$ CO4 $$ Audio System: Micro moving coil. velocity, loaded, woofer, tweeteSound Recorder:So mechanism, Digital so		ing of co	urse out	comes v	vith prog	gram ou	tcomes					
CO2 √ CO3 √ CO4 √ √ Audio System: Micromoving coil. velocity, loaded, woofer, tweete √ Sound Recorder: Somechanism, Digital somechanism, Source Source	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO3 √ CO4 √ √ Audio System: Micromoving coil. velocity, loaded, woofer, tweeter Sound Recorder: Some chanism, Digital some cha												
CO4 √ Audio System: Micromoving coil. velocity, loaded, woofer, tweeter Sound Recorder: Sound Recorder:												
Audio System: Micro moving coil. velocity, loaded, woofer, tweete Sound Recorder: So mechanism, Digital so		\checkmark										
moving coil. velocity, loaded, woofer, tweete Sound Recorder: So mechanism, Digital so												
moving coil. velocity, loaded, woofer, tweete Sound Recorder: So mechanism, Digital so		Un	it-I							12 hrs.		
mechanism, Digital so	crystal,	condense 1ge, mult	er type, c i-speake	ordless	microph	one, lou	dspeaker			g, horn		
mechanism, Digital so		Uni	<u>t-II</u>							12hrs.		
	und recor	ding on t	tape and	disc, CE) system	and DV	D Forma	it, CD/D	-	-		
		Uni	t-III							12 hrs.		
Satellite TV and Cab up-link, and down-link only). Cable TV: Block Dia	frequenc	ies. Bloc	k diagra	m and w	orking p	rinciple	of TVRC) receive	r (TV re	ceiving		



	Unit-IV	12 h	rs.							
		application of Digital watch /clock, Calculat al camera, DTH, Electronic ignition system f								
RECOMMENDED BOOKS										
Title	Author	Publisher								
1. Audio Visual Systems	Sanjay Attri.	BPB Publishers New Delhi								
2. Audio Video Systems	TMH, New Delhi India									



LTPCredits0042Course Objectives:The objective of this lab is to give students an in depth knowledge of various electron audio and video devices and systems. Further this subject will introduce the students w working principles, main features of consumer electronics gadgets/goods/devices 1 audio-systems, DTH and cable TV system, VCR, and other items like, mobile phot microwave ovens etc. which in-turn will develop in them capabilities of assembling, fat diagnosis and rectification in a systematic wayCourse Outcomes:1. Understand the frequency response of loudspeaker, microphone, and audio-amplifie audio-amplifie2. Understand the working principle and fault diagnosis of various consume equipment/gadgets.Mapping of course outcomes with program outcomes				EC-218 Troubleshooting of Electronic Equipment-I											
O O 4 2 Course Objectives: The objective of this lab is to give students an in depth knowledge of various electro audio and video devices and systems. Further this subject will introduce the students v working principles, main features of consumer electronics gadgets/goods/devices i audio-systems, DTH and cable TV system, VCR, and other items like, mobile phot microwave ovens etc. which in-turn will develop in them capabilities of assembling, for diagnosis and rectification in a systematic way Course Outcomes: 1. Understand the frequency response of loudspeaker, microphone, and audio-amplifite 2. Understand the working principle and fault diagnosis of various consum equipment/gadgets. Mapping of course outcomes with program outcomes PO1 PO2 PO3 PO4 PO5 PO6 PO7 PO8 PO9 PO10 PO11 PC CO2 √ √ √ √ √ √ √ √ 3. To study the block diagram and working of an audio tape recorder. 3. To study & plot the frequency response of Audio Amplifier. 6. To study & plot the frequency response of Cross-over network used in ster amplifier. 7. To understand the working of DTH system. 8. To understand the working of DTH system. 8. To understand the working of adjital clock and fine out the faults. 11. To study the block diagram and working of a VCR trainer. 12. To measure the wav									-	Г		Credits			
Objectives: audio and video devices and systems. Further this subject will introduce the students v working principles, main features of consumer electronics gadgets/goods/devices i audio-systems, DTH and cable TV system, VCR, and other items like, mobile photmicrowave ovens etc. which in-turn will develop in them capabilities of assembling, for diagnosis and rectification in a systematic way Course Outcomes: 1. Understand the frequency response of loudspeaker, microphone, and audio-amplific Quipment/gadgets. Mapping of course outcomes with program outcomes Mapping of course outcomes with program outcomes PO1 PO1 PO2 PO3 PO4 PO5 PO6 PO7 PO8 PO9 PO10 PO11 PC CO2 V V V V V V V V Itst of Experiments: 1. To plot the directional pattern of a loudspeaker. 2. To plot the directional pattern of a noving coil microphone. 3. To study the block diagram and working of an audio tape recorder. 4. Fault Finding in Tape Transport Mechanism of a Cassette Tape Recorder. 5. To study & plot the frequency response of Cross-over network used in ster amplifier. 7. To understand the working of DTH system. 8. To understand the working of microwave oven. 9. To understand the working of digital clock and fine out the faults. 11. To study the block diagram and working of a VCR trainer.															
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working principles, main features of consumer electronics gadgets/goods/devices laudio-systems, DTH and cable TV system, VCR, and other items like, mobile phr microwave ovens etc. which in-turn will develop in them capabilities of assembling, fr diagnosis and rectification in a systematic wayCourse Outcomes:1. Understand the frequency response of loudspeaker, microphone, and audio-amplifie 2. Understand the working principle and fault diagnosis of various consume equipment/gadgets.PO1PO2PO3PO4PO5PO6PO7PO8PO9PO10PO11PCCO1 $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ CO2 $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ Co2 $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ Co3 $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ Co4 $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ Co5 $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ Co5 $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ Co5 $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ Co5 $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ $\sqrt{1}$ Co6FO3P						•			·		•				
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Course Outcomes: I. Understand the frequency response of loudspeaker, microphone, and audio-amplifie Poil PO2 PO3 PO4 PO5 PO6 PO7 PO8 PO9 PO10 PO11 PC CO1 √ √ √ √ √ √ √ ✓ CO2 √ √ √ √ √ ✓ ✓ ✓ List of Experiments: 1. To plot the directional pattern of a loudspeaker. 2. To plot the directional pattern of a moving coil microphone. 3. To study the block diagram and working of an audio tape recorder. 4. Fault Finding in Tape Transport Mechanism of a Cassette Tape Recorder. 5. To study & plot the frequency response of Cross-over network used in ster amplifier. 6. To study & plot the frequency response of cross-over network used in ster amplifier. 7. To understand the working of microwave oven. 9. To understand the working of digital clock and fine out the faults. 11. To study the block diagram and working of a VCR trainer. 12. To measure the AC/DC voltage at different point in Different section of VCR trainer. 13. To measure the waveform at different point in Different section of VCR trainer. 13. To PC PC PC <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th>in them</th> <th>capabili</th> <th>ities of a</th> <th>ssemblin</th> <th>g, fault</th>									in them	capabili	ities of a	ssemblin	g, fault		
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CO1 V V V V V CO2 V V V V V V List of Experiments: 1. To plot the directional pattern of a loudspeaker. 2. To plot the directional pattern of a moving coil microphone. 3. To study the block diagram and working of an audio tape recorder. 4. Fault Finding in Tape Transport Mechanism of a Cassette Tape Recorder. 5. To study & plot the frequency response of Audio Amplifier. 6. To study & plot the frequency response of cross-over network used in ster amplifier. 7. To understand the working of DTH system. 8. To understand the working of microwave oven. 9. To understand the working of digital clock and fine out the faults. 11. To study the block diagram and working of a VCR trainer. 12. To measure the AC/DC voltage at different point in Different section of VCR trainer.		DO1	DOA		-		1		-		DO10	DO11	DO1		
CO2 V V V List of Experiments: 1. To plot the directional pattern of a loudspeaker. 2. To plot the directional pattern of a moving coil microphone. 3. To study the block diagram and working of an audio tape recorder. 4. Fault Finding in Tape Transport Mechanism of a Cassette Tape Recorder. 5. To study & plot the frequency response of Audio Amplifier. 6. To study & plot the frequency response of cross-over network used in ster amplifier. 7. To understand the working of DTH system. 8. To understand the working of microwave oven. 9. To understand the working of digital clock and fine out the faults. 11. To study the block diagram and working of a VCR trainer. 12. To measure the AC/DC voltage at different point in Different section of VCR trainer. 13. To measure the waveform at different point in Different section of VCR trainer	CO1	1	1	1	P04	P05	1	PO/	PO8	PO9	POIU	POII	PO12		
 List of Experiments: To plot the directional pattern of a loudspeaker. To plot the directional pattern of a moving coil microphone. To study the block diagram and working of an audio tape recorder. Fault Finding in Tape Transport Mechanism of a Cassette Tape Recorder. To study & plot the frequency response of Audio Amplifier. To study & plot the frequency response of cross-over network used in ster amplifier. To understand the working of DTH system. To understand the working of microwave oven. To understand the working of digital clock and fine out the faults. To study the block diagram and working of a VCR trainer. To measure the AC/DC voltage at different point in Different section of VCR trainer. 				1	2	2	v								
15. To study the block diagram and working principle of mobile trainer.16. To measure the AC/DC voltage and waveform at different point in Different sector of mobile trainer.			2. 3. 4. 5. 6. 7. 8. 9. 10. 11. 12. 13. 14. 15.	To plot To study Fault Fi To study To study To under To under To under To under To under To study To mean To find To study To study To mean To find	the direct y the blo nding in y & plot by & plot er. erstand the erstand the erstand the erstand the sure the blo sure the blo out fault y the blo sure the blo	tional pa ck diagra Tape Tr the frequent the frequent the working working the working the	attern of am and y ansport uency re equency ng of D7 ng of mi ng of mi ng of dig am and y c voltage m at diff rent sect am and y	a movin working Mechani sponse o respons TH syster crowave ashing m gital cloc working e at diffe erent poi ions of V working	g coil mi of an auc sm of a 0 of Audio e of cro m. oven. achine. k and fin of a VCl erent poi int in Dif /CR train principle	dio tape a Cassette Amplifie ss-over ne out the R trainer int in Di fferent se ner. e of mobi	recorder. Tape Re er. network e faults. ifferent s ection of ile traine	corder. used in section o VCR tra r.	of VCR iner.		

Page 22

						EC-211								
		1		Fundan	ientals of			_	_					
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		G •	1.1.6	3			0		2		4			
			nal Ma								<u>50</u>			
				Exami							50			
Course											f the ele			
Object	ives:							-	ide the u	Inderstan	ding app	olication		
				ectronics										
Course		1.			owledge	about s	emicond	uctor pl	nysics fo	or intrins	ic and e	extrinsic		
Outcor	nes:		materia											
	 This course gives an overview of various semiconductor devices. Acquired knowledge about active and passive components, voltage and current 													
	3. Acquired knowledge about active and passive components, voltage and current													
	sources.													
	4. Able to understand the working principles of electronic circuits e.g. Rectifiers													
	Filters, Regulated circuits, etc.													
	Mapping of course outcomes with program outcomes													
	PO1	PO2 PO3 PO4 PO5 PO6 PO7 PO8 PO9 PO10 PO11 PO12												
CO1	1													
CO2			$\sqrt{1-\sqrt{1-1}}$											
<u>CO3</u>			,	V		,								
CO4				N										
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resistar	ces, their	r various	s types fi	xed and	variable	capacito	ors, their	various t	types and	l importa	int specif	ications		
and col	or codes.													
Voltag	e and cu	rrent so	ources –	concept	of const	tant volta	ages and	constant	current	sources,	symbol a	and		
graphic	al repres	entation	, charac	teristics	of ideal	and prac	tical sour	rces.						
0 1				Uni		•						8 hrs.		
Introd	uction:	Classif	ication			to condu	ictors, s	emi-con	ductors.	and ins	ulators,			
										conductor				
structur		mamam		con sem	1-0011000	ctors, int	inisic an	u exum	sie seine	onducto				
Unit-III 14 hrs.														
Semiconductor Diodes: PN junction, basic principles of operation and VI characteristics of PN junction														
											wave and			
					ies indu	ctor filte	r, Zener	diode a	and its a	pplicatio	ns, as a	voltage		
regulate	or, light e	emitting	diode (I	LED).										



	<u>Unit-IV</u>		14 hrs.							
Transistors: Introduction of BJT	, working of PNP and NPN transis	tor, input a	nd output characteristics,							
transistor configurations, biasing of	of a transistor, amplifying action of	a transistor	, comparison of different							
configurations, MOSFET, their ch	aracteristics and applications.									
RECOMMENDED BOOKS										
Title	Author	Publisher								
1. Basic Electronics and Linear	N. N. Bhargava and Kulshreshta	McGraw	Hill							
Circuits										
2. Electronics Devices and	Miliman and Halkias	McGraw	Hill							
Circuits										

			Fundamentals of Electronics Engineering Lab For reinforce learning through hands-on experience by examining the electrical												
Course	e	To rein	nforce 1	learning	throug	h hands	s-on exp	perience	by ex	amining	the el	ectrical			
Object	tives:	characte	eristics of	of variou	is semic	onducto	r device	s, such	as diode	es, BJTs	and FE	Ts. To			
		provide	the stude	ent with	the capa	bility to :	measure	and reco	rd the ex	perimen	tal data,	analyze			
		the resu	lts of var	rious ser	niconduc	ctor devi	ces.								
Course	e	1.	To unde	erstand t	he functi	ioning of	f various	s electron	nic instru	uments li	ke CRO	, signal			
Outco	mes:		generato	or and m	ulti-mete	er.									
										rify their					
		3.	,												
			Mapping of course outcomes with program outcomes												
	PO1	PO2													
CO1															
CO2															
CO3															
		2. 3. 4. 5. 6. 7. 8. 9.	To meas To calcu To obse To obse To obse To obse To verif To cons its ripple	sure valu ulate the rve the f rve the f rve the V rve the V Sy the NF truct hal e factor. truct full e factor.	value of ront pan ront pan /I charac /I charac PN and P f-wave r l-wave re	various el of CR el of sign cteristics cteristics PNP trans ectifier, v	passive O. nal gener of semio of Zene sistors. wave sha	compone rator. conducto r diode i ape of the upe of the	ents usin or diode i n reverse e electric e electric	cal signa al signal	eter. d bias. l and cale				



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		Session	al Mark	-			-		-			50
					ation Ma	rks						50
Course	9	The obj	ective of	the cou	rse is to e	expose to	o the stud	lents to t	he evolu	tion of n	nicropro	cessors,
Object	ives:	the arch	itecture	and inst	ruction se	et of typ	ical 8-bit	t microp	rocessor	8085. It	also dea	als with
					ogrammir			_				
			• •	0	nming, a	0			-			
Course	<u> </u>				ution of o		•					
Outcor					ture of th	-		onroces	sor and 8	051 mici	ocontro	llers for
			•					oproces	sor and o		0001110	
	its various applications.3. Apply the programming techniques in designing simple assembly language programs											
	for solving simple problems by using instruction sets of microprocessor and											
	microcontroller.											
	4. Use the addressing modes and timing diagram for executing program efficiently. Mapping of course outcomes with program outcomes											
	DO1											
C01	PO1	PO2 √	PO3	PO4 √	PO5 √	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1 CO2		V		$\sqrt{\frac{1}{\sqrt{2}}}$	V							
CO2		V	V	V								
CO4		Ń	Ń	,								
					Unit-I			1	1			14 hrs.
		Typical						m and f	unctions	of its	various	blocks,
		r, its evol										
		o 8-bit M										
block d	lagram	of 8085,	tunction	s of each		t 8085 ai	chitectui	re, pin de	etails of a	8085 and	related	•
Momo	niog ond	I I/O Inte	mfaaina	Mama	Unit-II	zation	anaant	of mome	m	ing nort	itioning	8hrs.
		address										
		ROM, E				mapped			y mappe	u 1/0. D		leept of
					Unit-II	[12hrs.
Progra	mming	using 80	085 Mici	roproce			progran	nming m	odel, bri	ief ideas	of mach	
assemb	ly langu	ages, ma	chines a	nd mnen	nonic coc	les, basi	c idea of	instructi	on forma	at and add	dressing	modes,
basic concept of instruction set for data transfer group, arithmetic group, logic group, stack, subroutine, I/O												
and ma	chine co	ontrol gro	oup, writi	ing assei	mbly lang	guage pr	ograms.					



Unit-IV		14hrs.								
Architecture of 8051 Microcontroller: Architec	ture of 8051, I/O ports in	8051, basic concept of memory								
in 8051, basic idea of addressing Modes in 8051, basic idea of instructions in 8051.										
RECOMMENDED BOOKS										
Title	Author	Publisher								
1. Microprocessor Architecture- Programming & Applications with 8085/8080A	Ramesh S Gaonkar	5th Edition, Penram International Publishing								
2. Introduction of Microprocessors & Microcomputers	Ram B	4th Edition, Dhanpat Rai Publisher (P) Ltd.								
3. The 8051 Microcontroller	Kenneth J. Ayala	3rd Edition, Cengage Learning, 2004								

Course	0	Thach	The objective of this lab is to familiarize the students with architecture, pin configuration											
Cours														
Object	tives:	and programming of 8085 microprocessor kit. Further students will perform various												
		arithmetic operations on microprocessor kit. The basic concept of 8051 microcontroller												
0		has also been included in this lab.												
Cours		1. Understand the architecture and pin configuration of 8085 microprocessor and 8051 microcontrollers.												
Outco	mes:	2					0005:							
		2.	.	ntroller		ams on a	8085 mic	roproces	ssor kit a	ina 8051				
						aamaa	with nrow	mom ou	toomog					
				_			vith prog	-	1					
~~ 1	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1		N	V	N		,								
CO2														
		1. 2. 3. 4. 5. 6. 7. 8. 9. 10. 11. 12.	 To get familiarize with Pin Configuration of 8085 Microprocessor. Familiarization of different keys of 8085 microprocessor kit. To familiarize with entering various steps of a program in 8085 kit. Steps to enter, modify data/program and to execute a programme on 8085 kit. Writing and execution of program for addition of two 8 bit numbers. Writing and execution of program for subtraction of two 8 bit numbers. Writing and execution of program for multiplication of two 8 bit numbers. Writing and execution of program for division of two 8 bit numbers. To study the architecture of 8051 Microcontroller. To get familiarize with Pin Configuration of 8051 Microcontroller. 											

Course Objecti				Comput								
		-		L	ci i logi		<u>g & Арр</u> Т	1			Credits	
				3			0	2	2		4	
		Sessio	onal Mai	rks			-				50	
		End S	Semester	Exami	nation M	larks					50	
Objecti		This course is useful as it develops the ability to write computer programs, to compare										
	ives:	values and perform alternative operations based upon the results of the comparison. Students will also learn how to identify the proper structure of loops, the use of arrays,										
						identify	the prop	er structi	are of loo	ops, the u	ise of arr	cays,
<u></u>			e use of			. 11		C 11	6			
Course										ter scien	ce.	
Outcon	ne:		o underst							imming ite, com	nilo and	dobuc
			ograms i			lesign p	rograms	1.e., 110	w to wi	ne, com	prie and	uebug
		4. To										
							vith prog	gram ou	tcomes			
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1												
CO2												
CO3												
CO4				√ Un								12 hrs
working	iction to g, associa ries: Me	ated per	ipherals.	ROM, Se	condary	-				-	-	
***	17		6.0	Uni		1	1		G (•	12hrs
	1g Know g knowle											
	ORD, MS											
system.								-				-
Interne	et: Evolu	tion of l	Internet a			ns and S	ervices.					
	~ • •			Uni								12hrs
	n Solvin m Dlann										nd mean	dooodo
	m Plann ive exam				n, Progra	am desig	, in tools,	Algorith	ms, now	charts a	na pseud	locode
	ew of C				to C la	nguage	Structure	of a C	program	Concer	ts of co	mniling
	king, ID											
	ental dat			·		0.				-	•	
				<u>Uni</u>	<u>t-IV</u>							12hrs
	ors and										expression	ons and
	aluation,	• •			.	•			-			1
	ing with itional br							n, Progra	am Stru	ctures: (Condition	ial and
	and Poin							String I	Rules to	initialize	arrays	
•	s, declara		•				•	0.			unuyb,	



Г

RECOMMENDED BOOKS											
Title Author Publisher											
1. Fundamentals of Computers	V. Rajaraman	Prentice Hall of India									
2. Object Oriented Programming with C++	E. Balagurusamy	Tata McGraw Hill									
3. Let us C	Y. P. Kanetkar	BPB Publications									



	Computer Programming & Applications Lab												
Cours Object			ective of									e. They	
Cours		1.		rm various programs in C language to learn its practical applications. Inderstand basics of C language.									
Outco		2.	Implem					al applica	ations.				
		3.											
	3. Perform arithmetic operations on matrices. Mapping of course outcomes with program outcomes												
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1													
CO2													
CO3			\checkmark										
		1. 2. 3. 4. 5. 6. 7. 8. 9.	^P Experi Program Program Program Program Program Program Program Program Program	n to find n to add : n to calcu n to find n to print n to find n to find n to find n to find	first 10 r ulate sun greatest Fibonac factorial whether sum of t subtract	hatural nu n of 5 su in 3 nun cci series of a nur given no wo matr ion of tw	umbers bjects. abers. a up to 10 nber. o is a prin ices. vo matric)0. me no or es using	arrays.	s.			



						EC-227	,					
				Service	and Ma	intenan	ce of Co	mputers				
				L			Т	I)		Credits	
				2			0	4	l I		4	
		Sessio	nal Mai	rks							50	
		End S	emester	· Examir	nation M	larks					50	
Course	•	This c	course is	s useful	as it eq	uips pa	rticipants	s with b	asic kno	owledge	about p	ersonal
Object	ives:	compu	iters. Pa	articipant	s will al	so learn	about PC	C hardwa	re and s	oftware,	mainten	ance of
-		PC sys	stems an	d trouble	eshooting	g of com	mon prol	blems.				
Course		1. (Gain basi	ic knowl	edge abo	ut perso	nal comp	outers				
Outcor	nes:	2. L	learn abo	out PC h	ardware	and soft	ware					
		3. U	Jndersta	nding the	e archite	cture of o	computer	svstems	3			
 Understanding the architecture of computer systems Understanding the functions of peripheral devices in microcomputer systems 												
 5. Troubleshooting of common problems in personal computer systems 												
networking												
Mapping of course outcomes with program outcomes												
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10 PO11 PO12		
CO1	$\frac{101}{}$	102	$\frac{103}{}$	104	105	100	10/	100	109	1010	1011	1012
CO1	$\sqrt{1}$	V	√ √									
CO2	$\sqrt{1}$	V	v									
CO4	$\sqrt{1}$	V		V								
CO4		V										
005	•	,		Un								12 hrs.
Renair	, Servic	ing an	d Maiı			nte	Repair,	Servicin	σ and	Mainten		
	ction to s											
	nance pol											
techniq							ver supp					
-	ers and it			unig gio	unung	and por	ver supp	ny icqu	liements		instactat	10113 01
-	mental ti			nrocedu	re & ins	tallatin	n of OS+	Fault loc	eation F	ault findi	ng aids	Service
	ls - Test											
System		and m	Jusuing	motrum	ents, sp		/15 101 U	oublesh	Jouing, 1	mstanati	on or of	orating
System				Uni	t-II							12 Hrs.
Hardw	are and	Softwar	re Fault			no techr	niques T	Different	trouble	shooting		
methods, Functional area approach, Split half method, Divergent, convergent and feedback path circuits, analysis measured techniques.												
	eshootin			omnone	nts and	Perinhe	rals: Mo	other Bos	rd FDD		CDRON	//DBD
	s, Moder				1165 allu	i ci ipite	1 413. 1110		uu, I DD	, 1100, (
. muere	, 11000011	10, 1 1 10111										



Teaching Scheme for Integrated Certificate and Diploma (DEC-CSME) Programme

<u>Unit-III</u>	12 Hrs.
Maintenance and repair of peripherals: Specification, Maintenance and Repair	of CVTs and UPS, Sight

preparation and design of computer rooms. Testing specifications and installation of computer systems and peripherals.

CD Drive and its troubleshooting: Working Principle and its types. CD-ROM drive: - CD drives mechanism installation of CD drive. Drive technologies: - CD-ROM: SCSI\CD-R, CD-RW, DVD-ROM. Working Principals, IDE controller card.

<u>Unit-IV</u>	12 Hrs.

Modem: Fault Finding, Repairing, modem Circuit Diagram, Repairing MODEM. **Network Components:** Introduction of Network Cable like UTP, STP, Fiber Optics, Hub, Unmanageable Switch, Manageable Switch, Router, Wi-Fi, Access Point, PCI Wireless Card, USB Wireless Device, Print Server, USB Network Sharer, Backup Device, Server Hardware etc. Installation and troubleshooting of Routers.

RECOMMENDED BOOKS											
Title	Author	Publisher									
1.Electronic test equipment	RS Khandpur	McGraw Hill									
2. Maintenance and Troubleshooting Guide	SK Chauhan	SK Kataria and Sons, New Delhi									
3. Trouble shooting computer system	Robert C Benner										

			Se	rvice an	d Maint	tenance	of Comp	outers L	ab				
Course Object		The objective of this lab is to familiarize the students with the basic hardware part of computer. It includes assembly as well as troubleshooting of various components in											
0		CPU. Students will also learn the basics of operating systems.1. Identify the various components connected to motherboard.											
Course Outcor		1.										1	
Outcor	nes:	2.					nts of cor	nputer s	uch as na	ard disk,	sound al	Id	
video cards, printer etc. Mapping of course outcomes with program outco													
	PO1	PO2											
CO1													
CO2													
		 Study and identification of components of desktop PC. Study and identification of components on motherboard and practic connecting components on motherboard. Assemble and disassemble the desktop PC. Understanding Specifications of RAM and its selection. To study various types of processors and their specifications. To study the setup and installation of operating system Windows 7 and Windows 8. To study the partition and formatting of Hard disk drive using disk manager. To study the troubleshooting of sound and video card in the system To study the troubleshooting device manager issues in Win 7. To study the troubleshooting of printer problems. Running the disk clean-up program (Manually/Automatically) and disk defragmen program To study and perform the troubleshooting of computer interfacing devices. To study software problems and repair them using various techniques. To understand the maintenance schedule for desktop PC. To understand the use of antivirus in the desktop PC. 									ws 8.		

						EC-228						
				r	NETW	1	HEORY				<u>a</u> 14	
						T		I			Credits	
		G •		3		1		()		4	
			al Mark								50	
					tion Ma						50	
Course					rovide t							
Object	ives:				help of f							
			•		c theorem			ns to pr	ovide ne	ecessary	backgro	und for
					us circuit							
Course												
Outcomes: 2. Analyze the circuit using Kirchhoff's law and Network simplification theorems.											ems.	
	3. Able to analyze resonant circuits and magnetically coupled circuits.											
		4.			ace trans					ts and us	e of test	signals.
					urse out							
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1		N		N								
CO2		N		N								
CO3		V				<u>√</u>						
CO4												
				Un								14 hrs.
		Voltage										
		e transfor										
		g nodal c								loop curi	rent metl	nod and
branch	current	method f	or solvir			ems, star	-delta co	nversior	l.			101
		~		Uni								12 hrs.
		orems: S										
		er transfe	er theore	m and T	ellegen's	theorem	n for the	solution	of netw	orks wit	h DC ex	citation
and AC	excitat	10n.		T T •								101
D		1.3.6	. 11	<u>Unit</u>		τ.	1		0	•		<u>12 hrs.</u>
		d Magno										
		ncept of s						upling c	oefficier	nt, magn	etically of	coupled
circuits	s, Simple	e series a	nd parall			onventio	n.					101
<u> </u>		C! 1	TT •/	Uni			1 1 1	1.6	1	1 • 7		<u>10 hrs.</u>
		Signals										
	: Introd	luction to	o low pas	s, nign p	bass, ban	a pass a	na band	enminat	ion filter	rs, protot	ype LC	and KC
filters.												



REC	OMMENDED BOOKS			
Title	Author	Publisher		
1. Fundamentals of Electric Circuits	Charles K. Alexander and Matthew N.O. Sadiku	Tata McGraw Hill		
2. Network Analysis	Van Valkenburg	Prentice Hall of India		
3. Networks and Systems	D. Roy Choudhary	New Age International		
4. Circuit and Networks: Analysis and Synthesis	A. Sudhakar and S. Palli	Tata McGraw Hill		

			Troub	leshoot		EC-229 Electron	ic Equi	pment-	п			
		L				T		<u>P</u>		Credits		
		0				0		4		2		
Course	e	The cou	irse prov	ides the	students	with nee	cessary k	nowledg	ge and co	ompetenc	y to diag	gnose
Object	ives:									enance of		
			ent and t									
Course		1.								like mob	ile teleph	iones,
Outco	mes:	C.R.O, function generator, power supplies, digital multi-meter.										
		2.	•		uiled fund	ctioning,	fault fin	iding and	l repair o	of UPS a	nd home	
				system.			• 41					
	1			-			vith prog	-		1	1	
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
<u>CO1</u>	N	N		N	N							
CO2		V		N	N							
		 Demo Demo Demo Demo Demo Demo Demo To st To m UPS sy To st To m 	onstratio onstratio onstratio onstratio tudy the leasure the stem trai udy the l	n and pr n and pr n and pr n and pr n and pr block di he AC/D ner. block dia he AC/D	actice of actice of actice of agram an C voltag ngram an C voltag	fault fin fault fin fault fin fault fin d worki e and wa d workin	ding and ding and ding and ding and ng princ aveform ng princi	l repair of l repair of l repair of l repair of l repair of iple of U at different ple of ho	of C.R.O of Function of Power of Digital PS systement point pome inve	e telephon on Gener supplies I multime em traine in Diffe rter syste in Diffe	rator. eter. r. rent sect em traine	r.

						EC-221							
					nentals of	of Electr		1	U.S.		<u>a</u> 114		
				L		_	<u>T</u>			Credits			
		~ •		3			0		2		4		
			nal Mar								50		
		End S	emester	Exami	nation N	Iarks					50		
Course	:	The c	The course intends to provide the basic concept and characteristics of the electronics										
Object	ives:	devices such as diode, BJT, FET, etc. Also aims to provide the understanding application											
			of different electronics devices and simple circuits.										
Course	:	1.			owledge	about s	emicond	luctor pl	nysics fo	or intrins	ic and e	extrinsi	
Outcon	nes:		materia										
		2.				erview o							
		3.	Acquire	ed know	ledge al	oout acti	ve and	passive	compone	ents, volt	age and	currer	
		_	sources								_		
		4.					g princip	ples of e	electroni	c circuits	s e.g. Re	ctifier	
					ed circui								
			1		1	tcomes v							
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO1	
CO1		V	,	N									
CO2		V	N	,									
CO3		V	,	N									
CO4		\checkmark											
				Un								10 hr	
Active	and Pa	ssive C	ompone	nts: Int	roduction	n to act	ive and	passive	compon	ents; fix	ed and	variabl	
resistan	ces, their	r various	s types fi	xed and	variable	capacito	ors, their	various t	ypes and	l importa	nt specif	ication	
and col	or codes.												
Voltage	e and cu	rrent so	ources –	concept	of const	ant volta	iges and	constant	current	sources,	symbol a	and	
_	al repres			-			-			,	5		
8F			,			F						10.1	
T / T		<u> </u>	· .	Uni		1			1 .	1 .	1 .	10 hrs	
Introdu										and ins		Atom	
structur	e of Ger	manium	and Sili	con sem	i-conduc	ctors; int	rinsic an	d extrins	sic semic	conductor	rs.		
				Unit	t-III							12 hr	
~ •	nductor	Diodes	: PN im			nciples o	f operati	ion and	VI chara	acteristics	s of PN i		
Semico			0		-	-	.						
	tatic and								., //				
diode, s		•		lter. ser	ies induc	ctor filte			and its a	pplicatio	ns. as a	voltag	
diode, s rectifier		nunt cap	acitor fi		ies indu	ctor filte			and its a	pplicatio	ns, as a	voltag	



	<u>Unit-IV</u>	12 hrs.								
Transistors: Introduction of BJT	, working of PNP and NPN transis	tor, input and output characteristics,								
transistor configurations, biasing	of a transistor, amplifying action of	a transistor, comparison of different								
configurations, MOSFET, their ch	aracteristics and applications.									
RECOMMENDED BOOKS										
Title	Author	Publisher								
1. Basic Electronics and Linear	N. N. Bhargava and Kulshreshta	McGraw Hill								
Circuits										
2. Electronics Devices and	Miliman and Halkias	McGraw Hill								
Circuits										

			Fu	ndamen	tals of E	lectroni	cs Engir	neering	Lab					
Course	e	To rein	nforce	learning	throug	h hands	on exp	perience	by ex	amining	the el	ectrical		
Object	tives:	characte	eristics of	of variou	is semic	onducto	r device	s, such	as diode	es, BJTs	and FE	Ts. To		
		provide	the stud	ent with	the capa	bility to	measure	and reco	rd the ex	perimen	tal data,	analyze		
					niconduc									
Course	e	1.	To unde	erstand t	he functi	ioning of	f various	electron	nic instru	uments li	ke CRO	, signal		
Outco	mes:				ulti-mete									
		 To understand the characteristics of diode and BJT and verify their responses. To construct various electronic circuits on the bread board and analyses their output. 												
		3.	To cons	truct var	ious elec	tronic ci	rcuits on	the brea	d board a	and analy	ses their	output.		
			Mappi	ng of co	urse out	tcomes v	vith prog	gram ou	tcomes					
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1														
CO2														
CO3														
		2. 3. 4. 5. 6. 7. 8. 9.	To meas To calcu To obse To obse To obse To obse To verif To cons its rippl To cons its rippl	sure valuate the rive the f rive the f rive the V rive the V rive the V fy the NH truct hall e factor. truct full e factor.	value of front pan front pan V-I chara V-I chara PN and P f-wave r l-wave re	various el of CR el of sign acteristic conteristic PNP trans ectifier, v	passive of O. nal gener s of semi s of Zeno sistors. wave sha	compone rator. iconduct er diode ape of th upe of the	ents usin or diode in revers e electric e electric	cal signal cal signal	eter. rd bias. l and cale			



						EC-311						
				Ele L	ectromag		ield The T	ory I)		Credits	
				<u>L</u> 3			0				3	
		Sessio	nal Ma	-			0		,		50	
				· Examir	nation M	larks					50	
Course Object Course Outcou	ives:	electro potent to give	omagneti ial, flux, e the brid gation. B Under	e of this ic field a charge o ef descrij gasic idea stand fur Maxwel	and wave density, f ption about about tr adamenta	e propag field inte out Maxy cansmiss al terms	ation. V nsity and well's eq ion lines related to	arious p l energy uation fo is also c electron	arameter density or electro overed. magnetic	rs related is covere omagneti	d to a fie ed. Next : c field a:	eld like focus is
		3.		stand fur								
		4.	<u>U</u> nder	stand fur	ndamenta	als of wa	ve transi	nission i	n differe	nt media	ı.	
			Mappi	ng of co	urse out	comes v	vith prog	gram ou	tcomes			
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	101		100	√	$\sqrt{\frac{100}{}}$	100	10/	100	10,	1010	1011	1012
CO2												
CO3												
CO4												
				Uni	it-I							12 hrs.
vectors differen Static potenti	uction to , different nt co-ordi Electric al, chargo or, diverg	it co-ord inate sys Field : F e densit	linate systems, lin Stems, lin Force ber y, gradie	stems, ca ne integra tween po	artesian, al, surfac	cylindric ce integra ges, cou	cal and sj al and vo lombs la	pherical olume int w, elect	systems, egral. ric field	transfor intensity	mation b	etween c scalar
				Uni	t-II							12hrs.
magnet potenti Time V Maxwe Maxwe Wave	Magneti tic flux of al. Varying ell equati ell equation Transmis quation f ves in spa	density, Fields: on fron on for fr ssion: M	Ampere Faraday n Farada ee space faxwell e nducting	es law, 1 's law, r ny's law <u>Unit</u> equations medium	Maxwell noving c , displac :- III s, plane v , sinusoi	equation conducto cement of vaves, El dal time	ons, ener r in a ch current, M wave i variation	anging manging man manging manging man	ity in in magnetic 's equat	nductor, e field, S ion from s medium fficient,	magnete toke's tl n amper n, unifor wave eq	b static neorem, es law, 12 hrs. m plane juations



	<u>Unit-IV</u>		12 hrs.
Transmission Lines: Introduction	n, basic princip	es, termination lines with lo	oad, voltage and current
distribution, characteristic impedar	ice, propagation	constant attenuation constant,	phase constant, reflection
coefficient, VSWR, open and short	t circuited transm	ission lines and their impedar	ces, stub matching, types
of high frequency transmission line	es.		
	RECOMME	NDED BOOKS	
Title	Author	Publishe	r
1 Electromagnetic Engineering	Howt	McGrou	11:11

1. Electromagnetic Engineering	Hayt	McGraw Hill
2. Field theory	Gangadhar	Khanna
3. Electromagnetic	Karus	McGraw Hill
4. Electromagnetic Fields and	K.D. Prasad	Satya Prakashan
Waves		



						EC-312							
				L	inear IC	C's & Ap	plicatio	ns					
				L			Т]	P		Credits		
				3			1	4	4		6		
			nal Maı								50		
		End S	emester	Examiı	nation M	larks					50		
Course	•	Lear	Learning op-amp and its characteristics. Ability to design different configurations of										
Object	ives:	op-amp circuits and design linear and non-linear op-amp applications, active filters,											
		and detectors. Also analyses of 555 timer IC.											
Course		1.	Acqui	red knov	vledge of	f fundam	ental cha	aracteris	tics of or	o-amps.			
Outcor	nes:	2.	To ana	alyze op-	amps wi	th and w	vithout us	sing feed	lback and	d determi	ine		
				• •	•		he perfo	•					
		3.		U			-		-	•			
			 To learn the linear applications of operational amplifiers. Understand various applications using 555 timers. 										
							vith prog						
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1													
CO2													
CO3													
CO4													
				Un								14 hrs.	
circuits parame	and the ters of O	eir type p-Amp,	s, IC pa equivale	ackage tent circuit	types, 74 it of an Io	41 pin deal and	configur practica	ation, cl l Op-Am	haracteri	stics and s voltage	l perfor transfer	mance curve.	
	al Op-A al drift, V	- I		0	· •		· •					0	
				<u>Uni</u>								14 hrs.	
amplifi level de	t ional an er, voltag etector, so	ge follov quare wa	ver, com ave gene	parator, erator, vo	adder, su	ubtractor current c	r, integra	tor, diffe , current	erentiato to volta	r, zero cr ge conve	ossing d rter.	etector	
0	ve feedba e- voltage	-			•	•		feedback	configu	rations, V	Voltage-	current	
				Uni	-							10 hrs	
	e regulat regulato										e termina	ls	



<u>Unit-IV</u>		10 hrs.
Specialized IC applications: 555 timer IC and	its pin configuration, Block	diagram, application of 555 as
Monostable and Astable Multivibrator.		
RECOM	MENDED BOOKS	
Title	Author	Publisher
1. Op Amps & Linear Integrated circuits	Ramakant Gayakwad	Pearson Education
2. Operational Amplifiers and linear integrated	R.F. Coughlin & F.F.	Prentice Hall
circuits	Driscol	
3. Design with Operational Amplifiers and Analog Integrated Circuits	S. Franco	Tata Mc-Graw Hill

				Line	ar IC's a	and App	lication	s Lab					
Course Object		This lab ai		•		• •		-	-			s of op-	
Course Outcon	9	 amp. To understand the various linear and non-linear applications of op-amp. Analyse and deign of basic op-amp circuits, particularly various linear and non-linear circuits, active filters, signal generators, and data converters. Analyse and deign op-amp oscillators, single chip oscillators and frequency generators. Examine the operation of a PLL and verification of hardware results using SPICE. Mapping of course outcomes with program outcomes 											
					-		vith prog	-	tcomes				
	PO1	PO2 I	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1													
CO2 CO3					V								
		 To me Appli Appli To us To De To De Appli Appli Appli Appli Desig at a l 	udy co easure ication	omparato the perf of Op a of Op-a Op-Amp different: of Op A of Op A es regula urrent of cansistor a delay c a +5V ur +5V regu	Formance mp as In imp as N as sumr iator and imp as L Amp as S Amp as Z tors with f 1.5 Ar gain and ircuit us megulate ulated po ulated po	e parame iverting a fon Inven ning, sca l Integrat ow-pass quare wa zero Cros n an erro np. Use l the mat ing555. ed power ower sup ower sup	r amplifi a 741 (ximum p supply. ply. ply.	r. olifier. overaging Op-Am h-pass fi rator. ector and ier to pro Dp-Amp ower dis	g amplifi p. lter. d windov ovide an and spe ssipation	w detector output vo cify the of the tra	oltage of Zener v		



						EC-313						
					Digital	Commu	nication					
				L			Т	I			Credits	
				3			1	2	2		5	
			nal Mai								50	
		End S	emester	Exami	nation M	larks					50	
Course	9	The course aims at studying the concepts of digital communication with the introduction										
Object	ives:	to var	rious cor	nponents	s of digi	tal com	nunicatio	on syster	ns. The	students	will und	lerstand
				-	-		ues invol	-				
		-				-	signals			0 0		
Course	<u>د</u>			<u>^</u>	•		ntal conc					stems
Outco							gnal and					
							gnals fo					acces
		teo	chniques	6		-	-		-	-	-	
		4. A		0		0	odulation					
	n	1					vith prog				T	
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO1 2
CO1		N	N	V								
<u>CO2</u>		N	N	V	V							
CO3			N									
CO4			N	٦ ا	4 T							10 1
			~	Un								12 hrs
	nts of I											
represe	ntation o	r Analog	g signais	, Advant	ages and	Disadva	antages c	of Digita	Comm	unication	i system,	,
-	pt of am		Inform	ation a	nd entro	py: Rat	e of info	rmation,	Shanno	on Fano	Source (Coding
Huffma	an source	coding										
				<u>Uni</u>	t-II							14 hrs
Sampli	ing Theo	rem: Sa	mpling,	Natural	sampling	g, flat toj	o samplir	ng, Samp	oling Rat	e, Aliasi	ng	
Pulse (Code Mo	dulatio	n: Block	diagram	of PCM	l system	, Quantiz	ation, D	elta Moo	lulation,	continuo	ously
variabl	e Slope E	Delta Mo	dulator	(CVSDN	(I) or Ada	aptive D	elta Mod	ulation.				
				Uni	t-III							12 hrs
	oding: L				rties. NF	RZ & RZ	types, s	ignaling	format f	or unipo	lar, Pola	r,
bipolar	(AMI) a	nd Mano	chester c	oding.								
Multip	lexing T	echniqu	es: Fund	lamental	s of time	and free	auency d	ivision r	nultiplex	king.		
							queries a	1,1010111	··· · I. ··	\mathcal{O}		



<u>Unit-IV</u>			10 hrs.									
Digital Carrier Modulation Techniques : In ASK Modulator, Frequency Shift Keying (F		hift Keying (A	ASK), ASK Spectrum,									
Digital Carrier Demodulation Techniques coherent FSK Detector, Coherent FSK Detector		, Non-cohere	nt ASK Detector, Non-									
RECOMMENDED BOOKS												
Title	Author	Publis	her									
1 Principles of Communication Systems	Taub and Schilling	Tata McGra	aw-Hill Education									
2. Introduction to Communication Systems	Gary M. Miller	6 th editi	on, Prentice-Hall, 1999									
3. Modern Electronic Communication	D. Roy Choudhary	New Age In	nternational									
4. Modulation and Coding Techniques in	Evgenii	WILEY, 20)11.									
Wireless Communications	Krouk, Sergei											
	Semenov											
5. Digital Communication	E.A. Lee and D.G.	Kluwer	Academic									
	Messerschmitt	Publish	ers,1994									

			Digital Communication Lab s lab aims to understand the building blocks of digital communication system.												
Course		This la	b aims to	unders	tand the	building	blocks	of digit	al comm	unication	system.				
Object	ives:														
Course										commun					
Outcor	nes:	2. Unde								tal comm	unication.				
<u> </u>			Марр	ing of c	ourse ou	itcomes	-	rogram	outcom	es					
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12			
CO1				√											
CO2															
		List of	st of Experiments:												
		1.	 t of Experiments: Study of Sampling and reconstruction techniques. 												
		2.	Study of	of Pulse	code mo	dulation	and der	modulat	ion.						
		3.	Study of	of Delta	modulati	ion and o	demodu	lation.							
		4.	Study of	of differe	ent data f	formats/	line cod	es.							
		5.	Study of	of data co	oding tee	chniques	5.								
		6.	Study of	of ASK 1	nodulati	on and c	lemodul	ation.							
		7.	Study of	of FSK n	nodulatio	on and d	emodul	ation.							
		8.	Study of	of PSK n	nodulatio	on and d	emodul	ation.							
		9.	Study of	of TDM	PCM rec	ceiver ar	nd transi	nitter.							
		10.	Study of	of Adapt	ive Delta	a modula	ation an	d demod	lulation						

					Elec		-314 worksh	op-II				
				L			Т	- F	Р		Cred	lits
				0			0		4		2	
Cours	se	To ma	ake stud	ents far	niliar wi	ith diffe	rent typ	es of fil	lters, tra	insistors a	nd electro	onic circuits
Objec	ctives	and de	esigning	various	s electro	nic circ	uit.					
Cours	se	1.	Able to	analyze	e output	of diffe	erent ele	ectronic	devices	and circu	iits.	
Outco	omes	2.	Design	and ana	alyze ap	plicatio	n of trar	sistor a	s an am	plifier and	d as a swit	ch.
		3.	Design	and ana	alyze var	rious ele	ectronic	circuits		•		
		•	Мар	ping of	Course	Outco	nes wit	h progr	am out	comes		
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	2	3	1	3	2	1	2	0	1	3
CO2	3	3	3	3	2	3	2	1	1	0	1	3
CO3	3	3	2	3	3	3	2	1	2	0	2	3
List o	f Exper	iments [.]				•						1

- 1. To observe the waveform for different types of clipper circuit.
- 2. To observe the waveform for different types of clamper circuit.
- 3. To observe and analyse the waveform for low pass filter.
- 4. To observe and analyse the waveform for high pass filter.
- 5. To observe and analyse the waveform for band pass filter.
- 6. To observe and analyse the waveform for band stop filter.
- 7. To observe and analyse V-I characteristics of BJT transistor in common collector configuration.
- 8. To observe and analyse V-I characteristics of JFET in common source configuration
- 9. To observe and analyse V-I characteristics of MOSFET.
- 10. Design and analyse the output of CE amplifier using voltage divider biasing.
- 11. Design and analyse the output of CC unity gain amplifier.
- 12. To study the switching action of transistor.
- 13. To study the effect of load on gain of voltage amplifier.
- 14. Design and fabrication of relaxation oscillator using UJT.
- 15. Design electronic circuit to generate the waveform using IC-555 timer/IC 741 op-amp.

						EC-3						
		-		Princ	iples of	Microv		gineeri	ng			
				L			Т		Р		Credit	S
				2			1		0		3	
			nal Mai								50	
		End S	emester	Exami	nation I	Marks					50	
Course Objectiv	ves		of Micro								to learn working, a	
Course		1. Acq	uire kno	wledge	about th	e micro	wave fr	equenci	es and th	ne wavegi	uides that	are used
Outcom	ie:	-	y them.	0				1		- 0		
			-	owledge	about i	solator	circulat	or com	ler mi	rowave	solid state	devices
			icrowav	-		solutol,	encula	.or, couj	<i></i> , min		sona state	
						perform	nance ar	nalvsis c	f Micro	wave Tub	es and Ci	renits
		5. 010			ourse ou							icuits.
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1												
CO2												
CO3												
				Ur	nit-I							12 hrs.
Introdu microwa Microw	ave and	RF radia	ation on	animals	and pla	nts, Mic	rowave	heating	effect.		length. E	ffects of
		-		Un	it-II	•						12hrs.
	ional tuł	bes, lum	ped elen	bes: D	irectiona microwa						ers. Proble vo cavity l	em with clystrons
				Uni	it-III							12 hrs.
Semicon Circuits,						_			dvantag	es of Mic	rowave Ir	ntegrated
				Un	it-IV							12 hrs.
Samias	ductor	Mionar	vove D!			o onnlin	otion a	volonol	offect		diode TR	

Semiconductor Microwave Diodes: Gunn diode application, avalanche effect, IMPATT diode, TRAPATT diode, characteristics and application of avalanche diode, principle of pin diodes & its application, Schottky barrier diode.

	RECOMMENDED BOOKS	
Title	Author	Publisher
1.Microwaves	K C Gupta	New Age International
2. Microwave and Radar Engg.	M Kulkarni	Umesh Publications, Delhi
3. Microwave Devices and Circuits	Liao S Y	Prentice Hall of India
4. Foundation of Microwave Engg.	R. E. Collin	McGraw-hill



						TP-30)1E							
					In	dustrial		5						
Course Objecti		mainte becom and to	nance, a e aware expose	of the p studen	ng in In ractical ts to the	ience ir ndustry applicat e 'real'	variou Trainiz ion of th working	us doma ng Centr neoretica g enviror	re's/ Co il concej nment a	rporate O pts studied nd get ad	ardware, offices so d in the cl cquainted	that they assrooms		
Course Outcon		 organization structure, business operations and administrative functions. 1. Generate a report based on the experiences and projects carried out with the ability to apply knowledge of Mathematics, Science, and Engineering Fundamentals. 2. Demonstrate competency in relevant engineering fields through problem identification, formulation, and solution. 3. Effectively implement skills in communication, in writing and using multimedia tools. 4. Develop the ability to work as an individual and in group with the capacity to be a leader or manager as well as an effective team member. 5. Master the professional and ethical responsibilities of an engineer. 												
				•				program		e e				
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1														
CO2						\checkmark								
CO3										,				
CO4											ļ			
CO5														

						EC-321						
				L	Indust	trial Ele	<u>ctronics</u> T	1	P		Credits	
				3			1		4		6	
		Sessio	nal Mai	rks		1		1			50	
		End S	emester	Exami	nation N	larks					50	
Course Object		power	semico	nductor	devices,	analyse	vide in-de s and de d their a	sign of	various j	power co	onverter	circuits
		areas.	power s	enneona	uetor de	vices un	a then a	ppileatic		mineren	ii uiid iii	dustriur
Course			quire ki	nowledg	e about	fundam	nental co	oncepts	and tecl	nniques	used in	power
Outcor	nes:	electro	onics.	-				-		-		-
		2 . Ana	lyses va	rious sin	gle phas	e and th	ree phase	e power	converte	r circuits	and und	lerstand
			pplicatio									
							oot powe					
				ty to unc	lerstand	the use	of power	convert	ers in co	ommercia	al and in	dustrial
		applic		ngofoo		taamaa	with prog	anom on	toomog			
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	101	102	$\frac{103}{}$	104	$\frac{105}{}$	100	107	100	107	1010	1011	1012
CO2		V.	V									
CO3						V						
CO4												
				Un	it-I	•		•	•		•	12 hrs.
Power	Devices	: Symbo	ols, speci	fications	s, and te	sting of	SCRs, D	DIACS, 7	RIACS,	, UJT, C	haracteri	istics of
the abo	ve device	es.										
							constru					
							ds-DC ga					
						nd forced	l turn off	method	s. thyrist	or protec	tion, Cir	cuit for
over vo	ltage and	l over cu	irrent pr							[
				<u>Uni</u>	<u>t-II</u>							12hrs.
Phase	Controll	ed Rect	ifiers: E	xplanati	on of the	e workin	g of sing	gle-phase	unconti	rolled ha	If wave a	and full
wave r	ectifier (resistive	e and in	ductive	loads) w	vith the	help of	wave for	rms, Exj	planation	of wor	king of
control	led rectif	fier usin	g SCR	(resistive	e and in	ductive	loads) w	ith help	of wave	e forms	and app	ropriate
mathen	natical ex	pression	n (no der	rivations): three-j	phase co	ntrolled	half wav	ve, full w	vave and	bridge r	ectifier,
Princip	le of dua	l conver	ters & th	neir appli	ications.							
				Uni	t-III							12 hrs.
Chopp	ers: Inti	oductio	n, types			step-up	and ste	p-down	chopper	s. volta	ge and	
	tated typ					• •		-				
Cycloc	onverter	s: Intro	duction a	and princ	iple of o	peration	of conve	erter, up	and dow	n Cyclo	converte	rs.
Inverte	ers: Princ	ciple of o	operation	n of basic	c inverte	rs circui	t, basic s	eries and	l parallel	commut	ated inv	erters.
		T					,					

Γ

	<u>Unit-IV</u>	12 hrs.									
Thyristor Applications: Advantages of electronic control of devices, basics of DC motor speed control, speed control of DC and small AC motors using thyristor technology, principal of operation and working of the following switching circuits using SCRs: Automatic battery charger, Voltage regulator, Time delay relay circuit, Emergency, light, Burglar alarm circuit, Light operated alarm, AC phase control circuit using TRIAC and its applications : Illumination control, Fan speed control, Temperature control.											
	RECOMMENDED BOOKS										
Title	Author	Publisher									
1. Industrial Electronics and Control	S K Bhattacharya and S Chatterji	Tata McGraw Hill									
2. Power electronics	P S Bimbhra	Khanna Publishers, New Delhi									
3. Power electronic	M Rama Murthi	New age									

				I	ndustria	l Electr	onics La	b						
Course Objecti		like SC	jective i CR, DIA semicor	C and T ductor	TRIAC a devices.	and to st	udy var	ious bas	sic pow	er contro	ol circui	t using		
Course		1.		halyse the V-I characteristics of various industrial electronic devices like SCR, IAC, TRIAC and UJT.										
Outcon	nes:	2					and and	full wo	vo contr	allad maa	tifion not	anation		
		2.	Analyse		er circui		vave allu	Tun-wa	ve contro	oneu rec	unier, lei	axation		
					urse out		vith prog	gram ou	tcomes					
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1														
CO2														
		 To cur To To To To To To and To and To and To and To To To To To 	 test and test and test and test and draw the draw the draw the draw the draw the draw the study the trigger the draw the o draw the 	d draw the draw the e difference e difference average he difference he SCR e difference speed of he difference speed of he difference speed of	the charact e charact nt wavef ent wavef ent wavef int wavef f a univer rent wave d find th	eristics of orms of 1 forms of forms of forms of forms of axation orms of sal moto veforms eir duty	of UJT an half-wav full wav full wave relaxation oscillator half wav or using S of volta cycle.	IAC and nd find i e contro ve mid-p e bridge on oscill r. e voltage SCR and ge com	d find it ts intrins lled rection oint con configur ator usin draw ne mutated	's latchi ic stando ifier and trolled r ration-co ag UJT a ller and f ecessary and cu	ng and off ratio. find its a ectifier a ntrolled a nd find i ind its av waveform rent con	average. and find rectifier ts time- verage. ns.		

			WIR	ELESS A		EC-322 OBILE		UNICA'	TION			
				L			T	1	P		Credits	
				3			0	(0		3	
		Sessio	onal Mar	:ks		•					50	
		End S	Semester	• Examir	nation M	Iarks					50	
Course			of the cou									
Object	ives:		various	• •	•					• •		
			ques use					nally, it i	introduce	es the CI	OMA an	d GSM
0			ques use									
Course		1.		stand the						•	vstem.	
Outcol	nes:	2.		stand the					-			
		3.		stand the						mmunica	ation.	
	1	1		ng of co			· · · ·	Ť	1	1	1	1
~~ 1	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
<u>CO1</u>		N	N	N								
CO2				N								
CO3				√ Uni	4 T							12hrs.
and sys	ining the stem capa e Radio I ation med	city. Propaga	ation: In	<u>Uni</u> troductio	t-II on to rad	io wave	propagat	tion, free	e space p	oropagati	on mode	12 hrs. el, basic
~ ~ ~	pler sprea						0, 11			U,	e	
				<u>Unit</u>								12 hrs.
	ation Te							nniques,	minimur	n shift k	eying, g	aussian
minimi	ım shift k	eying, s	spread sp			on techn	iques.					10 1
Advon	ced Tra	naniva	n Sahan	Unit		do divisi	on mult	inla ago	acc avet	oma CS		<u>12 hrs.</u>
	ction to f							-	-	ellis, Os	JWI, 15-2	95, and
				R		AENDE	D BOOI	KS				
Title					Aut				Publ			
I. Wire	eless com	municat	tions		T.S	Rappap	ort		Pear	son Edu	cation, 2	003.
2. Princ	ciples of I	Mobile	Commur	nication	Go	rdon L. S	Stuber		Spri 2002	nger Inte 1.	ernationa	l Ltd.,
3. Wire	eless Con	munica	itions		An	drea Gol	dsmith		Cam	bridge U s, 2007	Jniversit	у



						EC-323						
			Microj I		sor & N		<u>ntroller</u> T	· Applic	1		Credits	
			3				<u>1</u>		2		<u>5</u>	
		Session	al Mark				1	4			50	
			mester E		tion Ma	mlza					<u> </u>	
~												
Course Object			urse is a ntroller v		.		•		-		.	sor and
					-	- -		-		-		
Course			rstand the		•		• •		-			
Outcor	ne:		the prog			U	periphera	als with r	nicrocor	troller.		
		3. Desig	gn the cir									
								gram ou			1	1
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1												
CO2												
CO3			\checkmark									
				Un	it-I							12 hrs.
Line d	rivers:	lication p MC 1488 ata Convo	3, 1489, 8	<u>Uni</u> 250, 82 0AC 080	<u>t-II</u> 51(in de)0.	tail)						12hrs.
				Unit	t-III							12 hrs.
	cing of	Microcon f Microc			Externa		-		•			or, and 12 hrs.
	-• •					C	(1	0071		
T4 P	cing: 1	nterfacing	g of Micr	ocontro	ner Data	Conver	ters, Inte	rracing t	o enhano	רכטאיים איני	capabiliti	
	255 & T	imer chip er Ports:	s.									les
with 82 Microc	255 & T	imer chip	s.	R	ECOMN	MENDE	D BOOI	KS			•	les
with 82	255 & T	imer chip	s.		ECOMN	MENDE	D BOOI	KS Pu	ıblisher			
with 82 Microo Title 1. Micr program	controll	imer chip er Ports: sor Archi and applie	s. connecti	R Auth	ECOMN		D BOOI	KS Pu	ıblisher	stern ltd		
with 82 Microo Title 1. Micr program with 80	controll coproces nming, 980/808:	imer chip er Ports: sor Archi and applie	s. connecti tecture, cation	RI Auth Ram	ECOMN Ior		D BOOI	KS Pu	iblisher Villey Ea		New De	

	Microprocessor & Microcontroller Applications Lab Course This lab aims to design various applications based on microprocessor and microcontroller.														
Course	e	This lat	o aims to	design	various a	pplicatio	ons based	d on mic	roproces	sor and	microcol	ntroller.			
Object										otor, AI					
U U			, relays <i>e</i>		U					ŕ					
Course	e	1.	Design	various r	eal time	applicat	ions base	ed on mi	croproce	ssor and	microco	ontroller			
Outco	mes:		such as	traffic li	ght, temj	perature	control e	etc.	-						
		2.	Control												
		3.								l microco	ontroller.				
			Mappi	ng of co	urse out	comes v	vith prog	gram ou	tcomes						
	PO1	PO2													
CO1															
CO2															
CO3															
		 Write micropp Write 	rocessors e a progr PI. e a progr 55 PPI. e a progr PI. e a progr e a progr e a progr e a progr t a progr t a progr t a progr	am to co am to 25 am for fi am to co am to co am to co am of Fl am to ge am to inf am to co gram to co	55 PPI. nding sq ntrol the ntrol the ntrol spe ashing L nerate a terface th ntrol a si ontrol th	uare of a tempera traffic li eed of D0 ED conn Ramp w he ADC. tepper m e speed	a number iture usir ight syste C motor i nected to aveform otor in d of DC m	r using long 8085/ em using 80 using 80 port 1 o using D irection, otor.	bok-up ta 8086 mio g 8085/80 85/8086 f the Mio AC with speed a	8085/80 able and croproces 086 micr micropro cro Contr micro c nd numb as, LED, s	verify. ssors and oprocess ocessors roller ontroller er of stej	sors and ps.			

						EC-324								
			1		ntenna		ropagati T		D		Credits			
				L 3			T	P 0			3			
		G					0		J	-				
	Sessional Marks End Semester Examination Marks										50			
										50				
Course		The objective of this course is to provide students with opportunities to learn different types												
Object	ives:	of antennae. This course provides an introduction to the basic antenna parameters, antenna arrays, aperture type antennas and wave propagation.												
		arrays,												
Course							ns related		-					
Outcor	nes:					fferent ty	pes of a	ntenna st	ructure f	for differ	ent			
		applications.3. Understand fundamentals of wave propagation.												
	D 04	D 00					vith prog	1	1	D 010	2011			
001	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
<u>CO1</u>	N	N	N	N		N								
<u>CO2</u>		N		N		N								
CO3				 Un		N						12 hrs.		
Wire H dipole a Antenn broadsi	Radiato and its r na Arra	rs: Volta adiation f ys: Intro- ys, end fin	ge and c resistance duction,	<u>Uni</u> urrent di e. monoj linear ur	istributic istributic pole radi niform a	on, hertz ator, loo rray isoti	dipole a p antenn ropic sou	a. Irces, pri	nciples o	of patterr	n multipl	ication,		
periour		1 a .		Uni	f_TTT							12 hrs		
-	• -	e Anten rements,	-	erture ar	ntennas,		•	orns, pyr	amidal 1	horn, ref				
				Uni	t-IV							12 hrs.		
propaga path lo	ation ov ss calcu	of Radio ver flat ar ulation, s ble freque	nd curved ky wave	l earth, o propag	optical a ation - i	nd radio	horizon tion, Stru	s, surfac	e waves f ionosp	and trop here, cri	osphere tical free	waves.		
				R	ECOMN	MENDE	D BOOI	KS						
Title				Aut	hor				ublisher					
1. Ante	ennas			Kra	aus			Ν	/Ic Grav	v Hill				
2. Ante	enna an	d Wave		K.	D. Parsa	ad		F	arkash	Publicat	ions			
								-						

Propagation



					Ъ.Л.	EC-325							
		1		r	Mie	croelectr	ronics T	I	>		Credits		
							0				<u>4</u>		
		3 0 2 Sessional Marks 0 1									50		
		End Semester Examination Marks									<u> </u>		
Cours	0												
Objectives:		The objective of the subject microelectronics is to discuss the design and fabrication proces of thick film, thin film, and hybrid IC's. It also aims at understanding each and every step											
		of thick film, thin film, and hybrid IC's. It also aims at understanding each and every step of fabrication from crystal growth to photolithography to manufacturing and to have a brief											
		knowledge of fabrication process flow and learning design and fabrication of BJT, diode,											
		FET, MOS <i>etc.</i>											
Cours	e			and the	physical	and ele	ctrical p	operties	of semi	conducto	or materi	als and	
Outco						nic circu		- F					
		2.				ng about		cts of the	e microel	lectronic	s industr	y, fron	
				•		sing, to p	• •					•	
		3.				he core p							
			Mappi	ing of co	urse ou	tcomes v	vith prog	gram ou	tcomes				
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO1 2	
CO1													
CO2													
CO3				\checkmark									
				Un	<u>it-I</u>							12 hrs	
Classif hybrid		of integ	rated ci			ation of	integrate	d circuit	s (IC's),	thick fi	lm, thin	tılm ð	
				Uni	4 TT								
				Un	<u>t-11</u>							12hrs	
Fabric	cation of	f Compo	nents: F			mponen	t design,	resistor,	capacito	ors and ir	ductors,		
	cation of	-	nents: F			omponen	t design,	resistor,	capacito	ors and ir	ductors,		
and fat Monol	orication	ı. echnique	es: Proce	abricatio ess on si	on and co	ystals, li	ne grow	th, refini	ing, subs	strate sli	cing, po	desigi lishing	
and fat Monol chemic	orication l ithic T e cal vapo	e chnique ur, depos	es: Proce	abricatio ess on si ermal ox	n and co licon cr	ystals, li	ne grow	th, refini	ing, subs	strate sli	cing, po	desigi lishing	
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Teaching Scheme for Integrated Certificate and Diploma (DEC-CSME) Programme

2. Inte	grated	Circuits		KR	K R Botkar TMH							
					Micro	electron	ics Lab					
Course Object		The objective of micro-electronics lab is to make the students familiar with PCB designing processes. It also includes layout designing using different software followed by etching and mounting the components on final prepared PCB.										
Course Outco		 Understand the working of PCB making CNC Mill-15. Design the layout of electronic circuit with the help of various software. Perform various steps involved in the design of PCB. Mapping of course outcomes with program outcomes 										
	DO1	DO1		-			_	-	1	DO10	DO11	DO12
CO1	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1 CO2							N					
CO2					V	V	V					
		1. To S 2. To S 3. To S 4. To S 5. To do 6. To po 7. To po machin 8. To po 9. To m	repare N erform D e.	observe observe observe d Implen egative o bip Coati	the Mac the Spri the Cop nent the I of a given ng Opera peration of nents on	h Mill 3 nt Layou per CAN PCB usin n artwort ation on on given	Softwar at Softwar A Softwar ng PCB k using V Copper (Printed	re. are. making l Vertical J Clad Boa	Machine. process (Camera. ing dip C	Coating	



	EC-327													
	Project													
			L			<u> </u>			Р		Cree	lits		
		0 0 4									2			
Course Object		0042Project Work aims at developing innovative skills in the students whereby they apply in totality the knowledge and skills gained through the course work in the solution of particular problem or by undertaking a project. In addition, the project work is intended to place students for project oriented practical training in actual work situation for the 												
Course Outcomes:1. Refine and complete the selected project making use of engineering knowledge which meets the expected outcome.2. Work with the modern tools required for the implementation of 3. Achieve the results within in the stipulated time.4. Acquire problem solving, system integration, project managem interpersonal and communication skills.									use of come. ntation of	the proje	ct.			
			Map	oing of o	course o	outcome	s with p	orogram	outcon	nes				
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1					\checkmark									
CO2														
CO3														
CO4				\checkmark								\checkmark		