

Sant Longowal Institute of Engineering and Technology
 (Deemed to be University, Under MoE, Govt. of India)
 Longowal, Sangrur, Punjab, India



SLIET Students' Chapter of Institute of Engineers (I) of Electronics and Communication Engineering Department is going to Organize a Webinar

Coordinator Faculty Advisor

on

Expert



“VLSI Physical Design for 5G and Beyond” (06-October-2021), Time 5:30 PM (IST)

Dr. Kundan Kumar
SLIET

Prof. (Dr.) J.S. Ubhi
SLIET

Samuel Jigme Harrison
(Senior Digital IC Design Engineer)
ST Microelectronics, Singapore

Link for the Registration:

<https://docs.google.com/forms/d/e/1FAIpQLSctgEk3cckS7WXfN-dn55A1dJipC6BU3ki7y5Zv9HBdaYoNOQ/viewform?vc=0&c=0&w=1&flr=0>

Eligibility for Registration:

B.Tech./M.Tech., Ph.D., Faculty and Industry person.

Date of Registration: 06/10/2021

• Only registered Participants can get E-Certificate with provision of 100 % attendance.
 • Maximum limit of Participants is 100 on the basis of first come first serve.

Link for Webinar:
<https://meet.google.com/mui-owjs-djb>

Six Weeks Online Summer Internship program

On

Recent Trends of RF Technology

(Simulation and Fabrication)

June 21- July 30, 2021



Organized By

Department of Electronics and Communication Engineering
Sant Longowal Institute of Engineering & Technology

(Deemed to be University Under MOE, Government of India)
Longowal -148106, Dist. Sangrur (Punjab)

Chief Patron

Patron

Chairman

Prof. Shailendra Jain

Prof. A. S. Arora

Prof. Surinder Singh

Director, SLIET

Dean (Academics)

HOD (ECE)

Coordinators

Dr. Kundan Kumar

Er. Alka Singla

Assistant Professor

Assistant Professor

Contact Persons:

Dr. Kundan Kumar, Assistant Professor (+91-9608576776)

Er. Alka Singla, Assistant Professor (+91-9988526425)

Department of Electronics and Communication Engineering

Sant Longowal Institute of Engineering and Technology, Longowal

Sangrur, Punjab, 148106, India

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ABOUT THE INSTITUTE



Sant Longowal Institute of Engineering & Technology (SLIET), Deemed to be University, has been established and funded by MoE, Government of India in 1991, to provide technical education in emerging areas of Engineering and Technology. The institute caters to the technical manpower requirements at various levels by adopting a concept of a modular system in imparting professional education with an emphasis on practical training in the industry. The study programs include various courses at certificate, diploma, B.E. M.Tech. and Ph.D. levels in different branches of engineering and technology. The institute has a sprawling area of 451 acres of land. Surrounded by lush green land, the campus of the institute extends a beautiful and well-developed area with many topographically featured picturesque landscape.

INSTITUTE VISION

SLIET shall strive to act as an international podium for the development and transfer of technical competence in academics through formal and non-formal education, entrepreneurship, and research to meet the changing need of society.

INSTITUTE MISSION

- Non formal, flexible, modular, multipoint entry programmes in engineering and technology and in the areas like rural development, educational planning, information and management sciences.
- Education and training in modern technology areas.
- Promotion of self-development among the students.
- Extension services to industry working population, passed-out students, social organizations and institutions of research and higher learning.
- Close interface with the industry to conduct research on the basis of manpower requirements leading integrated educational planning curriculum development and instructional material preparation in technology and interdisciplinary areas.

Online QIP-AICTE Sponsored Short Term Course on

Recent Trends of Microwave and Photonics Technology: 5 G and Beyond December 06 - December 11, 2021

ORGANIZED BY



Department of Electronics and Communication Engineering
Sant Longowal Institute of Engineering & Technology
(Deemed to be University Under MoE, Government of India)
Longowal -148106, Dist. Sangrur (Punjab)

SPONSORED BY



Quality Improvement Programme
All India Council for Technical Education (AICTE), New Delhi, India

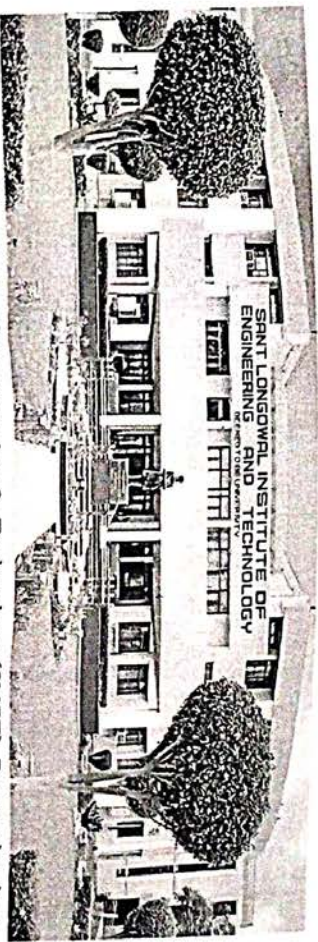
Chief Patron	Patron	Chairman
Prof. Shailendra Jain	Prof. J. S. Dhillon	Prof. Surinder Singh
Director, SLIET	Dean (Academics)	HOD (ECE)

Coordinators
Dr. Kundan Kumar Er. Vipul Singhal
Assistant Professor Assistant Professor

Contact Persons:

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Four Weeks Institutional/Industrial Training program On Advance Communication Technologies

July 01- July 28, 2022



Organized By

Department of Electronics and Communication Engineering
Sant Longowal Institute of Engineering & Technology
(Deemed to be University Under MoE, Government of India)
Longowal -148106, Dist. Sangrur (Punjab)

Coordinator(s)

Dr. Kundan Kumar Er. Vipul Singhal
Assistant Professor Assistant Professor

Contact Persons:

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संत लौंगोवाल अभियांत्रिकी एवं प्रौद्योगिकी संस्थान,
लौंगोवाल, जिला-संगरूर, पंजाब-148106
(भारत सरकार, मा.स.वि.म. के अधीन समविश्वविद्यालय)
Sant Longowal Institute of Engineering and Technology
Longowal, District-Sangrur, Punjab - 148106
Deemed-to-be-University under MHRD, Government of India)

संदर्भ सं/Ref. No. SLIET/Acad./PG/2020/4795-28

दिनांक/Date: 24-8-2020

OFFICE ORDER

The competent authority has approved for conduct of following Short Term Courses (STCs) ONLINE under Quality Improvement Program (QIP), AICTE as per details given against each :

Department	STC Title / Name	Duration	Organizers
EIE & ECE	STC on Computational Techniques in Image and Signal Processing	28.09.2020 to 03.10.2020	Patron : Prof. Shailendra Jain, Director Co-Patron : Prof. A.S. Arora Convener : Prof. Sanjay Marwaha : Prof. Surinder Singh Coordinators : Sunil Kumar Bansal : Er. Vivek Harshey
EIE	STC on e-Course on 'Energy Efficient Practices in Industries:	02.11.2020 to 07.11.2020	Patron in Chief : Prof. Shailendra Jain Director Patron : Prof. A.S. Arora, Dean (A) Chairman : Prof. Sanjay Marwaha Coordinator : Prof. Surita Maini : Dr. Charanjiv Gupta
EIE	STC on Recent Advances in Affordable and Clean Energy (RAACE-2020) – A Roadmap to SDG 7	14.09.2020 to 19.09.2020	Patron in Chief : Prof. Shailendra Jain Director Chairman : Prof. Sanjay Marwaha, Coordinator : Dr. Manpreet Manna : Dr. Charanjiv Gupta
EIE	Future for Industrial Automation	05.10.2020 to 10.10.2020	Coordinators : Dr. Pratibha Tyagi : Dr. Ashwani Aggarwal
ECE	Recent Trends in Electronics & Communication Engineering (RTECE-2020)	07.12.2020 to 11.12.2020	Patron in Chief : Prof. Shailendra Jain, Director Chairman : Dr. Surinder Singh Coordinator : Dr. Amar Pratap Singh, : Er. Pankaj Kumar Dass,

... Continued



अखिल भारतीय तकनीकी शिक्षा परिषद्

(भारत सरकार का एक मासिक निकाय)

(मानव संसाधन विकास विभाग, भारत सरकार)

केन्द्रीय मन्डला मार्ग, वसन्त कुंज, नई दिल्ली-110070

ALL INDIA COUNCIL FOR TECHNICAL EDUCATION

(A Statutory Body of the Govt. of India)

(Ministry of Human Resource Development, Govt. of India)

Nelson Mandela Marg, Vasant Kunj, New Delhi-110070

Phone 011-26131577 - 78, 80
011-29581000

Website www.aicte-india.org

Col. B. Venkat

Director (FDC)

E-mail: director.fdc@aicte-india.org

Mob. No. 8130255472

F.No.RIFD/QIP/Govt/Engg/2012-13

Dated 23.07.2020

To,

All QIP Centers

Sub:-For information of AICTE approved QIP Centers which receive grants for conducting STCs under QIP scheme of AICTE.

Sir,

This is in reference to grants released by AICTE under QIP against budget proposals for 2019-20 for conduct of STCs. It is observed that due to present circumstances of ongoing pandemic of COVID-19, most of Institutes are facing difficulties in organizing and conducting STCs. In this regard, it is to inform that all such institutes, which have already received grants for conducting STCs through prevailing contact mode, are **allowed to conduct STCs through online mode subject to following conditions:**

(i) The Institute will be allowed to adjust the grants received at following rates:-

(A) Rates for One Week (SIX DAYS) STC

a.	Honorarium for Coordinator	Rs. 2000.00
b.	Honorarium to experts	Rs. 78000.00
c.	Provision for payment to lab attendant engaged during lab practices	Rs. 3000.00
d.	miscellaneous charge	Rs. 10000.00
Total for each STCs		Rs 93000.00

(B) Rates for Two Week (2 x SIX DAYS) STC

a.	Honorarium for Coordinator	Rs. 2000.00
b.	Honorarium to experts	Rs. 1,70,000.00
c.	Provision for payment to lab attendant engaged during lab practices	Rs. 3000.00
d.	miscellaneous charge	Rs. 10000.00
Total for each STCs		Rs 1,86,000.00

Contd.



SHASTRI MEMBERSHIP DEVELOPMENT GRANT (SMDG) 2020-21

Personal Details:	
First Name of Applicant: Pankaj Kumar	Surname: Das
Title: Prof.	Gender: M
Institution: Sant Longowal Institute of Engineering & Technology (SLIET, Longowal)	
Applicant's Mailing Address: Er. Pankaj Kumar Das Assistant Professor Electronics & Communication Engg. Department, Sant Longowal Institute of Engg. & Tech., Longowal. Deemed University (Established by Govt. of India) District: Sangrur (Punjab) 148106, India.	Telephone: 01672-253331 Mobile (if preferred): +919478214936 Fax: 01672-253117 E-mail: Pankaj.jkd@gmail.com
Name of the Event: Two Days Online Workshop on "Nanoelectronics & VLSI"	
Date: February 11 th – 12 th , 2021	
Venue: ECE Deptt. SLIET Longowal	
Number of participants: 133	
Types of Participants (i.e. academia, students, communities, government, etc.): Students	
a. Please describe briefly the activities conducted as part of this grant (500 words). Two Days Online Workshop has been conducted for UG and PG Students on "Nanoelectronics & VLSI" during February 11 th -12 th , 2021. Following experts of Academia has been delivered the experts talk:	
<ol style="list-style-type: none">Dr. Praveen Prajapati Associate Professor, Department of ECE, A. D. Patel Institute of Technology, Gujarat Topic: Basics of Design of Nano AntennasDr. Pankaj Kumar Pal Assistant Professor Department of ECE, NIT Utrkhand Topic: Basics of Nanoelectronics and VLSI DesignEr. Pankaj Kumar Das Assistant Professor, Department of Electronics & Communication Engineering, SLIET Longowal Topic: Carbon Nanotube as Global VLSI InterconnectsEr. Swapnil Gaul, Founder and Director Numergion Technology (OPC) PVT LTD, PUNE Maharashtra-411014 Topic: Advanced simulation based virtual lab: MyTaraNG Cloud	



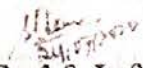
TEQIP III



Online Short Term Course on
Recent Trends in Wireless Communication (RTWC-2020)
20 – 24 July, 2020

CERTIFICATE OF APPRECIATION

This is to certify that *Dr. Dilip Kumar* of *SLIET, Longowal* has contributed as *Convener* in five days Online Short Term Course on “Recent Trends in Wireless Communication (RTWC-2020)” sponsored by TEQIP-III under Twinning activities with SLIET Longowal and organized by Department of Electronics Engineering, NIT Uttarakhand from 20 – 24 July, 2020.


Prof. S. L. Soni
Director, NIT Uttarakhand

S.A.


Certificate No. ATAL/2021/1430577944



ALL INDIA COUNCIL FOR TECHNICAL EDUCATION

Nelson Mandela Marg, Vasant Kunj, New Delhi - 110 070

AICTE Training And Learning (ATAL) Academy



Certificate of Appreciation

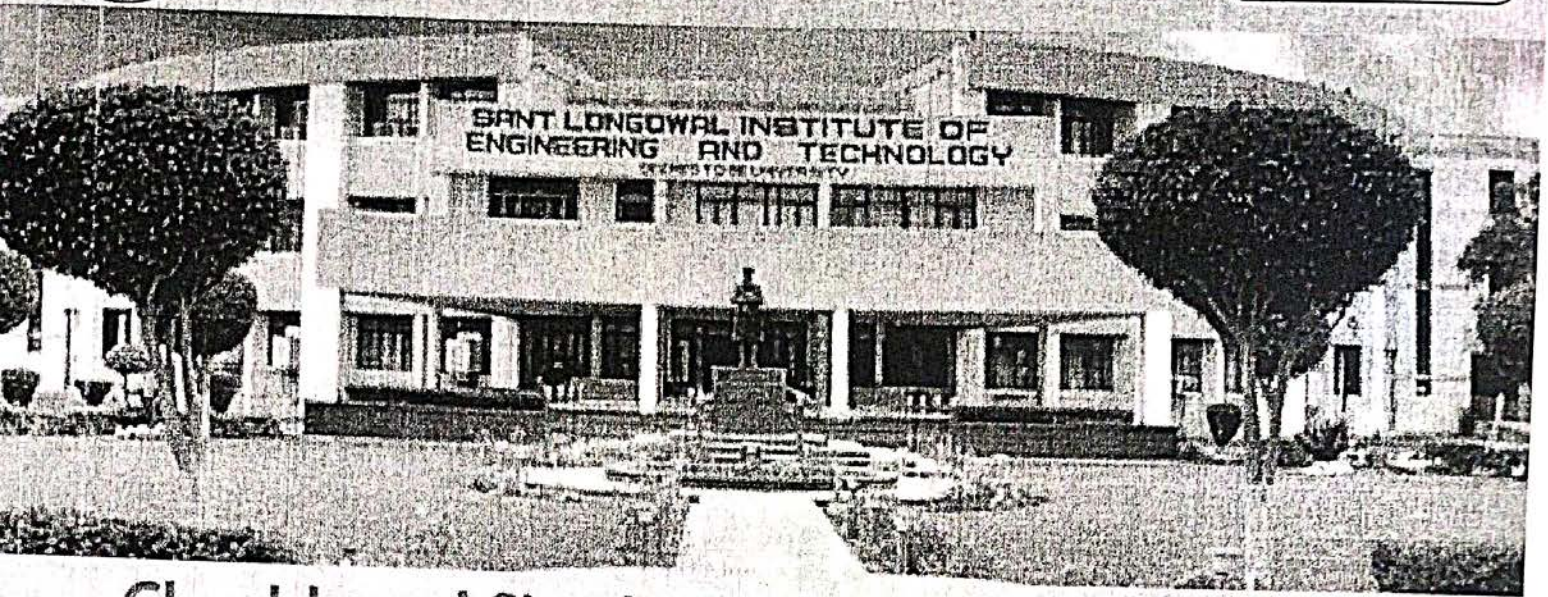
This is certified that Dr. Dilip Kumar Professor of Sant Longowal Institute of Engg. and Technology, Longowal, PUNJAB has successfully completed ATAL Online Faculty Development Programme on "RECENT TRENDS IN PRECISION AGRICULTURE" from 31/05/2021 to 04/06/2021.

Adviser-I, ATAL Academy

S.A.



संत लॉगोवाल अभियांत्रिकी एवं प्रौद्योगिकी संस्थान
Sant Longowal Institute of Engineering and Technology
(Deemed-to-be-University, under MHRD, Govt. of India)



Cloud-based Simulation Practices for Advanced Virtual Labs using myTaraNG

The cloud based interactive simulation solution specially installed for effective blended learning during the pandemic. myTaraNG portal proven to be best fit in current situation

07 TH FEBRUARY 2021 | 6:00 PM

www.mytarang.com



Swapnil Gaul
Founder,
NUMEREGION



Madhura Barshikar
Application Engineer,
NUMEREGION

Coordinators



Dr. Surinder Singh
Head, ECE Department
SLIET Longowal



Dr. Anupma Marwaha
Professor, ECE Department
SLIET Longowal



Mr. Kuldip Singh
Asst. Prof, ECE Dept.
SLIET Longowal

Registration Link: <https://forms.gle/hCJkwnu1WkL1wbWv7>



Workshop Report

“3 Days workshop on Cloud Based Simulation Practices for Advanced Virtual Labs using My TaraNG ”

A three-day online workshop on, “Cloud Based Simulation Practices for Advanced Virtual Labs using My TaraNG” has been organized by the department of **Electronics & Communication Engineering** on 07/02/21 onwards with hands on practice sessions covered on 13/02/21 and 14/02/21 respectively for the postgraduate and undergraduate students of the Institute.

The main objective of this workshop was to provide hands on practices about cloud based interactive simulation tool specially installed for effective blended learning to graduate and post graduate students during the pandemic. No financial assistance was taken from the institute and no participation fee was charged from the participants for this workshop.

First day of workshop scheduled on 07/02/2021 covered sign up and sign in process, installation of connector and access of TaraNG simulation software installed on remote system and a demonstration of TaraNG simulation software for designing of different circuits and components.

Hands on sessions on virtual lab for postgraduate and undergraduate students were scheduled on 13th and 14th February, respectively.

Resource Person:

1. **Er. Swapnil Narhari Gaul:** Founder and Director | NUMEREGION, Delhi NCR
2. **Madhura Barshikar :** Application Engineer | NUMEREGION, Delhi NCR

Coordinators:

1. **Dr. A Marwaha (Professor, ECE)**
2. **Kuldip Singh (AP, ECE)**





AICTE-QIP Sponsored

**Six Days Online Short-Term Course (STC) on
Computational Techniques in Image and Signal Processing
(Sep 28 – Oct 3, 2020)**

jointly organized by
**Department of Electronics & Communication Engineering
&
Department of Electrical & Instrumentation Engineering,
SLIET Longowal**

Program Schedule

Sep 28, 2020	
09:45 AM to 10:25 AM	Inauguration
10:30 AM to 12:00 PM	Speaker: Dr. Ram Bilas Pachori Professor, Department of Electrical Engineering, IIT Indore Topic: Time-Frequency Analysis
12:15 PM to 01:45 PM	Speaker: Dr. Amar Pratap Singh Professor, Department of Electronics & Communication Engg, SLIET, Longowal Topic: Introduction to Swarm Intelligence
Sep 29, 2020	
09:30 AM to 11:00 AM	Speaker: Dr. Tapan Kumar Gandhi Associate Professor, Department of Electrical Engineering, IIT Delhi Topic: Computational Neuroscience
11:15 AM to 12:45 PM	Speaker: Dr. Puneet Gupta Assistant Professor, Department of CSE, IIT Indore Topic: Heart Rate Estimation Using Non-Contact Face Videos
Sep 30, 2020	
09:30 AM to 11:00 AM	Speaker: Dr. Partha Pratim Roy Associate Professor, Department of CSE, IIT Roorkee Topic: EEG acquisition and Its Applications
11:15 AM to 12:45 PM	Speaker: Dr. Surinder Singh Professor, Department of Electronics & Communication Engg, SLIET, Longowal Topic: Optical Signal Processing
Oct 1, 2020	
09:30 AM to 11:00 AM	Speaker: Dr. Hariharan Muthusamy Associate Professor, Department of Electronics Engg, NIT Uttarakhand Topic: Algorithms for Feature Selection Problems
11:15 AM to 12:45 PM	Speaker: Dr. Ajat Shatru Arora Professor, Department of Electrical & Instrumentation Engineering, SLIET, Longowal Topic: Role of Liveness Detection In Biometrics
Oct 2, 2020	
09:30 AM to 11:00 AM	Speaker: Dr. Amit M Joshi Assistant Professor, Department of ECE, MNIT Jaipur Topic: Real-Time Video Watermarking for Authentication
11:15 AM to 12:45 PM	Speaker: Dr. Sanjay Marwaha Professor, Department of Electrical & Instrumentation Engineering, SLIET, Longowal Topic: Computational Electromagnetics in Medical Application

Report of STC Conducted Under AICTE OIP scheme

1. Name of the Department: Department of Electronics & Communication Engineering
& Department of Electrical & Instrumentation Engineering
2. Title of Program: Computational Techniques in Image and Signal Processing
3. Name of Coordinator: Er Vivek Harshey (ECE) & Er. Sunil K Bansal (EIE)
4. Duration (Dates): **Sep 28 – Oct 3, 2020**
5. Day wise schedule: Attached
6. Details of Experts/Keynote Speakers:

S. No.	Name of Experts with Designation	Organization	No. of Lectures Delivered/Hours
1	Dr. Ram Bilas Pachori, Professor	IIT Indore	01
2	Dr. Amar Pratap Singh, Professor	SLIET, Longowal	01
3	Dr. Tapan Kumar Gandhi, Associate Professor	IIT Delhi	01
4	Dr. Puneet Gupta, Assistant Professor	IIT Indore	01
5	Dr. Partha Pratim Roy, Associate Professor	IIT Roorkee	01
6	Dr. Surinder Singh, Professor	SLIET, Longowal	01
7	Dr. Hariharan Muthusamy, Associate Professor	NIT Uttarakhand	01
8	Dr. Ajat Shatru Arora, Professor	SLIET, Longowal	01
9	Dr. Amit M Joshi, Assistant Professor	MNIT Jaipur	01
10	Dr. Sanjay Marwaha, Professor	SLIET, Longowal	01
11	Er. Vivek Harshey, Assistant Professor	SLIET, Longowal	01
12	Dr. Vikas Mittal, Associate Professor	NIT Kurukshetra	01
13	Er. Sunil K Bansal, Assistant Professor	SLIET, Longowal	01

7. Details of Participants:

(a) No. of participants:

External	109
Internal	21
Total	130*

Details list of participants is attached.

*** registration fee – Nil.**

8. Expenditure Details: (In Rs.)

S. No.	Head	Expenditure
1.	Honorarium to Resource Persons	65,000.00
2.	Honorarium to Course Coordinators	2,000.00
3	Contingency and Consumables and Misc.	13500.00
	Total	80500.00*

* Subject to clearance from account section.

Uh

Speech signal analysis

- ❖ Finds application in diverse settings,
- ❖ Shown great promise in biomedical and life science applications.

New equipment and methodologies-

- ❖ facilitating diagnosis,
- ❖ severity assessment,
- ❖ monitoring disease progression and
- ❖ evaluating treatment response.

Speech signals

- ❖ Recorded under highly controlled acoustic conditions, hinders recruitment and monitoring of the population at large.
- ❖ Recorded under non-controlled acoustic conditions (using the internet, smart phone apps, or the telephone network) pose new challenges as speech signals are collected on massive scales .

Exciting new applications

- ❖ Assessment of early vocal pathologies,
- ❖ Assessment of neurodegenerative and mental disorders,
- ❖ communication and professional use of voice (e.g. singing).

U₂



Preetpal Kaur B.
and 26 more

03:38



09



BAVNEET KAUR



Mohd Naseem



sureshkumarA



Rajesh Mahatma



Jay



TEEQIP



A Short-Term Course on
Nano-electronics & VLSI: Devices, Circuits and Systems

November 04th – 08th, 2019

CERTIFICATE OF APPRECIATION

This is to certify that Er. Pankaj Kumar Das, Asst. Professor, Department of Electronics & Comm. Engineering, SLIET Longowal has contributed as Convener of Five-days Short-Term Course on “*Nano-electronics & VLSI: Devices, Circuits and Systems*”, sponsored by TEEQIP-III under twinning activities with SLIET Longowal and organized by Department of Electronics Engineering, NIT Uttarakhand during November 4th – 8th, 2019.

Head, ECE Deptt.

Director, NIT Uttarakhand



Sant Longowal Institute of Engineering & Technology

(Deemed to be University, Established by Govt. of India)

TEQIP-III Sponsored

TWO-DAYS WORKSHOP

"Low and High Frequency Designs using TaranG:19.0"

October 5-6, 2019

Organized by:

Department of Electronics & Communication Engineering
Sant Longowal Institute of Engineering & Technology
Longowal Distt. Sangrur (Punjab), India





Sant Longoval Institute of Engineering & Technology
(Deemed to be University) Established by Govt. of India

TEQIP-III Sponsored
Two Days Work

Low and High

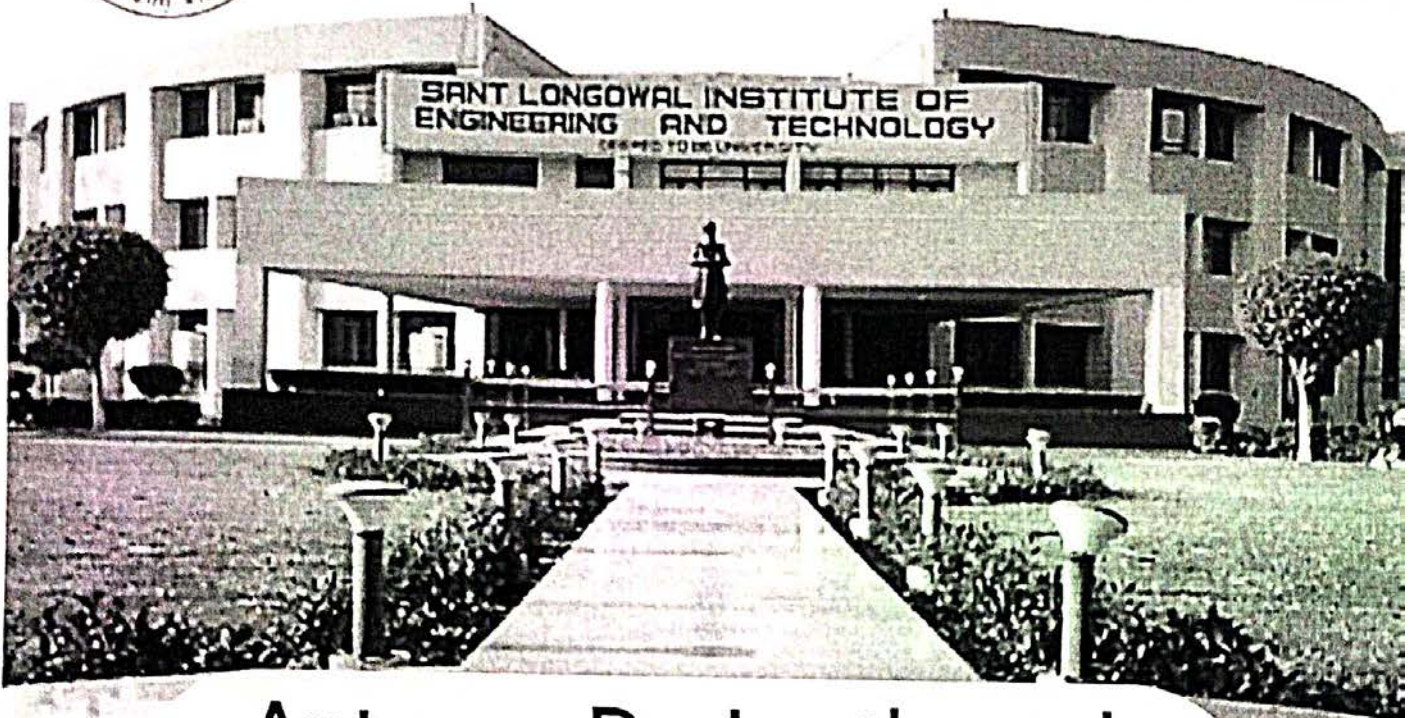
on
JRCY Design & Robert 5-6, 2012

Organized by
& Communicated by
Engineer Pratik (P.U.)



संत लौंगोवाल अभियांत्रिकी एवं प्रौद्योगिकी संस्थान
Sant Longowal Institute of Engineering and Technology
(Deemed-to-be-University, under MHRD, Govt. of India)

WEBINAR



Antenna Design through Simulation using TaraNG 19.1

SUNDAY JUNE 14, 2020 05:00 PM



Speaker
SWAPNIL GAUL
Founder | Director
NUMEREGION



Scan to Register

Registraton Link
www.numeregion.com/webinar/sliet/

Co-ordinators



Dr. Anupma Marwaha
Professor & Head,
Dept. of ECE, SLIET



Kuldip Lohchab
Assistant Professor,
Dept. of ECE, SLIET



Contact us:
Mr. Swapnil Gaul
+91 8390240903

**TEQIP-III SPONSORED
Short Term Training Programme**

**on
"Recent Trends in Electronics
& Communication Engineering"
(RTECE-2018)**

(September 24-28, 2018)



ORGANIZED BY:

**DEPARTMENT OF ELECTRONICS &
COMMUNICATION ENGINEERING,**

**SANT LONGOWAL INSTITUTE OF
ENGINEERING & TECHNOLOGY,
(Deemed-to-be-University, established by MHRD, GOI)
LONGOWAL-148106, DISTRICT
SANGRUR (PUNJAB), INDIA**

<http://www.sliet.ac.in>

ABOUT SLIET

Sant Longowal Institute of Engineering & Technology (SLIET) is a Deemed-to-be-University, established by MHRD, Government of India and controlled by SLIET Society and registered under Societies Registration Act, 1860. This Institute has come up beautifully in a sprawling green area of 451 acres, with many topographically featured picturesque landscape and presents spectacle of harmony and natural beauty, embedded with all the amenities required for a complete township. It offers unique modular, multi entry multi exit pattern of education in various branches of Engineering & Technology, to prepare the skilled and qualified manpower for self-employment. The study programmes include various courses at Integrated Certificate and Diploma, Degree, M. Tech. & Ph. D. levels in 11 departments of Engineering, Science and Technology. It caters to the technical manpower requirements at various levels in Industries/Institutions by imparting technical education with emphasis on industrial training and entrepreneurship development.

LOCATION AND WEATHER

The Institute is located at Longowal village and is connected by road with Sangrur (18 km), Barnala (30km), Patiala (80km), Ludhiana (110km) and Chandigarh (150km). The nearest railway stations are Sangrur (18 km), Sunam (18km), Barnala (30km) and Dhuri (35 km). The nearest airport is at Chandigarh. The month of September is end of rainy season with pleasant weather. The temperature in September ranges from 25 degree Celsius to 35 degree Celsius.

THE DEPARTMENT

The Department of Electronics & Communication Engineering offers Integrated Certificate & Diploma (ICD) Programme, Undergraduate (B. Tech.) and Post Graduate (M.Tech.) courses as well as Ph. D. Programme in the discipline of Electronics & Communication Engineering. The Department has highly qualified faculty and the same is actively involved in quality teaching and research. A number of research projects sponsored by different external agencies are also being undertaken vigorously.

ABOUT THE COURSE

The complexity of electronics and communication systems has grown considerably during the past three decades. The emergence of a variety of new technologies such as inexpensive high-speed hardware for digital signal processing has had significant impact on implementation of communication systems. While the growth in complexity of electronics and communication systems increases the time and effort required for analysis and design, the need to insert new technologies into commercial products quickly requires that the design be done in a timely, cost effective and effort free manner.

COURSE OBJECTIVES

- To provide broad exposure to the participants about the various aspects of Electronics and Communication Engineering.
- To enable the participants in the field of Electronics and Communication Engineering to introspect and learn techniques that can help them for active and successful participants in a knowledge



Department of Electronics & Communication Engineering

solicit your benign presence on the occasion of

Valedictory Ceremony

of

Three days' Workshop on

"Analog CMOS Integrated Circuit Design"

(14th -16th April, 2018)

on April 16, 2018 at 12:00 Noon

in the Computer Auditorium of the Institute

Prof. Shailendra Jain

Director, SLIET

has kindly consented to be the Chief Guest of the function

and

Prof. M B Bera

Dean (Academics)

&

Er. H S Jatana

Scientist/Engineer 'G' SCL / ISRO, Dept of Space Mohali

will be the Guests of Honour

R.S.V.P

Organizing Committee of the WACICD

Programme Overleaf

INVITATION

Department of Electronics & Communication Engg. is going to organize three days workshop on Analog CMOS Integrated Circuit Design for faculty, staff and students of SLIET, Longowal during 14th -16th April, 2018 (Saturday, Sunday & Monday) as per the details given below:

Venue – Computer Auditorium

Resource Persons: – Er. HS Jatana, Gr Head – Design & Process Group ; Er. Ashutosh Yadav, Sci/Engr 'E' DPG/VDD ; Er. Rajesh Srivastava, Sci/Engr 'D' DPG/VDD

The objective of this workshop is to provide comprehensive coverage of the basic concepts of analog integrated circuits and characteristics. It aims to provide an In-depth knowledge of analog design flow and Device Simulator/ cadence EDA tools.

Schedule

Day 1 (14th April, 2018):

Inauguration	Inauguration at 9:30 AM
LECT01	CMOS Process & Design Challenges in Current Technology
LECT02	Process Integration & Components available in Standard CMOS Technology
LECT03	MOS – Functionality, Models
LECT04	CMOS as a Switch – Issues, Challenges, Problems and remedies
LECT05	CMOS as Amplifier – Issues and Challenges
LAB01	Understanding and Using Device Simulator

Day 2 (15th April, 2018):

LECT06	Single MOS Tx Topologies – CS Configuration (issues in Tx sizes, I/p & O/p swings, lower node issues)
LECT07	CS Configuration – Frequency response. Effect of DSM technology nodes on freq response, effect of Ft and Fmax .
LECT08	Biasing techniques used in Chip Design
LECT09	Concept of Controlled Sources, significance, where, why and how used in chip design
LECT10	VCVS using MOS Tx
LAB02	Analog Design Flow ; Schematic Entry

Day 3 (16th April, 2018):

LECT11	CCCS using MoS Tx – Large signal analysis, Small signal analysis
LECT12	Current sources – relevance and used in chip design
LECT13	Voltage reference – concept, how to use in chip design
LAB03	Layout of Single Tx topology – meaning of DRC,LVS,PEX and its relevance
QUIZ	Brief Quiz of 25 minutes.
Valedictory Ceremony	3:00 PM onwards

Interested faculty, staff, research scholars and students are cordially invited to attend the same.

Convener : Dr. Jagpal Singh Ubbi,
Professor (ECE)

Chairperson : Dr. Anupma Marwaha,
Professor & HOD (ECE)

Copy to:

- Director Cell: for Information, please
- All Deans, for information, please
- HODs, with a request to circulate in the respective department

SIGNATURE OF STUDENT

S.NO.	TRADE	REGN NO	NAME OF STUDENTS	Morning	Afternoon
1	GEC	1640014	YASHVENDRA CHAUHAN	- X -	X
2	GEC	1640015	RITIKA NEGI	- X -	X
3	GEC	1640019	PULKIT AGGARWAL	- X -	X
4	GEC	1640024	NAVJOT JYOTI	Navjot Jyoti	Navjot Jyoti
5	GEC	1640033	UTTAM KUMAR	- X -	X
6	GEC	1640051	SHUBHAM KUMAR JHA	- X -	X
7	GEC	1640055	SHRISTI PRAKASH	Shristi Prakash	Shristi Prakash
8	GEC	1640056	PANIKAR	- X -	X
9	GEC	1640062	ATISH DEEPANKAR	Atish Deepankar	Atish Deepankar
10	GEC	1640086	RIA SHARMA	Ria Sharma	Ria Sharma
11	GEC	1640090	MUTYALA DEVENDRA VENKAT	- X -	X
12	GEC	1640092	ISHPREET KAUR	- X -	X
13	GEC	1640103	VARDIREDDY VASUDEVA REDDY	- X -	X
14	GEC	1640154	UBAIR ALI	- X -	X
15	GEC	1640157	MEHRAJ UD DIN TELI	MEHRAJ - DIN	
16	GEC*	1640034	SUDHANSHU KUMAR	Sudhanshu Kumar	Sudhanshu Kumar
17	GEC*	1640052	SHEFALI GOYANKA	Shefali Goyanka	Shefali Goyanka
18	GEC	1730964	ASHIF REZA	- X -	X
19	GEC	1730981	KUMARI BEENA SINGH	Beena Singh	Beena Singh
20	GEC	1730982	ANJALI SINGH	X	X
21	GEC	1730983	ANSHU VERMA	Anshu Verma	Anshu Verma
22	GEC	1731015	MD SIKANDAR	X	X
23	GEC	1731538	JASLEEN KAUR	X	X
24	GEC	1731561	ANIRUDH GOEL	Anirudh	Anirudh
25	GEC	1731562	SHEKHAR KAPUR	Shekhar Kapur	Shekhar Kapur
26	GEC	1731564	AMAN RAJ	X	X
27	GEC	1731565	SHAHANWAZ AHMAD	X	X
28	GEC	1731566	AKANKSHA KUMARI	Akanksha Kumari	Akanksha Kumari
29	GEC	1731567	KESHAV ACHARYA	Keshav Acharya	Keshav Acharya
30	GEC	1731568	PRIKSHIT AWASTHI	X	X

SIGNATURE OF STUDENT

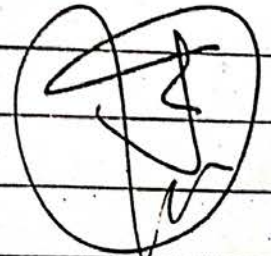
S.NO.	TRADE	REGN NO	NAME OF STUDENTS	Morning	Afternoon
31	GEC	1731569	PRAJJWAL	X	X
32	GEC	1731570	RAVI SHANKAR KUMAR	Ravishankar Kumar	
33	GEC	1731571	AMARTYA PAUL	Ampaul	Ampaul
34	GEC	1731572	BHANU PRATAP AGGARWAL	X	X
35	GEC	1731573	ANAND VERMA	X	X
36	GEC	1731574	NITIN PILLAY	X	X
37	GEC	1731575	DIVYA JYOTI	Divya Jyoti	Divya Jyoti
38	GEC	1731576	BHAWNA PRASHER	X	X
39	GEC	1731577	PRIYA KUMARI	Priya Kumari	Priya Kumari
40	GEC	1731578	SONAM KUMARI	X	X
41	GEC	1731579	PARITOSH CHHABRA	Paritosh	Paritosh
42	GEC	1731580	MANMEET KAUR SAINI	X	X
43	GEC	1731581	AKANKSHA SINGH	Akanksha	Akanksha
44	GEC	1731582	ANU PRIYA BHARTI	Anupriya	Anupriya
45	GEC	1731583	PUSHKAR ANAND	Pushkar Anand	Pushkar Anand
46	GEC	1731584	AMRITA PRITAM	Amrita Pritam	Amrita Pritam
47	GEC	1731585	SACHIN KUMAR	Sachin Kumar	Sachin Kumar
48	GEC	1731586	ISHA KIRTI	Ishakirti	Ishakirti
49	GEC	1731587	SHWETA KUMARI	Shweta Kumari	Shweta Kumari
50	GEC	1731588	ARUN BHASKAR	Arun Bhaskar	Arun Bhaskar
51	GEC	1731589	HARSIMRAN KAUR	X	X
52	GEC	1731590	DIVYANSHU RANJAN	Divyanshu Ranjan	Divyanshu Ranjan
53	GEC	1731591	RAUSHAN KUMAR	Raushan Kumar	Raushan Kumar
54	GEC	1731592	SADHANA SHUKLA	Sadhana Shukla	Sadhana Shukla
55	GEC	1731593	PALLAVI RAJ	Pallavi Raj	Pallavi Raj
56	GEC	1731594	NISHU KUMARI	Nishu Kumari	Nishu Kumari
57	GEC	1731596	IRFAN KASMI	X	X
58	GEC	1731597	NEERAJ KUMAR	Neeraj Kumar	Neeraj Kumar
59	GEC	1731598	ROHIT KUMAR	X	X
60	GEC	1731599	MANISH KUMAR	X	X
61	GEC	1731600	SUMIT KUMAR	X	X
62	GEC	1731602	DEV RAJ	Dev raj	Dev raj
63	GEC	1731603	MANISH KUMAR	X	X
64	GEC	1731604	JAYRAM SINGH YADAV	X	X
65	GEC	1731605	SHALINI DHIMAN	X	X
66	GEC	1731606	POOJA PRIYAMBADA	X	X

Programme
Valedictory Ceremony
April 16, 2018

Arrival of Guests	12:00 Noon
Kulgeet	12:02 PM
Welcome Address by Prof. A Marwaha, Chairman, WACICD	12:07 PM
About the workshop by Prof. J S Ubhi, Convener, WACICD	12:15 PM
Address by Prof. M B Bera, Dean (Academics)	12:25 PM
Address by Er. H S Jatana	12:35 PM
Valedictory Address by Chief Guest, Prof. Shailendra Jain	12:45 PM
Distribution of certificates/ facilitation	12:55 PM
Vote of Thanks	1:05 PM
National Anthem	

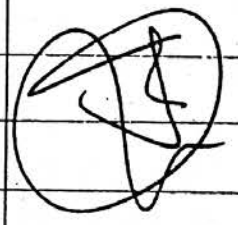

11/4/18

Sr. NO	Name	Registration No.	Signature
1.	PULKIT SINGHAL	GEC/121826	
2.	Ayushi Ranjan	GEC/121427	
3.	Shivani	GEC/121732	
4.	Manishu Raj	GEC/121741	
5.	Ramandeep Kaur	GEC/121712	
6.	Anjali Garg	GEC/121601	
7.	Amrit Joshi	GEC/121124	
8.	Sewanjali	GEC/121735	
9.	Priyanka Jindal	GEC/121509	
10.	Shivani	GEC/121512	
11.	Simran Soinika	GEC/121534	
12.	Shikha Guleria	GEC/121717	
13.	Shilpi Singh	GEC/121734	
14.	Swabli Ranjan	GEC/121445	
15.	Ankur Meena	Gec/1540085	
16.	Rushav Raj	GFC/121428	
17.	Rohan Singh Chauhan	GEC/SL/15/4219	
18.	Shilpa Garg	GEC/1630221	
19.	Sonal Kaur	Gec/1540080	
20.	Tejas Somanji	Gec/1540083	
21.	Payush Ranjan	Gec/1540081	
22.	VINAY KUMAR	GEC/1574209	
23.	Chitranshi	GEC/1540077	
24.	Brijkishor	GEC/1540076	



GEC/16

List of students for Workshop on Analog CMOS Integrated Circuit Design during 14th-16th April

Sr. NO	Name	Registration No.	Sign.
1.	Rahul Rey	GEC-1630003	Rahul Rey
2.	Shirani	GEC-1630036	Shirani
3.	Priya	GEC-1630033	Priya
4.	Bhavana Sharma	GEC-1630021	Bhavana
5.	SAKSHAM SOOD	GEC-1630020	Saksham
6.	Anchal	GEC/1630072	Anchal
7.	Shirani	GEC/1630048	Shirani
8.	Pulak	GEC/1630073	Pulak
9.	Snijet Kaur	GEC/1630047	Snijet
10.	Avinash Thakur	GEC/1630106	Avinash
11.	Rajesh Verma	GEC/1630166	Rajesh
12.	Arjun Boro	GEC/1630174	Arjun
		GEC/1630174	Arjun
			

GCC//6

<u>Name</u>	<u>Registration no.</u>
1. Kritika Negi	1640015
2. Navjot	1640024
3. Sudhanshu Kumar	1640034
4. Shubham Kumar Jha	1640051
5. Shifali Goyanka	1640052
6. Shristi Prakash	1640055
7. Riya Sharma	1640086
8. Dshpreet Kaur	1640092
9. Mehraj ud-din Teli	1640157
10. Ashif Reza	1730964
11. Kumari Beena Singh	1730981
12. Anjali Singh	1730982
13. Anshu Verma	1730983
14. Anirudh Goel	1731561
15. Shekhar Kapur	1731562
16. Aman Raj	1731564
17. Akanksha Kumari	1731566
18. Keshav Acharya	1731567
19. Amartya Paul	1731571
20. Akanksha Singh	1731581
21. Amrita Pritam	1731584
22. Sachin Kumar	1731585
23. Gcha Kirti	1731586
24. Shweta Kumari	1731587
25. Arun Bhacker	1731588
26. Divyanshu	1731590
27. Sadhana Shukla	1731592
28. Pallavi Raj	1731593
29. Nishu Kumari	1731594
30.	

VLSI workshop

SEMESTER:.....

S.No.	TRADE	REGN NO	NAME OF STUDENTS	SIGNATURE OF STUDENT	
				Morning Session	Afternoon session
30	GEC	1630074	AKSHAY	X	
31	GEC	1630076	NARINDER SINGH	X	
32	GEC	1630100	RINKI GUPTA	X	
33	GEC	1630106	AVINASH THAKUR	<i>Avinash</i>	
34	GEC	1630156	PRABHJOT KAUR	<i>Prabjit</i>	
35	GEC	1630166	RAJESH VERMA	X	
36	GEC	1630168	SAURAV KUMAR	X	
37	GEC	1630174	GUNIN BORO	X	
38	GEC	1630221	SHILPA VYAS	X	
39	GEC	GEC121124	AMIT JOSHI	X	
40	GEC	GEC121425	RISHAV RAJ	X	
✓41	GEC	GEC121427	AYUSHI RANJAN	<i>Ayushi Ranjan</i>	<i>Ayushi Ranjan</i>
✓42	GEC	GEC121445	SURABHI RANJAN	<i>Surabhi Ranjan</i>	<i>Surabhi Ranjan</i>
43	GEC	GEC121446	SAMEER KUMAR JHA	X	
✓44	GEC	GEC121509	PRIYANKA JINDAL	<i>Priyanka Jindal</i>	<i>Priyanka Jindal</i>
✓45	GEC	GEC121512	SHIVANI	<i>Shivani</i>	<i>Shivani</i>
✓46	GEC	GEC121534	SIMRAN SRINIKHA	<i>Simran Srinika</i>	<i>Simran Srinika</i>
47	GEC	GEC121601	ANJALI GARG	X	
✓48	GEC	GEC121712	RAMANDEEP KAUR	<i>Ramandeep Kaur</i>	<i>Ramandeep</i>
49	GEC	GEC121717	SHIKHA GULERIA	<i>Shikha Guleria</i>	<i>Shikha Guleria</i>
✓50	GEC	GEC121732	SHIWANI	<i>Shivani</i>	<i>Shivani</i>
✓51	GEC	GEC121734	SHILPI SINGH	<i>Shilpi Singh</i>	<i>Shilpi Singh</i>
52	GEC	GEC121735	SEWANJALI	X	<i>Sevanjali</i>
53	GEC	GEC121741	MANISH RAJ	<i>Manish Raj</i>	<i>Manish Raj</i>
54	GEC	GEC121743	KUMAR ABHISHEK	X	
55	GEC	GEC121744	DIVYANSHI	X	
56	GEC	GEC121826	PULKIT SINGHAL	<i>Pulkit Singh</i>	<i>Pulkit Singh</i>

NO. OF STUDENTS PRESENT:.....

NO. OF STUDENTS ABSENT:

SIGNATURE OF INVIGILATOR:

14-4-2018

VLSI Workshop

S.No.	Name	Roll No.	Signature of Student	
			Morning Session	Afternoon session
		PGECE-16		
1	RAHUL SINGH	1654001	Rahul Singh	Rahul Singh
2	KUMAR GAURAV SUMAN	1654002	G. Suman	G. Suman
3	GURDEEP Kaur	1654003	Gurdeep Kaur	
4	MANINDER Kaur	1654004	X	X
5	KALAKOLA ARUN	1654005	Arun	Arun
6	KOMAL KHADOTRA	1654006	Komal	Komal
7	BIKASH CHANDRA SAHOO	1654007	Bikash	Bikash
8	UJALA CHOUDHURY	1654008	Ujala	Ujala
9	VISHAL SHARMA	1654009	Vishal	Vishal
10	NEHA SHARMA	1654011	Neha	Neha
11	POOJA RANI	1654012	Pooja	
12	PRASHANT BHARDWAJ	1654013	X	X
13	KRISHNAMURTI RAI	1654014	Krishnamurti	Krishnamurti



DEGREE-2016
VLSI workshop

(JEE MAIN) & LEET 2017

SIGNATURE OF STUDENT

S.NO.	TRADE	REGN NO	NAME OF STUDENTS	SIGNATURE OF STUDENT	
				Morning	Afternoon
1	GEC	1640014	YASHVENDRA CHAUHAN		
2	GEC	1640015	RITIKA NEGI		
3	GEC	1640019	PULKIT AGGARWAL		
4	GEC	1640024	NAVJOT JYOTI	Navjot Jyoti	Navjot Jyoti ✓
5	GEC	1640033	UTTAM KUMAR		
6	GEC	1640051	SHUBHAM KUMAR JHA		
7	GEC	1640055	SHRISTI PRAKASH	Shristi Prakash	Shristi Prakash ✓
8	GEC	1640056	PANIKAR		
9	GEC	1640062	ATISH DEEPANKAR	Atish Deepankar	Atish Deepankar ✓
10	GEC	1640086	RIA SHARMA		
11	GEC	1640090	MUTYALA DEVENDRA VENKAT		
12	GEC	1640092	ISHPREET KAUR		
13	GEC	1640103	VARDIREDDY VASUDEVA REDDY		
14	GEC	1640154	UBAIR ALI		
15	GEC	1640157	MEHRAJ UD DIN TELI		
16	GEC*	1640034	SUDHANSHU KUMAR	Sudhanshu Kumar	Sudhanshu Kumar ✓
17	GEC*	1640052	SHEFALI GOYANKA	Shefali Goyanka	Shefali Goyanka ✓
18	GEC	1730964	ASHIF REZA		
19	GEC	1730981	KUMARI BEENA SINGH	Beena Singh	Beena Singh ✓
20	GEC	1730982	ANJALI SINGH		
21	GEC	1730983	ANSHU VERMA	Anshu Verma	Anshu Verma ✓
22	GEC	1731015	MD SIKANDAR		
23	GEC	1731538	JASLEEN KAUR		
24	GEC	1731561	ANIRUDH GOEL	Anirudh Goel	Anirudh Goel ✓
25	GEC	1731562	SHEKHAR KAPUR	Shekhar Kapur	Shekhar Kapur ✓
26	GEC	1731564	AMAN RAJ		
27	GEC	1731565	SHAHANWAZ AHMAD		
28	GEC	1731566	AKANKSHA KUMARI	Akanksha Kumari	Akanksha Kumari ✓
29	GEC	1731567	KESHAV ACHARYA	Keshav Acharya	Keshav Acharya ✓
30	GEC	1731568	PRIKSHIT AWASTHI		

Registration/Attendance Sheet PG 2017 batch,

VLSI workshop			Signature of Student	
S.NO.	ROLL NO.	NAME	Morning	Afternoon
1	1754001	VISHU GOYAL	<i>Vishu</i>	<i>Vishu</i>
2	1754002	GARIMA JAISWAL	<i>Garima</i>	<i>Garima</i>
3	1754003	SAMPURNANAND DWIVEDI	<i>Sampurnanand</i>	<i>Sampurnanand</i>
4	1754004	ARUN KAMAL SINGH	<i>Arun Kamal Singh</i>	<i>Arun Kamal Singh</i>
5	1754005	HARSHITA SHARMA	<i>Harshita</i>	<i>Harshita</i>
6	1754006	MANISH PRAKASH	<i>Manish</i>	<i>Manish</i>
7	1754007	SHIVANI SHARMA	<i>Shivani Sharma</i>	<i>Shivani Sharma</i>
8	1754008	DEEPSHIKHA PRIYADARSHI	<i>Deepshikha</i>	<i>Deepshikha</i>
9	1754009	JAY DIP MATHUR.	<i>Mathur</i>	<i>Mathur</i>
10	1754010	BHUPESH KUMAR	X	X
11	1754011	TARANVIR SINGH	X	X
12	1754012	ABHIRUCHI SRIVASTAVA	X	X
13	1754013	ASHMEET KAUR	<i>Ashmeet</i>	<i>Ashmeet</i>
14	1754014	BEERPAL KAUR	X	X
15	1754015	RITTU BALA	X	X

✓ ✓
 ✓ ✓
 ✓ ✓
 ✓ ✓
 ✓ ✓
 ✓ ✓

SIGNATURE OF STUDENT

S.NO.	TRADE	REGN NO	NAME OF STUDENTS	Morning	Afternoon
31	GEC	1731569	PRAJJWAL	/	
32	GEC	1731570	RAVI SHANKAR KUMAR	/	
33	GEC	1731571	AMARTYA PAUL	Amartya Paul	Amartya Paul ✓
34	GEC	1731572	BHANU PRATAP AGGARWAL	/	
35	GEC	1731573	ANAND VERMA	/	
36	GEC	1731574	NITIN PILLAY	/	
37	GEC	1731575	DIVYA JYOTI	/	
38	GEC	1731576	BHAWNA PRASHER	Bhawna Prasher	
39	GEC	1731577	PRIYA KUMARI	Priya Kumari	Priya Kumari ✓
40	GEC	1731578	SONAM KUMARI	/	
41	GEC	1731579	PARITOSH CHHABRA	Paritosh	Paritosh ✓
42	GEC	1731580	MANMEET KAUR SAINI	/	
43	GEC	1731581	AKANKSHA SINGH	Akanksha	Akanksha ✓
44	GEC	1731582	ANU PRIYA BHARTI	Anupriya	Anupriya ✓
45	GEC	1731583	PUSHKAR ANAND	Pushkar	Pushkar ✓
46	GEC	1731584	AMRITA PRITAM	Amrita Pritam	Amrita Pritam ✓
47	GEC	1731585	SACHIN KUMAR	Sachin Kumar	Sachin Kumar ✓
48	GEC	1731586	ISHA KIRTI	Ishakirti	Ishakirti ✓
49	GEC	1731587	SHWETA KUMARI	Shweta Kumari	Shweta Kumari ✓
50	GEC	1731588	ARUN BHASKAR	Arun Bhaskar	Arun Bhaskar ✓
51	GEC	1731589	HARSIMRAN KAUR	/	
52	GEC	1731590	DIVYANSHU RANJAN	Divyanshu Ranjan	Divyanshu Ranjan ✓
53	GEC	1731591	RAUSHAN KUMAR	Raushan Kumar	Raushan Kumar ✓
54	GEC	1731592	SADHANA SHUKLA	Sadhana Shukla	Sadhana Shukla ✓
55	GEC	1731593	PALLAVI RAJ	Pallavi Raj	Pallavi Raj ✓
56	GEC	1731594	NISHU KUMARI	Nishu Kumari	Nishu Kumari ✓
57	GEC	1731596	IRFAN KASMI	/	
58	GEC	1731597	NEERAJ KUMAR	Neeraj Kumar	Neeraj Kumar ✓
59	GEC	1731598	ROHIT KUMAR	/	
60	GEC	1731599	MANISH KUMAR	/	
61	GEC	1731600	SUMIT KUMAR	/	
62	GEC	1731602	DEV RAJ	Dev Raj	
63	GEC	1731603	MANISH KUMAR	/	
64	GEC	1731604	JAYRAM SINGH YADAV	/	
65	GEC	1731605	SHALINI DHIMAN	/	
66	GEC	1731606	POOJA PRIYAMBADA	Pooja	

