

Department of Electronics & Communication Engineering

solicit your benign presence on the occasion of

Valedictory Ceremony

of

Three days' Workshop on

"Analog CMOS Integrated Circuit Design"

(14th -16th April, 2018)

on April 16, 2018 at 12:00Noon

in the Computer Auditorium of the Institute

Prof. Shailendra Jain Director, SLIET

has kindly consented to be the Chief Guest of the function

and

Prof. M B Bera

Dean (Academics)

&

Er. H S Jatana

Scientist/Engineer 'G" SCL / ISRO, Dept of Space Mohali

will be the Guests of Honour

R.S.V.P

Organizing Committee of the WACICD

Programme Overleaf



INVITATION

Department of Electronics & Communication Engg. is going to organize three days workshop on Analog CMOS Integrated Circuit Design for faculty, staff and students of SLIET, Longowal during 14th -16th April, 2018 (Saturday, Sunday & Monday) as per the details given below:

Venue – Computer Auditorium

Resource Persons: – Er. HS Jatana, Gr Head – Design & Process Group ; Er. Ashutosh Yadav, Sci/Engr 'E' DPG/VDD ; Er. Rajesh Srivastava, Sci/Engr 'D' DPG/VDD

The objective of this workshop is to provide comprehensive coverage of the basic concepts of analog integrated circuits and characteristics. It aims to provide an in-depth knowledge of analog design flow and Device Simulator/ cadence EDA tools.

Schedule

Inauguration	Inauguration at 9:30 AM
LECT01	CMOS Process & Design Challenges in Current Technology
LECT02	Process Integration & Components available in Standard CMOS Technology
LECT03	MOS – Functionality, Models
LECT04	CMOS as a Switch – Issues, Challenges, Problems and remedies
ECT05	CMOS as Amplifier – Issues and Challenges
LAB01	Understanding and Using Device Simulator

Day 2 (15th April, 2018):

Single MOS Tx Topologies – CS Configuration (issues in Tx sizes, I/p
& O/p swings, lower node issues)
CS Configuration – Frequency response. Effect of DSM technology nodes on freq response, effect of Ft and Fmax .
Biasing techniques used in Chip Design
Concept of Controlled Sources, significance, where, why and how used in chip design
VCVS using MOS Tx
Analog Design Flow ; Schematic Entry
and

Day 3 (16th April, 2018):

LECT11	
4	CCCS using MoS Tx – Large signal analysis, Small signal analysis
LECT12	Current sources releases and sold shall signal analysis
LECT13	Current sources – relevance and used in chip design
LECTIS	Voltage reference – concept, how to use in chip design
LAB03	Lavout of Single Tests in the date in this design
1	Layout of Single Tx topology – meaning of DRC,LVS,PEX and its
4	relevance
QUIZ	Brief Quiz of 25 minutes.
Valedictory Ceremony	3:00 PM onwards

terested faculty, staff, research scholars and students are cordially invited to attend the same.

onvener : Dr. fa rofessor (ECE)

Copy to:

- Director Cell: for information, please
- All Deans, for information, please
- HODs, with a request to circulate in the respective department

Chairperson : Dr. Anupma Marwaha, Professor & HOD (ECE)

DEGREE-2016 VLSI workshop

(JEE MAIN) & LEET 2017

				SIGNATURE OF STUR	DENT
5.NO.	TRADE	REGN NO	NAME OF STUDENTS	Morning	Afternoon
1	GEC	1640014	YASHVENDRA CHAUHAN	- * -	×
2	GEC	1640015	RITIKA NEGI	- ×-	×
3	GEC	1640019	PULKIT AGGARWAL	-1-	×
4	GEC	1640024	NAVJOT JYOTI	Namet Jysti	Navjot Jyolf
5	GEC	1640033	UTTAM KUMAR	-x	*
6	GEC	1640051	SHUBHAM KUMAR JHA	- 8	×
7	GEC	1640055	SHRISTI PRAKASH	Straisti Praka	Shrish Praketh
8	GEC	1640056	PANIKAR		X
9	GEC	1640062	ATISH DEEPANKAR	KHIX End M	Afisherhold
10	GEC	1640086	RIA SHARMA	Ria shalima	Ria Sharman
11	GEC	1640090	MUTYALA DEVENDRA VENKAT		×
12	GEC	1640092	ISHPREET KAUR	-x	×
13	GEC	1640103	VARDIREDDY VASUDEVA REDDY	-x	×
14	GEC	1640154	UBAIR ALI	- ×	×
15	GEC	1640157	MEHRAJ UD DIN TELI	MEHEAT - DINI	
16	GEC*	1640034	SUDHANSHU KUMAR	Sudhashuk	Sudharshmann.
17	GEC*	1640052	SHEFALI GOYANKA	Support	The billing to
18	GEC	1730964	ASHIF REZA		X
19	GEC	1730981	KUMARI BEENA SINGH	Biena Singh	Bungsingh
20	GEC	1730982	ANJALI SINGH	the state	Xyy
21	GEC	1730983	ANSHU VERMA	Angle vaema	Anster vacora
22	GEC	1731015	MD SIKANDAR	X	×
23	GEC	1731538	JASLEEN KAUR	r	×
24	GEC	1731561	ANIRUDH GOEL	Aniquelly	Animaly
25	GEC	1731562	SHEKHAR KAPUR	Sheburraher	SHOT July
26	- GEC	1731564	AMAN RAJ	X	×
27	GEC	1731565	SHAHANWAZ AHMAD	X	×
28	GEC	1731566	AKANKSHA KUMARI	Flandlefe Kimer	Alkantisha Kiner
29	GEC	1731567	KESHAV ACHARYA	Keshar Achin	Kestan Acto
30	GEC	1731568	PRIKSHIT AWASTHI	X	X

NO. OF STUDENTS PRESENT:..... NO. OF STUDENTS ABSENT: SIGNATURE OF INVIGILATOR:

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DEGREE-2016 VLSI workshop

(JEE MAIN) & LEET 2017

				SIGNATURE OF STUD	ENT
S.NO.	TRADE	REGN NO	NAME OF STUDENTS	Morning	Afternoon
31	GEC	1731569	PRAJJWAL	X	X
32	GEC	1731570	RAVI SHANKAR KUMAR	Pawishantas turner	
33	GEC	1731571	AMARTYA PAUL	KABOULL	Apault
34	GEC	1731572	BHANU PRATAP AGGARWAL	×	×
35	GEC	1731573	ANAND VERMA	1	X
36	GEC	1731574	NITIN PILLAY	2	×
37	GEC	1731575	DIVYA JYOTI	Ding gyati	phypa fycti
38	GEC	1731576	BHAWNA PRASHER	Y	X
39	GEC	1731577	PRIYA KUMARI	Prtya Kumini	Jorga Kumani
40	GEC	1731578	SONAM KUMARI	4	X
41	GEC	1731579	PARITOSH CHHABRA	Paritosh.	Paritah
42	GEC	1731580	MANMEET KAUR SAINI	X	X
43	GEC	1731581	AKANKSHA SINGH	AKanksho_	Akankshaz
44	GEC	1731582	ANU PRIYA BHARTI	Anupriya	Anupliya
45	GEC	1731583	PUSHKAR ANAND	Dista mary	Pushka fren
46	GEC	1731584	AMRITA PRITAM	Amrita Pritam	Amrita Pritam
47	GEC	1731585	SACHIN KUMAR	bacha Kumas	Sechinghung
48	GEC	1731586	ISHA KIRTI	Scharkitle	Maxieti
49	GEC	1731587	SHWETA KUMARI	Thedakovai	Shwele Kuman'
50	GEC	1731588	ARUN BHASKAR	Arun Bhaskon	Ann Sherley
. 51	GEC	1731589	HARSIMRAN KALIR	X	X
52	GEC	1731590	DIVYANSHU RANJAN	Diyonshu Rawan	Diyonshu Ranjon
53	GEC	1731591	RAUSHAN KUMAR	R wester kunner	Brushen law is
54	GEC	1731592	SADHANA SHUKLA	Ladhara Shurla	Sadhang Shukla
55	GEC	1731593	PALLAVI RAJ	Kallavi Raj	Callavi Raj
56	GEC	1731594	NISHU KUMARI	Dis he homen	Nishu kumat
57	GEC	1731596	IRFAN KASMI	X	X
58	GEC	1731597	NEERAJ KUMAR	Neuraf Au y	Neuroper,
59	GEC	1731598	ROHIT KUMAR	× ×	XI
60	GEC	1731599	MANISH KUMAR	ĸ	×
61	GEC	1731600	SUMIT KUMAR	X	X
62	GEC	1731602	DEV RAJ	Devraj	Bervar
63	GEC	1731603	MANISH KUMAR	K	X
64	GEC	1731604	JAYRAM SINGH YADAV	K	X
65	GEC	1731605	SHALINI DHIMAN	K	X
66	GEC	1731606	POOJA PRIYAMBADA	X	x



NO. OF STUDENTS PRESENT:..... NO. OF STUDENTS ABSENT: SIGNATURE OF INVIGILATOR:

Mail - HOD (ECE), SLIET, Longowal - Outlook



Cloud-based Simulation Practices for Advanced Virtual Labs using myTaraNG

The cloud based interactive simulation solution specially installed for effective blended learning during the pandemic. myTaraNG portal



Swapnil Gaul

NUMEREGION

Founder.

proven to be best fit in current situation 07 TH FEBRUARY 2021 | 6:00 PM www.mytarang.com



Madhura Barshikar Application Engineer. NUMEREGION



Dr.Surinder Singh Head, ECE Department **SLIET** Longowal Registration Link: https://forms.gle/hCJKwnu1WkL1wbWv7

Coordinators



Dr.Anupma Marwaha Professor, ECE Department **SLIET Longowal**

Mr. Kuldip Singh Asst. Prof, ECE Dept. **SLITE Longowal**

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Workshop Report

"3 Days workshop on Cloud Based Simulation Practices for Advanced Virtual Labs using My TaraNG "

A three-day online workshop on, "Cloud Based Simulation Practices for Advanced Virtual Labs using My TaraNG" has been organized by the department of Electronics & Communication Engineering on 07/02/21 onwards with hands on practice sessions covered on 13/02/21 and 14/02/21 respectively for the postgraduate and undergraduate students of the Institute.

The main objective of this workshop was to provide hands on practices about cloud based interactive simulation tool specially installed for effective blended learning to graduate and post graduate students during the pandemic. No financial assistance was taken from the institute and no participation fee was charged from the participants for this workshop.

First day of workshop scheduled on 07/02/2021 covered sign up and sign in process, installation of connector and access of TaraNG simulation software installed on remote system and a demonstration of TaraNG simulation software for designing of different circuits and components.

Hands on sessions on virtual lab for postgraduate and undergraduate students were scheduled on 13th and 14th February, respectively.

Resource Person:

1. Er. Swapnil Narhari Gaul: Founder and Director | NUMEREGION, Delhi NCR

2. Madhura Barshikar : Application Engineer | NUMEREGION, Delhi NCR

Coordinators:

1. Dr. A Marwaha (Professor, ECE)

2. Kuldip Singh (AP, ECE)



https://outlook.office.com/mail/inbox/id/A4/OkAGFmYmFm/MmJhLWUwYTQINDAzMC05NjY5LTikOGQzNTZhNGU5YwAQAHuRrMsd4TVKnvVTwpsgVRI%3D/sxs/AANIkAGFmYmFmMimJhLWUwYTQINDAzMC05NjY...

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Workshop Report

"Low Frequency (LF) and High Frequency Design (HF) Using TaraNG:19.0"

Two-day workshop on, "Low Frequency (LF) and High Frequency Design (HF) Using TaraNG:19.0" is organized by the department of ECE Under TEQIP-III during 5th-6th October 2019 for the undergraduate students of the institute. First day of workshop covered the insight on circuit simulations i.e. mathematical modelling of electronics components and provided insight on how the simulation algorithms are developed. During this, hands-on session on linear and non-linear circuits are conducted. At the end of session non ideal behavior of the electronic systems and effect of parasitic are discussed. The second day of workshop demonstrated the need of 3D software for design of electronics systems and briefed on EMI/EMC issues, signal crosstalk and need of S Parameters analysis at higher frequency. Second day focused on Antenna Design with modelling and simulation of sets of antennas especially Wire Antennas. After the end of workshop, students will be able to learn how computers are solving the electronics designs and helping us in entire product life-cycle at industries. Also, students will be able to develop and code their own algorithms to solve electronics designs.

Resource Person:

Er. Swapnil Narhari Gaul: Founder and Director | NUMEREGION, Delhi NCR

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First Name of Applicant: Pankaj Kumar	Surname: Das
Title: Prof.	Gender: M
Institution: Sant Longowal Institute of Engineering & Technology (SLIET, Longowal)	and although his of the set of th
Applicant's Mailing Address: Er. Pankaj Kumar Das	Telephone: 01672-253331
Assistant Professor	Mobile (if preferred): +919478214936
Electronics & Communication Engg. Department, Sant Longowal Institute of Engg. & Tech., Longowal. Deemed University (Established by Govt. of India)	Fax: 01672-253117
District: Sangrur (Punjab) 148106, India.	E-mail: Pankaj.jkd@gmail.com
Venue: ECE Deptt. SLIET Longowal	
Number of participants: 133	
Types of Participants (i.e. academia, students, communities, govern	nment, etc.): Students
And the second second state particular products and the second second second second second second second second	
a. Please describe briefly the activities conducted as part of this gra	
Two Days Online Workshop has been conducted for UG and PG Stu 2021. Following experts of Academia has been delivered the exper	ts talk:
1. Dr. Praveen Prajapati	
Associate Professor, Department of ECE, A. D. Patel	Institute of Technology, Gujarat
Topic: Basics of Design of Nano Antennas	
2. Dr. Pankaj Kumar Pal	

- Dr. Pankaj Kumar Pal Assistant Professor Department of ECE, NIT Uttrakhand Topic: Basics of Nanoelectronics and VLSI Design
- 3. Er. Pankaj Kumar Das Assistant Professor, Department of Electronics & Communication Engineering, SLIET Longowal Topic: Carbon Nanotube as Global VLSI Interconnects
- 4. Er. Swapnil Gaul,

Founder and Director Numergion Technology (OPC) PVT LTD, PUNE Maharashtra-411014 Topic: Advanced simulation based virtual lab: MyTaraNG Cloud b. Was this event successful in achieving the intended objectives and how? (300 words)

After having rigorous brainstorming, it is observed that the workshop in question was conducted meticulously for the target group including UG and PG Students with the achievement of the following aims/objectives:

✓ Provided broad exposure to the participants about the various aspects of Nano-electronics & VLSI design.

- Enabled the participants in the field of Electronics and Communication Engineering to introspect and learn techniques that help them for active and successful participants in a knowledge society.
- Enhanced the student's effectiveness in learning and research in the field of nano-electronics & VLSI design, modeling and simulation of device and circuits.
- Enriched the knowledge of participants by exposing them to latest trends and dimensions in nano-electronics such as spintronics, GaN and SiC devices.

Provided interaction with experts, which helps the participants to initiate their research in recent area in VLSI.

c. Enlist the possibilities wherein the event could promote SICI in your organization? (100 words)

- 1. We can Sign MOU for collaborative research with the Canadian Universities/School
- 2. Short out the possibilities to organize Conference in collaboration with SICI.
- 3. Will encourage the students to participate in different programme announced by SICI time-to-time.

d. Please indicate future plans (if any) based on the experience of this event. We could conduct Five-day Short-Term Course for faculty, Research Scholar, Industry Persons and Students.

e. Please provide your feedback (if any) on the SMDG grant.

I am thankful to SICI for awarding SMDG grant to me. While organizing the workshop it seems we need more money for successful conduction of programme. So, please increase the amount of SMDG Grant.

FINANCIAL REPORT

Note: Please Complete The Following Financial Report Detailing All Your Expenses As Part Of This Grant.

Description of items (expenses)		Amount
Remuneration to Experts Dr. Praveen Prajapati, AD Patel Institute, Gujarat		Rs. 5000/-
Remuneration to Experts Dr. Pankaj Kumar Pal, NIT Uttarakhand	and the appropriate Name of Statistical Con-	Rs. 5000/-
Remuneration to Experts Er. Pankaj Kumar Das, SLIET Longowal	Education of ECC 40 maniferrange evenes	Rs. 5000/-
Remuneration to Experts Er. Swapnil Gaul, Director Numergion Technology		Rs. 5000/-
	Total Expenses	Rs. 20,000/-
, 200 Joseph and States (1994) All Clause	Funds received by the Shastri Institute	Rs. 20,000/- (Rs. 17000/- as an advance. Rs. 3000/- is due)

MoE/SICI Sponsored (Online) WORKSHOP ON Nanoelectronics & VLSI (February 11- 12, 2021) (February 11- 12, 2021)

ORGANIZED BY Sant Longowal Institute of Engineering & Technology (Deemed to be University under MHRD, Govt. of India) Longowal, Distt. Sangrur-148106 Punjab (India).

Course Coordinator

Pankaj Kumar Das Electronics & Communication Engg. Department, Sant Longowal Institute of Engg. & Tech., Longowal. Deemed to be University (Established by Govt. of India) District: Sangrur (Punjab) 148106, India.

INSTITUTE

Sant Longowal Institute of Engineering & Technology (SLIET), Deemed to be University, has been established and funded by MHRD, Government of India in 1991, to provide technical education in emerging areas of Engineering and Technology. The institute caters to the technical manpower requirements at various levels by adopting a concept of a modular system in imparting professional education with an emphasis on practical training in the industry. The study programmes include various courses at Integrated Certificate and Diploma, Degree, M.Tech & Ph.D levels in 11 departments of Engineering, Science and Technology. The Institute has come up beautifully in a sprawling green area of 451 acres, with many topographically featured picturesque landscape and presents spectacle of harmony and natural beauty, embedded with all the amenities required for a complete township.

LOCATION AND WEATHER

The Institute is located at Longowal village and is connected by road with Sangrur (18 km), Sunam (18 km), Barnala (30 km), Patiala (80 km), Ludhiana (110 km) and Chandigarh (150 km). The nearest railway stations are Sangrur (18 km), Sunam (18 km), Barnala (30 km) and Dhuri (35 km). The nearest airport is at Chandigarh. The month of Febuary is winter season and temperature ranges from 20°C to 22°C.

SHASTRI INDO-CANADIAN INSTITUTE

The Shastri Indo-Canadian Institute is a binational organization that promotes understanding between India and Canada through academic activities and exchanges. The institute support the creation of binational links between academia, government, the business community and civil society organizations by funding research and hosting seminars. The Shastri Institute is funded by and partners closely with government bodies in both India and Canada. The Institute aims to achieve gender equality and reduce poverty by focusing programming on sustainable development and other United Nations Millennium **Development** Goals.

ABOUT THE WORKSHOP

Semiconductor devices are the basic building blocks of integrated chips (ICs). Due to the rapid development of semiconductor technology, the dimensions of the electronic devices are shrinking and the density of transistors have increased in the ICs which enables multiple features and functionalities in the electronic gadgets where energy, power, speed and area are the important design metrics. The proportionate change in the device dimensions in the nanometer regime also faces several challenges, worldwide. The nanoscale devices need quantum mechanics in order to model and understand the device behavior. Hence, this workshop will focus on various aspects of Nano-electronic devices and the possibility of circuits and system design.

EXPECTED OUTCOMES

In the current era of Nanoelectronics & VLSI semiconductor devices have been developed along with manufacturing technologies to improve the yield of semiconductor devices. The proposed workshop is planned for UG & PG students of the institute. After attending this workshop students have the broad exposure about the various aspect of Nano-electronics & VLSI Design from device level to low-power circuits & systems. Further, students can aware about the latest trends and dimensions in nano-electronics such as spintronics, GaN and SiC devices. Enabled the participants in the field of Electronics and Communication Engineering to introspect and learn techniques that help them for active and successful participants in a knowledge society. Enhanced the PG students ' effectiveness in research and learning in the field of nanoelectronics & VLSI design, modeling and simulation of device and circuits.

RESOURCE PERSONS

- 1) Dr. Pankaj Kumar Pal, Assistant Professor Department of ECE, NIT Uttrakhand
- 2) Dr. Praveen Prajapati, Associate Professor, Department of ECE, A. D. Patel Institute of Technology, Gujarat
- 3) Er. Pankaj Kumar Das, Assistant Professor Department of ECE, SLIET Longowal
- 4) Er. Swapnil Gaul, Founder and Director Numergion Technology (OPC) PVT LTD, PUNE Maharashtra-411014Praveen

REGISTRATION

The registration will be done online using the following link. The link is also available at <u>http://sliet.ac.in/</u> Link for Registration: https://forms.gle/qff7QAvcM9BZ7Aof6 No registration fees for the students from Indo-Canadian Institutes.

Course Contents

- Basics About Nanoelectronics
- Carbon Nanotubes Interconnect Design
- Nano antenna Design
- Challenges in Electronic System Design
- Recent Issues in VLSI Design
- Importance of Interconnects in System Design
- Importance and effective communication of e-Resources.

ELIGIBILITY:

The workshop is open to Degree and PG Students of the institute/ Indo-Canadian Institute. The number of participants for the course is limited to sixty (60) on a first-come, first-serve basis.

MODE OF CONDUCTION

The workshop will be conducted on a suitable online platform. All the participants who will attend all the workshop will be issued ecertificates post successful verification of the attendance.

REGISTRATION

The registration will be done online using the following link. The link is also available at <u>http://sliet.ac.in/</u>

Link for Registration:

https://forms.gle/qff7QAvcM9BZ7Aof6

No registration fees for the students from Indo-Canadian Institutes.

IMPORTANT DATES

Last date for registration: February 10, 2021 Notification of selection: February 10, 2021

ADVISORY COMMITTEE

Chief Patron: Prof. Shailendra Jain, Director SLIET Longowal Patron: Prof. Prachi Kaul, Director, SICI Prof. A. S Arora, Dean (Academics) Chairman: Prof. Surinder Singh, HOD(ECE), SLIET Longowal SICI Member Representative: Prof. Surita Maini, SLIET Longowal Coordinator: Er. Pankaj Kumar Das, SLIET Longowal

Address for correspondence Coordinator: Er. Pankaj Kumar Das, A.P. (ECE) Sant Longowal Institute of Engineering and Technology, Longowal, Sangrur, Punjab-148106 Ph. No. 9478214936, 9463122255 E-mail.: pankajkdas@sliet.ac.in





The Department of Electronics & Communication Engineering Sant Longowal Institute of Engineering & Technology, Longowal

> Solicit your benign presence on the occasion of Inaugural Ceremony of the MoE/SICI Sponsored Two Days Online Workshop

on

Nanoelectronics & VLSI Prof. Shailendra Jain, Director, SLIET Longowal has kindly consented to be the Chief Guest of the function

R.S.V.P Organizing Committee of the Programme

Inaugural Ceremony, February 11, 2021

10:00-10:05 AM	Initiation of the inaugural ceremony of the online workshop, by Er. Pankaj
	Kumar Das, Programme Coordinator, SLIET, Longowal
10:05-10:10 AM	Welcome address by Prof. Surinder Singh, Chairman (Workshop), SLIET,
	Longowal
10:10-10:20 AM	Address by SICI Member representative
10:20-10:30 AM	Address by Dean (Academics) SLIET Longowal
10:30-10:40 AM	Address by Sponsor, Director, SICI
10:40-10:50 AM	Inaugural Address by the Chief Guest
10:50-10:55 AM	Vote of thanks by Er Pankaj Kumar Das, Coordinator (Workshop), SLIET,
	Longowal

The event will be compered by Er Pankaj Kumar Das, AP (ECE).

Attendance for MoE/SICI Sponsored 2-Day Workshop on "Nanoelectronics & VLSI"

Sno	Regno	Student	PROGRAM	2/11/2021	2/12/202:
1	1740621		Туре	Present	Present
2	and the second of the second se	SHUBHI SHARMA	UG	Present	Present
3	Contraction of the second s	APURVA AKRITI		Present	Fresent
3	1740627	NAVNEET KUMAR RATNAKAR	UG	Present	Present
4	1740630	MD. REYAZUDDIN	UG	Present	Absent
5	1740632	SUJAY KUMAR	UG	Present	Present
6		SACHIN KUMAR	UG	Present	Absent
7		UTKARSHA KUMARI	UG	Absent	Present
8	1830282	PRITY KUMARI	UG	Present	Present
9	1830283	MALVIKA ROY	UG	Present	Present
10	1830284	SNEHA JHA	UG	Absent	Absent
11	1830285	PRIYA PALLAVI	UG	Present	Present
12	1830286	MADHU KUMARI	UG	Present	Present
13	1830287	DEEP MALA	UG	Present	Present
14	1830288	SHARDA KUMARI	UG	Present	Present
15	1830289	SHUBHAM SINHA	UG	Present	Present
16	1830290	AJAY KUMAR	UG	Present	Present
17	1830291	HARSH RAJ	UG	Present	Absent
18		ATUL MOHAN JHA	UG	Present	Present
19	1830294	VIKARSH KUMAR RAO	UG	Present	Present
20	1830295	ABHIRAJ VERMA	UG	Present	Present
21	1830296	SATYAM KUMAR	UG	Present	Present
22	1830297	SOUJANYA KALITA	UG	Present	Present
23	1830298	PRANJAL GUPTA	UG	Present	Present
24	1830299	MANISH KUMAR JHA	UG	Present	Present
25		PRERNA	UG	Absent	Present
26		ADARSH SHUBHAM	UG	Present	Present
27	1830302	HARSH CHAURASIA	UG	Present	Present
28	1830303	ANKIT KUMAR	UG	Present	Present
29		ADARSH KARN	UG	Present	Present
. 30	1830305	BITTU KUMAR	UG	Present	Present
31	1830306	ANKIT KUMAR	UG	Present	Absent
32	1830307	PRAKALP SHARMA	UG	Present	Present
33	1830308	NITISH SURYAN	UG	Present	Present
34	1830310	NISHAR TIWARI	UG	Present	Present
35	1830389	SHER KHAN	UG	Present	Present
36	1830394	MD SALMAN	UG	Present	Present
37		NIDHI JHA	UG	Present	Present
38		N V RAGHAVENDRA	UG	Present	Present
.39		SHUBHAM	UG	Present	Present
40	1930078	BHAVESH SINGH JADON	UG	Present	Present

Parkaj

41	1931501 544	SHAM GOPAL SINGH	lug	Present	Procent
41		VETANK RAMAN	UG	Present	Present Present
42	1931502 SHW		UG	Present	Present
43	1931504 ANS		UG	Present	Present
44	1931504 ANS		UG	Present	Present
	1931505 SON		UG	Present	Present
46	1931507 POC		UG	Present	
47	1931508 VIS		UG	Present	Present Present
48		HIL KUMAR CHAUDHARY	UG	Present	Present
50	1931510 SUF		UG	Present	Present
51		VRADIP PAUL	UG	Present	Present
52		MAR SAURAV	UG	Present	Present
53	1931513 AV	the second state of the se	UG	Present	Present
54	1931514 SHI		UG	Present	Present
55		PESH KUMAR THAKUR	UG	Present	Present
56		ARNA SHIVANI	UG	Present	Present
57	1931517 PR		UG	Present	Present
58		LONI RANJAN	UG	Present	Present
59	1931519 SU		UG	Present	Present
60	1931520 SH		ŲG	Present	Present
61	1931521 AN		UG	Present	Absent
62		UMYA SHREE	UG	Present	Present
63		URAV KUMAR	UG	Present	Present
64	1931525 PR	ACHI KUMARI	UG	Present	Present
65	1931526 DE	EKSHA SHARMA	UĠ	Present	Present
66	1931527 M	ANPREET KAUR	UG	Present	Present
67	1931528 SU	JBHASH KUMAR	UG	Present	Present
68	1931529 AY	USH KUMAR SRIVASTAVA	UG	Present	Present
69	1931530 GC	OPAL SINGH SOLANKI	ÜG	Present	Present
70	1931531 AK	KSHAY KUMAR	UG	Present	Present
71	1931534 VI	KAS KUMAR	UĠ	Present	Present
72	1840051 PR	RADHYUMAN JOSHI	ÜG	Present	Present
73	1840054 PR	RASHANT KUMAR	ÜG	Present	Present
74	1840059 UT	TKARSH YADAV	UG	Absent	Present
75	1840061 DI	HANESH KUMAR	UG	Present	Present
76	2050301 M	IOHAMMAD SALIM	PG	Present	Present
77	2050305 SU	JMIT KUMAR	PG	Present	Present
78	2050306 SH	HWETA KUMARI	PG	Present	Absent
79	1950053 V	IVEK KUMAR	PG	Present	Present
80	1950054 A	NCHAL	PG	Absent	Present
81	1840068 R	AHUL CHAUDARY	UG	Present	Present
82	1840069 S/	AHARSH KUMAR	ÜG	Absent	Present
83	1840070 S		UG	Present	Present
84	1840071 P	ANKAJ KUMAR	UG	Present	Present
85	1840072 P	RAVIN KUMAR	UG	Present	Present
86		MRESH KUMAR	UG	Present	Absent

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list of registered students for MoE/SICI Sponsored 2-Day Workshop on "Nanoelectronics & VLSI"

Sno	Regno	Student	PROGRAM
			Туре
1		SHUBHI SHARMA	UG
2	1740622	APURVA AKRITI	UG
3	1740627	NAVNEET KUMAR RATNAKAR	UG
4		MD. REYAZUDDIN	UG
5		SUJAY KUMAR	UG
6		SACHIN KUMAR	UG
7		UTKARSHA KUMARI	UG
8		PRITY KUMARI	UG
9		MALVIKA ROY	UG
10	1830284	SNEHA JHA	UG
11	1830285	PRIYA PALLAVI	UG
12	1830286	MADHU KUMARI	UĢ
13	1830287	DEEP MALA	UG
14	1830288 S	HARDA KUMARI	ÜG
15	1830289 S	HUBHAM SINHA	ŬĜ
16	1830290 A	JAY KUMAR	ŪG
17	1830291 H	ARSH RAJ	UG
18	1830293 A	TUL MÖHAN JHA	UG
19	1830294 V	IKARSH KUMAR RAO	UG
20	1830295 A	BHIRAJ VERMA	UG
21	1830296 SA	ATYAM KUMAR	UG
22	1830297 SC	DUJANYA KALITA	UG
23		RANJAL GUPTA	UG
24		ANISH KUMAR JHA	UG
25	1830300 PR		UG
26			UG
27			UG
28	1830303 AN		UG
29	1830304 AD		UG
30	1830305 BIT		JG
31	1830306 AN		JG
32			JG
33	1830308 NIT		JG
34	1830310 NIS		
35	1830389 SHE		JG
36	1830394 MD		JG
37	1830851 NID		IG
38		DACHANGANA	IG
39	1930074 SHU	DUIANA	G
40			G
	1920018 BHA	VESH SINGH JADON	G

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41	1931501 SAKSHAM GOPAL SINGH	UG
42	1931502 SHWETANK RAMAN	UG
43	1931503 ALOK KUMAR	UG
44	1931504 ANSHU KUMAR	UG
45	1931505 SONU KUMAR	UG
46	1931506 RICHA RAI	UG
47	1931507 POOJA RANI	UG
48	1931508 VISHAL ANAND	UG
49	1931509 NIKHIL KUMAR CHAUDHARY	UG
50	1931510 SURUCHI	UG
51	1931511 SUVRADIP PAUL	UG
52	1931512 KUMAR SAURAV	UG
53	1931513 AVINASH	UG
54 55	1931514 SHREYA JHA	UG
56	1931515 RUPESH KUMAR THAKUR	UG
57	1931516 APARNA SHIVANI	UG
58	1931517 PRACHI JHA	UG
59	1931518 SALONI RANJAN	UG
60	1931519 SUMIT KUMAR	UG
61	1931520 SHIVANI JHA	UG
62	1931521 ANISH RAJ 1931522 SAUMYA SHREE	UG
63	1931522 SAUMTA SHREE 1931523 GAURAV KUMAR	UG
64	1931525 PRACHI KUMARI	UG
65	1931526 DEEKSHA SHARMA	UG
66	1931527 MANPREET KAUR	UG
67	1931528 SUBHASH KUMAR	UG
68	1931529 AYUSH KUMAR SRIVASTAVA	
69	1931530 GOPAL SINGH SOLANKI	
70	1931531 AKSHAY KUMAR	UG
71	1931534 VIKAS KUMAR	UG
72	1840051 PRADHYUMAN JOSHI	UG
73		UG
74	1840054 PRASHANT KUMAR	UG
75	1840059 UTKARSH YADAV	UG
76	1840061 DHANESH KUMAR	UG
77	2050301 MOHAMMAD SALIM	PG
78	2050305 SUMIT KUMAR	PG
	2050306 SHWETA KUMARI	PG
79	1950053 VIVEK KUMAR	PG
80	1950054 ANCHAL	PG
81	1840068 RAHUL CHAUDARY	UG
82	1840069 SAHARSH KUMAR	UG
83	1840070 SAMEER RAJ	UG
84	1840071 PANKAJ KUMAR	UG
85	1840072 PRAVIN KUMAR	UG
86	1840073 AMRESH KUMAR	UG

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Attendance for 3 days Online workshop on "Cloud Based Simulation Practices for Advanced Virtual Labs using My TaraNG"							
no		Regno	Student	PROGRAM Type	07-02-2021	13-02-2021	14-02-202
	1	1740601	SIMRANJEET SINGH	UG	Present	Present	Absent
	2		KRISHAN BALDEV KAKOYIA	UG	Present	Present	Present
	3	The second se	YASHO VARDHAN SHARMA	UG	Present	Present	Present
	4		SHASHANK SINGH	UG	Present	Absent	Present
	5		VIBHANSHU RAJ	UG	Present	Present	Present
	6		KRISHN KANT VIMAL	UG	Present	Absent	Present
	7		ABHISHEK KUMAR	UG	Absent	Present	Present
	8		SHUBHI SHARMA	UG	Present	Present	Present
	9		APURVA AKRITI	UG	Present	Present	Present
	10		NAVNEET KUMAR RATNAKAR	UG	Absent	Absent	Present
						Present	Present
	11		MD. REYAZUDDIN	UG	Present		Present
	12		SUJAY KUMAR	UG	Present	Present	Present
	13		SACHIN KUMAR	UG	Present	Present	
	14		UTKARSHA KUMARI	UG	Present	Present	Present
	15		PRITY KUMARI	UG	Present	Present	Absent
	16		MALVIKA ROY	UG	Present	Present	Present
	17		I SNEHA JHA	UG	Present	Absent	Present
	18		5 PRIYA PALLAVI	UG	Present	Present	Present
	19		5 MADHU KUMARI	UG	Present	Present	Present
	20		7 DEEP MALA	UG	Present	Present	Present
	2:	1 183028	B SHARDA KUMARI	UG	Present	Present	Present
	22		9 SHUBHAM SINHA	UG	Present	Present	Present
	2		O AJAY KUMAR	UG	Present	Present	Present
	2.	4 183029	1 HARSH RAJ	UG	Present	Present	Present
	2		3 ATUL MOHAN JHA	UG	Absent	Present	Present
	2		4 VIKARSH KUMAR RAO	UG	Present	Present	Present
	2	7 183029	5 ABHIRAJ VERMA	UG	Present	Present	Present
	2	8 183029	6 SATYAM KUMAR	UG	Present	Present	Absent
	2	9 183029	7 SOUJANYA KALITA	UG	Present	Present	Present
	3	0 183029	8 PRANJAL GUPTA	UG	Present	Present	Present
	3	1 183029	9 MANISH KUMAR JHA	UG	Present	Absent	Present
			0 PRERNA	UG	Present	Present	Present
	3	3 183030	1 ADARSH SHUBHAM	UG	Present	Present	Present
	3	4 183030	2 HARSH CHAURASIA	UG	Present	Present	Present
	3	5 183030	3 ANKIT KUMAR	UG	Present	Present	Present
2	3	6 183030	4 ADARSH KARN	UG	Present	Present	Present
	3	7 183030	5 BITTU KUMAR	UG	Present	Present	Present
	3	8 183030	6 ANKIT KUMAR	UG	Present	Present	Absent
	3	9 183030	7 PRAKALP SHARMA	UG	Present	Present	Present
	4	0 183030	8 NITISH SURYAN	UG	Present	Present	Present
	4	1 183031	0 NISHAR TIWARI	UG	Present	Present	Present
	4	183038	9 SHER KHAN	UG	Present	Present	Present
	4	183039	4 MD SALMAN	UG	Present	Present	Present
	4	4 183085	51 NIDHI JHA	UG	Present	Present	Present
	1	184007	4 N V RAGHAVENDRA	UG	Present	Present	Present
	2	16 193007	74 SHUBHAM	UG	Present	Present	Present
	1	17 193007	78 BHAVESH SINGH JADON	UG	Present	Present	Present

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Attendance for 3 days Online workshop on "Cloud Based Simulation Practice	25
for Advanced Virtual Labs using My TaraNG"	

Sno		Regno	Student	PROGRAM			
				_Type	07-02-2021	13-02-2021	14-02-202
	48	1931501	SAKSHAM GOPAL SINGH	UG	Present	Present	Present
	49	1931502	SHWETANK RAMAN	UG	Present	Present	Present
	50	1931509	NIKHIL KUMAR CHAUDHARY	UG			
					Present	Present	Present
	51	1931510	SURUCHI	UG	Present	Present	Present
	52	1931511	SUVRADIP PAUL	UG	Present	Present	Present
	53	1931512	KUMAR SAURAV	UG	Present	Present	Absent
	54	1931513	AVINASH	UG	Present	Present	Present
	55	1931514	SHREYA JHA	UG	Present	Present	Present
	56	1931515	RUPESH KUMAR THAKUR	UG	Present	Present	Present
	57	1931516	APARNA SHIVANI	UG	Present	Present	Present
	58	1931517	PRACHI JHA	UG	Present	Present	Present
	59	1931518	SALONI RANJAN	UG	Present	Present	Present
	60	1931519	SUMIT KUMAR	UG	Present	Present	Present
	61	1931520	SHIVANI JHA	UG	Present	Absent	Present
	62	1931521	ANISH RAJ	UG	Present	Present	Absent
	63	1931522	SAUMYA SHREE	UG	Present	Present	Present
	64	1931523	GAURAV KUMAR	UG	Present	Present	Present
	65	5 1931525	PRACHI KUMARI	UG	Present	Present	Present
	66	5 1931528	SUBHASH KUMAR	UG	Present	Present	Absent
	67	7 1931529	AYUSH KUMAR SRIVASTAVA	UG	Present	Present	Present
	68	3 1931530	GOPAL SINGH SOLANKI	UG	Present	Present	Present
	69	9 1932013	KUMAR VAIBHAV	UG	Present	Present	Present
	70	1932025	RAUSHAN KUMAR	UG	Present	Present	Present
	7	1 1932027	AWANISH SHARMA	UG	Present	Present	Present
	7	2 1932028	PUSHKAR KUMAR SINGH	UG	Present	Present	Present
	7	3 1932034	PRACHI VATS	UG	Present	Present	Absent
	7	4 193203	5 SMITA KUMARI	UG	Absent	Present	Present
-	7	5 193355	1 KM PRIYANKA SINGH	UG	Present	Present	Present
	7	6 205030	1 MOHAMMAD SALIM	PG	Present	Present	Present
	7	7 205030	5 SUMIT KUMAR	PG	Present	Present	Absent
	7	8 205030	6 SHWETA KUMARI	PG	Present	Absent	Present
			3 VIVEK KUMAR	PG	Present		Present
	8	0 195005	4 ANCHAL	PG	Absent	Present	Present

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List of Registered students for 3 days Online workshop on "Cloud Based Simulation Practices for Advanced Virtual Labs using My TaraNG"

		Student	PROGRAM_Type
Sno	Regno	SIMRANJEET SINGH	UG
1	1740602	KRISHAN BALDEV KAKOYIA	UG
2	1740602	YASHO VARDHAN SHARMA	UG
3		SHASHANK SINGH	UG
4		VIBHANSHU RAJ	UG
5	1740012	KRISHN KANT VIMAL	UG
7	1740017	ABHISHEK KUMAR	UG
8	1740020	SHUBHI SHARMA	UG
9		APURVA AKRITI	UG
10	1740622	NAVNEET KUMAR RATNAKAR	UG
11		MD. REYAZUDDIN	UG
12		2 SUJAY KUMAR	UG
13		3 SACHIN KUMAR	UG
14		1 UTKARSHA KUMARI	UG
15		2 PRITY KUMARI	UG
16	-	3 MALVIKA ROY	UG
17		4 SNEHA JHA	UG
18		5 PRIYA PALLAVI	UG
19		6 MADHU KUMARI	UG
20		7 DEEP MALA	UG
2		8 SHARDA KUMARI	UG
2		9 SHUBHAM SINHA	UG
2		0 AJAY KUMAR	UG
2	4 183029	1 HARSH RAJ	UG
2	5 183029	3 ATUL MOHAN JHA	UG
2	6 183029	4 VIKARSH KUMAR RAO	UG
2	7 183029	95 ABHIRAJ VERMA	UG
2	8 183029	6 SATYAM KUMAR	UG
2	9 183029	7 SOUJANYA KALITA	UG
3	18302	98 PRANJAL GUPTA	UG
3	81 18302	99 MANISH KUMAR JHA	UG
3	32 18303	00 PRERNA	UG
	33 18303	01 ADARSH SHUBHAM	UG
	34 18303	02 HARSH CHAURASIA	UG
	35 18303	03 ANKIT KUMAR	UG
		04 ADARSH KARN	UG
	37 18303	05 BITTU KUMAR	UG

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38	182020 1		
39	1030306	ANKIT KUMAR	UG
40	/	PRAKAIDSHARMA	UG
	1030308	NITISH SURYAN	UG
41	1830310	NISHAR TIWARI	UG
42	1830389	SHER KHAN	UG
_ 43	1830394	MD SALMAN	UG
44	1820051		UG
45	1030051	NIDHI JHA	
46	100/4	N V RAGHAVENDRA	UG
47	1220014	SHUBHAM	
48	120010	BHAVESH SINGH JADON	UG
49	1	SAKSHAM GOPAL SINGH	UG
Construction of the local distance	Tantour	SHWETANK RAMAN	UG
50	1	NIKHIL KUMAR CHAUDHARY	UG
51		SURUCHI	UG
52		SUVRADIP PAUL	UG
53		KUMAR SAURAV	UG
54		AVINASH	UG
55		SHREYA JHA	UG
		RUPESH KUMAR THAKUR	UG
57	and an end of the second se	APARNA SHIVANI	UG
58		PRACHIJHA	UG
59		SALONI RANJAN	UG
60	and a statement of the second s	SUMIT KUMAR	UG
6		SHIVANIJHA	UG
6		ANISH RAJ	UG
6		SAUMYA SHREE	UG
6		GAURAV KUMAR	UG
6			UG
6		SUBHASH KUMAR	UG
6	-	AYUSH KUMAR SRIVASTAVA	UG
		GOPAL SINGH SOLANKI	UG
			UG
		5 RAUSHAN KUMAR	UG
		Z AWANISH SHARMA	UG
		8 PUSHKAR KUMAR SINGH	UG
7		4 PRACHI VATS	UG
		6 SMITA KUMARI	UG
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1		5 SUMIT KUMAR	PG
	the second se	6 SHWETA KUMARI	PG
	the second secon	3 VIVEK KUMAR	PG
T	80 195005	4 ANCHAL	PG
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