SANT LONGOWAL INSTITUTE OF ENGINEERING. & TECHNOLOGY,
(Deemed University)
LONGOWAL, DISTT. SANGRUR

Dated 01-09-2017

MINUTES OF MEETING OF BOS FOR M.TECH PROGRAMME
(ELECTRONICS & COMMUNICATION ENGINEERING)

The following committee (internal experts) in the Board of Studies of Departmentof ECE was approved by the Competent Authority and same was notified vide Ref. No. SLIET/ECE/31-32 dated 13.04.2016 to complete the process of finalizing scheme, syllabus of PG, UG and ICD programmes as per outcome based education (OBE) :
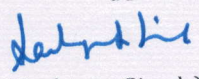
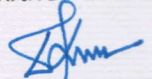|		
1	Dr J.S. Ubhi, HOD (ECE)	Chairperson
2	Dr A.P. Singh, Professor (ECE)	Member
3	Dr Anupma Marwaha, AsP (ECE)	Member
4	Dr Surinder Singh, AsP (ECE)	Member
5	Dr. Ajay Pal Singh, AsP (ECE)	Member
6	Dr. Dilip Kumar, Associate Professor (ECE)	Member
7	Er Sarbjeet Singh, Asstt. Professor (ECE)	Member

A meeting of above committee is held on 01.09.2017 to consider the recommendations dated 24.07.2017 of Departmental Academic Affair Commtitee (DAAC) regarding mapping of COs and POs of all the M.Tech courses and discrepancies between the contents of following courses, reported by the Course Coordinators :
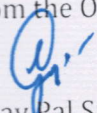
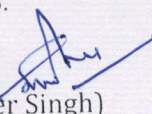| Sr. No	Course	Name of Course Coordinator
1	List of experiments for the course ECP-514	Er. Kuldeep Singh, AP (ECE)
2	List of Experiments for the course EC-817	Dr. Ajay Pal Singh, AsP (ECE)

Since, Dr. Anupma Marwaha, Professor (ECE) has taken over the charge of HOD (ECE), she acted as Chairperson of the Committee.
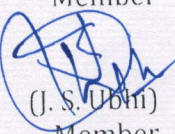
The committee members have verified and endorsed the recommendations of the DAAC and decided to implement /incorporate the changes in the syllabus of courses ECT-514, ECP-514 and EC-817 suggested by the Course Coordinators from the Odd Semester of 2017-18.
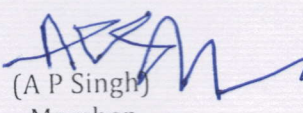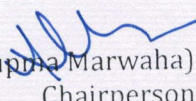
(Sarbjeet Singh)
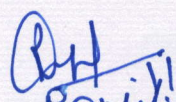Member

(Dilip Kumar)
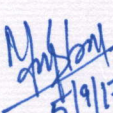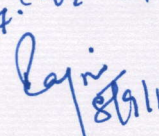Member

(Ajay Pal Singh)
Member

(Surinder Singh)
Member

(J. S. Ubhi)
Member

(A P Singh)
Member

(Anupma Marwaha)
Chairperson

EXTERNAL MEMBERS

(Dr. Ranjit Kaur) Prof., ECE, Pbi Univ. Patiala.

5/9/17 (Dr. Manoj Duhan. Prof, chairman ECE Dept, DCRUST Murthal.

8/9/17 (Dr Rajni), Prof. ECE, SBSSTC, Ferozepur

SANT LONGOWAL INSTITUTE OF ENGINEERING. & TECHNOLOGY,
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DEPARTMENT OF ECE

Ref. No : SLIET/ECE/1846A   Date : 24/7/2017

MINUTES OF DEPARTMENTAL ACADEMIC AFFAIR COMMITTEE (DAAC)

A meeting of Departmental Academic Affair Committee (DAAC) is held on 24.07.2017 at 10.00 AM in the office of HOD (ECE) to discuss following syllabus related matters.

- Approve minor revisions in the CO-PO mapping of M.Tech syllabus (2016 batch onwards)
- Consider the case of discrepancy in the contents of practical course on Electronic Devices & Circuits (EC-514).
- Minor modifications in List of Experiments for M.Tech course Digital Logic Design Lab (EC-817).
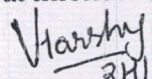
Following were present :

1. HOD (ECE)
2. Dr. J.S. Ubhi, Professor, Department of ECE
3. Dr. Dilip Kumar, Associate Professor (ECE)
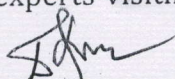4. Er. Vivek Harshey, Assistant Professor (ECE)

The DAAC discussed the correlation criteria in the COs and POs and finalized the mapping of all the M.Tech courses.
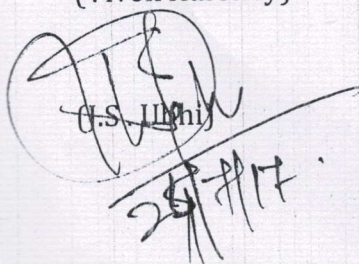
The matter of discrepancy between the contents of theory course (ECT-514) and practical course (ECP-514) has been reported by Er. Kuldip Singh, Asstt. Prof. (copy attached). The same has been discussed in DAAC and the list of experiments for the practical course (ECP-514) has been finalized in consultation with the course coordinator.
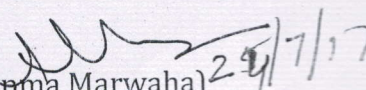
Minor modifications in the List of Experiments for M.Tech course Digital Logic Design Lab (EC-817) have been suggested by the course coordinator Dr. Ajay Pal Singh, Assoc. Prof. (copy attached). As discussed in DAAC, the proposed changes cover the Theory contents and the list of experiments for the practical course (EC-817) is therefore recommended for implementation.

It has also been decided that the cases may be put up before external experts visiting the Department from time to time for recommendations.

(Vivek Harshey)

(Dilip Kumar)

(J.S. Ubhi)

(Anupma Marwaha)

Chairman DAAC/ HOD (ECE)

ECP-514
Electronic Devices & Circuits

L	T	P	Credits
0	0	2	1

Course Objectives	The aim of this lab is give practical exposure to students by analyzing V-I characteristics of different Semiconductor Electronic Devices i.e diodes, Transistors, JFET and MOSFET.	
Course Outcomes	1. Observe and analyze the V-I characteristics of various Semiconductor diodes. 2. Observe and analyze the various modes of transistors in different configurations. 3. Design and analyze of various Electronic circuits. 4. Ability to measure and record the experimental data, analyze the results, and prepare a formal laboratory report.	

Mapping of Course Outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PO13
CO1	√		√	√									
CO2		√	√	√									
CO3		√		√									
CO4				√				√		√			

List of Experiments:
1. Design and analysis of full wave rectifier with and without LC filter.
2. To observe V-I characteristics of PNP & NPN transistor in common collector configuration.
3. To observe V-I characteristics of JFET in common source configuration.
4. To observe V-I characteristic of D- MOSFET.
5. To observe V-I characteristic of E- MOSFET.
6. To observe and analyze V-I characteristic of SCR.
7. To observe and analyze V-I characteristic UJT.
8. To observe and analyze various diode Clamping circuits.
9. To observe and analyze various diode Clipping circuits.
10. To observe and analyze V-I characteristic of Tunnel diode.
11. To observe and analyze V-I characteristic of PIN diode.
12. To observe and analyze V-I characteristic of Schottky Barrier diode.

(Dr Ranjit Leen)
External Expert.

(Dr Manoj Duhan)

Department of Electronics & Communication

Kuldip Singh   Vivek Harshey   I. S. Solanki   Dilip Kumar   L S Likhi