# **Curriculum Integrated Certificate and Diploma Programme**

ICD (DEC-CSME)

In

# **Electronics & Communication Engineering**



# Department of Electronics & Communication Engineering

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## **VISION**

The Department of Electronics & Communication Engineering shall strive to create engineering technocrats for addressing the global challenges in relevant areas to cater the ever changing needs of society at National and International level.

#### **MISSION**

- 1. To ensure dissemination of knowledge through effective teaching and learning in Electronics and Communication Engineering.
- 2. To excel in Research and Development activities in emerging areas.
- 3. To promote industry-institute and institute-institute linkages for sustainable development of academic, research, training and placement activities.
- 4. To establish center of excellence in thrust areas to nurture the spirit of innovation and creativity among faculty and students.



## **Programme Educational Objectives (PEOs)**

The Integrated Certificate and Diploma programme (DEC-CSME) shall produce skilled professionals who are:

- 1. Technically competent in maintenance, service and repair of electronic equipment.
- 2. Effective in communication and capable to work in a team.
- 3. Ethically and socially responsible for the development of country and community.
- 4. Able to demonstrate entrepreneurship skills and lifelong learning for successful career.
- 5. Able to adapt themselves with new technological challenges in relevant field.

#### **Programme Outcomes (POs)**

After successful completion of ICD (DEC-CSME) program, student will be able to:

- 1. Apply technical skill to troubleshoot, repair, service & maintenance of electronic equipment.
- 2. Use knowledge of science and humanities for personality development.
- 3. Demonstrate basic electronics engineering principles and conduct related experiments including programming skills.
- 4. Identify and analyze well-defined electronic engineering problems.
- 5. Use appropriate tools and techniques to solve well-defined electronic engineering problems systematically.
- 6. Assist in the design and development of engineering solutions.
- 7. Demonstrate technical skills in utilizing modern electronic engineering tools.
- 8. Communicate effectively with the engineering community and the society at large.
- 9. Demonstrate awareness for societal, health, safety, legal and cultural issues and the consequent responsibilities for sustainable development.
- 10. Develop entrepreneurship skills.
- 11. Understand professional ethics, responsibilities and norms of electronic engineering practices.
- 12. Function effectively as an individual or in teams with leadership qualities.



# INTEGRATED CERTIFICATE AND DIPLOMA (DEC-CSME)

		Semester-I					
S. No	Sub. Code	Subject Name	L	T	P	Hrs.	Credits
1.	AM-111	Mathematics- I	4	1	0	5	5
2.	PH-111	Physics-I	4	0	2	6	5
3.	CY-111	Chemistry-I	4	0	2	6	5
4.	HU-111	Communication Skills-I	2	0	0	2	2
5.	EE-111	Fundamental of Electrical Engineering	3	0	2	5	4
6.	WS-122	Workshop Practice	0	0	4	4	2
7.	EC-112	Electronic Devices	2	0	2	4	3
		Total	19	1	12	32	26
		Semester-II					
S. No	Sub.	Subject Name	L	T	P	Hrs.	Credits
	Code						
1	AM-121	Mathematics- II	4	1	0	5	5
2	PH-121	Physics-II	4	0	2	6	5
3	CY-121	Chemistry-II	4	0	2	6	5
4	ME-121	Engineering Drawing	0	0	4	4	2
5	EC-121	Digital Electronics	3	0	4	7	5
6	EC-122	Electronic Workshop Practice-I	0	0	4	4	2
		Total	15	1	16	32	24
		Semester-III (A)					
	TP-201	Two Weeks Practical Training during summer vacations				80	S/US



		Semester-III (B)					
S. No	Sub.	Subject Name	L	T	P	Hrs.	Credit
	Code						S
1	HU-211	Communication Skills-II	1	0	2	3	2
2	CS-216	Computer Fundamentals	3	0	2	5	4
3	EC-213	Electronic Measurements & Instrumentations	3	1	2	6	5
4	EC-214	Analog Communication	3	1	2	6	5
5	EC-217	Consumer Electronics	3	1	0	4	4
6	EC-218	Troubleshooting of Electronics Equipment-I	0	0	4	4	2
7	MC-211	Moral values and Professional ethics	1	0	0	1	0
	•	Total	14	3	12	29	22
		Semester-IV					
S. No	Sub.	Subject Name	L	T	P	Hrs.	Credit
	Code						S
1	AM-221	Applied Mathematics	3	1	0	4	4
2	EC-223	Fundamentals of Microprocessor & Microcontroller	3	1	4	8	6
3	EC-226	Computer Programming & Application	3	0	2	5	4
4	EC-227	Service & Maintenance of Computer	2	0	4	6	4
5	EC-228	Network Theory	3	1	0	4	4
6	EC-229	Troubleshooting of Electronic Equipment-II	0	0	4	4	2
		Total	14	3	14	31	24
		Semester-V(A)					
	TP301	Four Weeks Industrial Training during summer vacations				160	



		Semester-V (B)										
S. No	Sub Code	Subject Name	L	T	P	Hrs.	Credits					
1	MC-311	Environmental Studies	2	0	0	2	2					
2	HU-311	Entrepreneurship	2	0	0	2	2					
3	EC-311	Electromagnetic Field Theory	3	0	0	3	3					
4	EC-312	Linear IC's & Application	3	1	4	8	6					
5	EC-313	Digital Communication	3	1	2	6	5					
6	EC-314	Electronic Workshop Practice-II	0	0	4	4	2					
7	EC-315	Principles of Microwave Engineering	2	1	0	3	3					
8	TP-301E	Industrial Training					S/US					
		Total	15	3	10	38	23					
		Semester-VI										
S. No	Sub Code	Subject Name	L	T	P	Hrs.	Credits					
1	EC-321	Industrial Electronics	3	1	4	8	6					
2	EC-322	Wireless & Mobile Communication	3	0	0	3	3					
3	EC-323	Microprocessor & Microcontroller Applications	3	1	2	6	5					
4	EC-324	Antenna Wave Propagation	3	0	0	3	3					
5	EC-325	Microelectronics	3	0	2	5	4					
7	EC-327	Project	0	0	4	4	2					
		Total	15	2	12	29	23					
	Total Theory & Practical Load for Diploma 92 13 76 181 142											

Note:		The required credits for certificate pr	rogramme					96			
		The required credits for ICD program	nme					142	2		
		Maximum courses in one semester						7			
Maximum Contact Hrs. 32											
The common courses and their credits are fixed for all ICD											
programmes.											
Course	es offered	to other Departments:									
S. No	Sub Coo	le Subject Name		L	T	P	Hrs.	Credi	ts		
1	EC-211	Fundamental of Electronics		3	0	2	5	4			
	Engineering										
2	EC-221	Fundamental of Electronics		3	0	2	5	4			
Engineering											



					Fi	EC-1		ng.							
				L	151		T		P		Credits				
				2			0		2		3				
		Sessio	nal Ma	ırks		•					50				
		End S	Semeste	r Exami	ination [	Marks					50				
	Course  Objectives:  The course intends to provide the basic concept and characteristics of the electronics devices such as diode, BJT, FET, etc. Also aims to provide the understanding application of different electronics devices and simple circuits.  Course  1. To acquire knowledge about semiconductor physics for intrinsic and extrinsic														
Course		2. 3.	This co Acquir source Able t	als. ourse giv ed knov s. o under	ves an over whether the whole when the whole when the whole whether the whole when the whole whole when the whole who	verview about ac	of vario	ous semio d passiv	conductor re compo		tage and	current			
			Map	ping of	course o	outcome	es with p	rogram	outcome	es					
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12			
CO1	, ,														
CO2	√	√													
CO3		√,	,	V	ļ , , , , ,	,									
CO4															
				U	nit-T							6 hrs.			

**Introduction:** Classification of materials into conducting and insulating materials through a brief reference to atomic structure, Conducting Materials, Insulating Materials, Semi-conductor Material.

<u>Unit-II</u> 8 hrs.

**Active and Passive Components:** Introduction to active and passive components; fixed and variable resistances, their various types fixed and variable capacitors, their various types and important specifications and color codes.

**Voltage and current sources** – concept of constant voltages and constant current sources, symbol and graphical representation, characteristics of ideal and practical sources.

Unit-III 10 hrs.

**Semiconductor Diodes:** Atomic structure of Germanium and Silicon semi-conductors; intrinsic and extrinsic semiconductors, PN junction, basic principles of operation and VI characteristics of PN junction diode, static and dynamic resistance of a diode. Use of a diode in rectifiers, half wave, full wave and bridge rectifier with shunt capacitor filter, series inductor filter, zener diode and its applications, as a voltage regulator, light emitting diode (LED), liquid crystal display (LCD).



	<u>Unit-IV</u>		8 hrs.										
<b>Transistors:</b> Introduction to a	transistor, working of a PNP and	l NPN tran	sistor, input and output										
characteristics, transistor configura	characteristics, transistor configurations, biasing of a transistor, amplifying action of a transistor, comparison												
of different configurations, common emitter amplifier circuit, load line, concept, field effect transistor FET,													
JFET, MOSFET, their characteristics and applications, unijunction transistor (UJT).													
RECOMMENDED BOOKS													
Title	Author	Publisher											
1. Basic Electronics and Linear	N N Bhargava and Kulshreshta	McGraw	Hill										
Circuitis													
2. Electronics Devices and	Miliman and Halkias	McGraw	Hill										
Circuits													



	Electronic Devices Lab															
Course	e	To rei	nforce	learning	throug	h hand	s-on ex	perience	by ex	amining	the el	ectrical				
Object	tives:									es, BJTs						
		provide	the stud	ent with	the capa	bility to	measure	and reco	ord the ex	kperimen	tal data,	analyse				
					micondu											
Course	e	1.	To unde	erstand t	he funct	ioning o	f variou	s electro	nic instr	uments li	ke CRO	, signal				
Outco	mes:				ultimete											
		2.	To unde	erstand tl	he charac	cteristics	of diod	e and BJ	T and ve	rify their	response	es.				
		3.	To cons	truct var	ious elec	tronic ci	rcuits or	the brea	nd board a	and analy	ses their	output.				
			Mappi	ing of co	ourse out	tcomes v	with pro	gram ou	itcomes							
	PO1	PO2														
CO1																
CO2																
CO3																
		List of Experiments:  1. To measure values of different resistors by using the color coding chart. 2. To calculate the value of various passive components using multimeter. 3. To observe the front panel of CRO. 4. To observe the front panel of signal generator. 5. To observe the VI characteristics of semiconductor diode in forward bias. 6. To observe the VI characteristics of zener diode in reverse bias. 7. To verify the npn and pnp transistors. 8. To construct half-wave rectifier, wave shape of the electrical signal and calculate its ripple factor. 9. To construct full-wave rectifier, wave shape of the electrical signal and calculate its ripple factor.														



	EC-121 Digital Electronics													
				L			T		P		Cre	edits		
				3			0		4			5		
		Session	nal Mar	·ks								50		
		End So	emester	Examin	ation M	arks					4	50		
		This c	ourse w	ill provi	de the	introduc	tion of t	the basic	princip	oles, cha	racteristi	ics and		
		operati	ons of a	digital s	ystem. I	Next foc	us is to g	give the	detail de	escription	about E	Boolean		
Course		algebra	and the	various	methods	of Boole	ean funct	ion redu	ction, de	signing o	f combir	national		
Object	Objectives: circuits by using logic gates, design and analyses of asynchronous and													
	sequential Circuits using flip flops and at last to understand principle of opera													
	resistors and D/A an A/D converters.													
		1. Lear	n to appl	y Boolea	ın laws/I	K-Map-n	nethod m	ethod to	reduce a	given Bo	oolean fu	inction.		
		<b>2.</b> Able	e to des	ign & re	ealize co	ombinati	onal log	ic circui	ts using	logic ga	ites for	various		
Course	•	pract	ical appl	ications.					_					
Outco	mes:	3. Able	to demo	onstrate t	he opera	ation of 1	lip-flops	, counter	s and sh	ift registe	ers.			
		<b>4.</b> Able	to unde	rstand va	arious A	/D and I	O/A conv	erters.						
			Mappi	ng of co	urse out	tcomes v	with prog	gram ou	tcomes					
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1		$\sqrt{}$		V		V								
CO2		$\sqrt{}$			$\sqrt{}$	V								
CO3		V	V	V	$\sqrt{}$	V								
CO4	$\sqrt{}$	V	V			V								
	-	•		•	<u>Unit-I</u>		•	•	•			10 hrs.		

**Introduction:** Basic difference between analog and digital Signals, applications and Advantages of Digital Signals, digital systems and their application

**Review of number system:** Decimal, Binary, Octal, and hexadecimal number system and their interconversions Signed and unsigned number, Binary operations-addition; Subtraction, Multiplication and division; Excess 3 code, Gray code and ASCII code

Unit-II 12 hrs

**Logic gates:** Definitions, symbols and truth table of NOT, OR, AND, NAND, NOR, XOR, XNOR gates, De-Morgan's theorems, realization of basic gates using universal gates; realization of simple Boolean equations using universal gates, introduction to k-map (up to 4 variables)

**Combinational Circuits:** Combinational circuit design, adders, substractor, code converters, multiplexers, demultiplexer, encoders and decoders.

Unit-III 16 hrs.

**Sequential Circuits:** Introduction, Logic diagram, truth table, timing diagram and operation of following latches and flip flops, NOR latch, NAND latch, RS, T, D, and JK, Master / Slave JK flip flops. Operation using waveforms and truth tables of RS, T, D, and Master/Slave JK flip flops.

**Counters:** Introduction to Asynchronous and Synchronous counters Binary counters Divide by N ripple counters, Up/down counter, Ring counter with timing diagram,

**Shift Resistors:** Introduction, Serial in parallel out, serial in serial out, parallel in serial out, parallel in parallel out



<u>Unit-IV</u>			10 hrs.							
A/D and D/A converters: Binary Weighted D/A	converter, R/2R ladder D	A converter, St	air step Ramp							
A/D converter, Dual Slope A/D converter, Success	sive Approximation A/D C	onverter								
RECOMMENDED BOOKS										
Title	Author	Publisher								
1. Fundamentals of Digital Electronics	A. Anand Kumar	PHI 2 <sup>nd</sup> Editio	n							
2. Digital Electronics	R P Jain	McGraw Hill Edition	Education 4 <sup>th</sup>							
4. Digital Logic Designs	Morris Mano	PHI 5 <sup>th</sup> Edition	n							
5. Digital Systems: Principles and Applications	R J Tocci	PHI 10 <sup>th</sup> Edition	on							



	Digital Electronics Lab  Course To make students familiar with different types of designs as sequential logic circuits,															
Course		To mal	ke stude	nts fami	liar with	n differe	ent types	of desi	gns as se	equentia	l logic c	ircuits,				
Object	ives:	combin	national	logic ci	rcuits, t	rouble s	shooting	of vari	ous digi	tal syste	ms & st	udy of				
		various	s digital	system	s. Kno	wledge	of basi	c electr	onics &	digital	technic	ques is				
		useful	in under	standin	g theory	and pr	actical c	of the su	bject.							
Course										ke deco						
Outcom	mes:			and de-n	nultiplex	ers as w	ell as arit	hmetic o	circuits (l	nalf adde	r, full ad	der and				
			plier).					. 121 (2		• .						
			•	_	•	_				registers						
			erstand tr testabilit		tance an	a neea i	or verm	cation, to	esting of	digital l	ogic and	design				
		101			urse out	tromes v	with nro	gram Al	ıtcomes							
	DO1	PO2														
CO1	<b>PO1</b> √	PO2   √	PO3	<b>PU4</b> √	<b>PU3</b> √	<b>PO</b> 0 √	PO7	PU	PU9	POIU	POII	PO12				
CO2	٧	1	V V V V													
CO3	V	V														
			<u> </u>													
		List of	List of Experiments:													
			-		interpre	tation of	truth tab	les for A	ND, OR	, NOT N	AND, N	OR and				
		Е	xclusive	OR (EX	OR) and	l Exclusi	ve NOR	(EXNO	R) gates.							
									_	NOR gate	s.					
					_			_		rification		eration.				
		4.	Constru	ction of	a full a	ıdder cir	cuit usin	g XOR	and NA	ND gate	s and ve	erify its				
			peration.					C		C		·				
		_	_	gn a NO	R Gate L	Latch and	d verifica	tion of i	ts operat	ion.						
									•	tive edge	triggere	d, level				
						•	_	-	_	p-flop, JI						
				_	_				-	ux and D	_	,				
										ising JK/		ops and				
							,		0	υ	•	1				
			verification of their operation.  9. To design a 4 bit ring counter and verify its operation.													
			O. Async	_	_		, - J -	- F	•							
			•				L (a) divid	de by 2	(b) divid	e by 10 (	Counter					
			00001		J. CHAIV		_ (\a) \a.v.	, <u>-</u>	(2) 3.714	,	20011101					



				Elec	ctronics	EC-122 worksh		tice-I				
			I			Т		F	•		Credits	
			0			0		4			2	
Course	tives:	calibrat principl	e and m	nonitor a ld applic	a variety ations.	of elec	tronic in	nstrumer	nts so as	logy and to appl	y meası	ırement
Course		2. To	recogni pacitors, design a	ze and to diodes, and impl	est vario transisto ement el	us active rs etc. lectronic	and pas	sive elec	etronic co	timeter, omponen		
		_				tcomes v			1		1	1
961	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	N	V	V									
CO2	√ √	√	√ √			1						
		3. 4. 5. 6. 7. 8. 9. 10.	various to check Study of basic el Transist Testing compon To stud Solderi electron To sold To use a To find To stud To stud To stud Assembly for asse a. Half various check the study of t	electronic the frectors, IC's of electronic tors, IC's of electronic to	comports etc.) and ctronic setc.) and ctronic mg multing sualize the tice: Circonents. It base on the tion out and or plifying simple eletter circitier circ	mponents and ampliments and identification of the solder recuit associated a general avoltage commonutput V-1 characters.	itude of a ts: This d circuit ication of ents: The Active aring kit a embling all purpose regulate emitter of characteristics of a circuits and with	of CRO a signal topic co symbol of compo his topio nd passiv nd vario practice se PCB. or. configura eristics of f NPN and se: This to	waveform overs the s (Resist onent value c covers we compo us solder using pro- ation. of common of common of common of covers	familiar ors, Cap ues. how to	ization cacitors, test eleautions. cruit boa	ocedure of some Diodes, ectronic and with uration.

c. Simple LED flashing circuit using Transistors / ICs

d. DC regulated power supply.



	TP-201													
	Two Weeks Practical Training during summer vacations													
Course	Course Students have to undergo two – week practical training in Department of Electronics &													
Object	<b>ojectives:</b> Communication Engineering so that they become aware of the practical application of													
	theoretical concepts studied in the class rooms.													
Course	e	1. Gai	n experi	ence in	various o	domains	such as	hardwa	re, softv	vare, mair	itenance a	ınd		
Outco	mes:	testing	Ţ <b>.</b>											
			Map	ping of	course o	utcome	s with p	orogran	ı outcon	nes				
	PO1   PO2   PO3   PO4   PO5   PO6   PO7   PO8   PO9   PO10   PO11   PO12													
CO1	V	V				V								



						EC-213						
		1	Elec	<u>etronic I</u>	Measure	ments a	nd Instr	umenta	tions		~ 11.	
				<u></u>			<u>T</u>	1	,		Credits	
		- ·		3			1	2	2		5	
			al Marl		3.5						50	
			mester I								50	
Course										ndards. It		
Object	ives:						ridges. I	t discuss	ses the C	CRO in d	etail. Fii	nally, it
			ces signa									
Course										suremen	ts.	
Outcor	nes:						ents and	_	•			
		<b>3.</b> Able					nerators					
	DO1	DO2					vith prog	í –		DO10	DO11	DO12
001	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	√ √	1	2	2	2							
CO2		2/	√ √	٧	2/							
COS	V	V	V	Un								12 hrs.
of statis	stical an	nent, galv ter syster	vanomete ns, digit	<u>Uni</u> er, DC ar al multi	t-II nmeter, meters,	DC volti	meter, se	ries ohm	meter.	AC electronic wheatsto	ronic vol	12 hrs. tmeter, ge, low
		surement		esistance	e measu	ring inst	ruments.	.AC bric	lge theo	ry, capa	citance l	oridges,
Inducta	ince brid	dges, Q m	ieter.	Unit	+ TTT							12 hrs.
CRO	CRT w	ave form	display			race osc	illoscope	measiii	ement o	f voltage		
		, oscillos					_			i voitage	, meque	icy and
F	,	,	- P - P - S	Unit		~F		r				12 hrs.
	analyze	tor and a		Signal g lyzer and	generator d distorti ECOMN	on. Rec		X-Y reco				
		nstrument	ation and		id A. Be	11				PHI , Nev	w Delhi	
	rements	ion annem	anon an		10 / 1. DC	11			008.	111,110		
		<b>l</b> easurem	ents and	Oliv	er and C	lage			MH, 20	09.		
	nentation								,0			
	suremer			Alar	ı S. Mor	ris		Е	lsevier (	Butterwo	orth	
Instrun	nentatio	n Principl	les							n), 2008		



			Electr	onic Me	asuremo	ents and	Instrun	nentatio	ns Lab				
Course			oduce the							entation r	equireme	ent as a	
Course	9	1.	Underst	and basi	c measu	rement c	oncepts.						
Outcor	mes:	2.	Able to	measure	the valu	es of inc	luctance	, resistan	ice and c	apacitan	ce using	various	
			bridges							-	_		
		3.	Able to	operate s	signal ge	nerators	and sign	al analy	zers for e	electronic	measure	ements.	
	Mapping of course outcomes with program outcomes												
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1			√			$\sqrt{}$							
CO2	V			V									
CO3		$\sqrt{}$											
		1. 2. 3. 4. 5. 6. 7. 8.	Measure Measure Measure To find To study recorder To study data accorder Displace To mea	erve the verve the comment of the ement of t	Iynamic Inducta I small ref Capacit I medium oil by a serve the referve the system. easurem the temp	recording need by Mesistance of the resistance of the resistance reserves reserved acquisition acquisition to the recording acquisition acquisiti	g of difference with conance of difference of difference of days at LVDT, of sold-	erent signs bridge. Celvin's Tring Brid the help method arent signs ta from	nals on on the strain garant verification gara	at stone be by it by us sensors couge base p and cap thermoc	ridge. sing Q-m on magne d transdu	neter.  tic tape  ucer on  pick up.	



					Analog	EC-214							
				L	Allalog		meanon T	I	)		Credits		
				3			1	2	2		5		
		Sessio	nal Ma	rks		•		•			50		
		End S	Semester	ster Examination Marks 50									
Course	2	The r	nain foci	us of the	course is	s on und	erstandir	ng the im	portance	e and the	ories of a	analog	
<b>Object</b>	<b>iectives:</b> communication systems. The students will understand the various analog												
	communication techniques, AM, FM generation, detection, transmission and reception												
	methods, analog pulse modulation techniques.												
Course	<u> </u>	<b>1.</b> To	o gain kn	owledge	about th	ne funda	mental co	oncepts	of variou	ıs analog			
Outcor	=	communication systems.											
			o study tl										
		1	cquire kr	_					and rece	eption.			
		<b>4.</b> To	o study v										
								gram ou				r	
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1	V	V	V	V									
CO2	V	V	V	V		, , ,							
CO3		V	V	, ,		V							
CO4													
				Un	<u>it-I</u>							12 hrs.	

**Introduction:** Communication, information, Message and Signals, Electromagnetic Spectrum, Classification of signals, Periodic and non-periodic signals, Analog and digital signals, Deterministic and random signals, The elements of a communication system, Modulation, Definition, Types of modulation, Need for modulation.

**Amplitude Modulation:** Definition, Expression of AM wave, modulation index, frequency, spectrum, bandwidth, power contents of sidebands and carrier.

Unit-II 14 hrs.

**Frequency Modulation:** Modulation index, frequency deviation, frequency spectrum and bandwidth of FM wave, Power contents in FM, Phase modulation.

**Generation of AM and FM Waves:** DSB-SC, DSB-SC, SSB-SC, their comparison and areas of applications, Basic principle of AM generation, Generation of DSB and SSB signals. Basic principle of FM generation, Varactor diode modulator.

Unit-III 12 hrs.

**Radio Transmitter and Receiver:** Block diagram of AM and FM transmitter, Working principle with block diagram of AM and FM receiver (Superhetrodyne).

**Demodulation:** AM diode detection, envelope detector, FM detection, basic principle of slope detection, balanced slope detector.



Unit-IV	7	10 hrs.									
Pulse Modulation: Sampling process, Sam	pling theorem, Basic idea abou	t PAM, PWM and PPM and									
typical applications, Reconstruction of message, Pulse code modulation.											
RECOMMENDED BOOKS											
Title Author Publisher											
1. Electronic communication systems	Kennedy	Tata McGraw Hill									
2. Electronic Communications System:	by Wayne Tomasi	5 <sup>th</sup> Edition, Pearson									
Fundamentals Through Advanced,		Education									
3. Electronic communications	Roddy and Coolen	Prentice Hall of India									
4. Principles of communication systems	Taub and Schilling	Tata McGraw Hill									



				<b>A</b> -	1 C -		4º T	-1-						
<b>C</b>		This lat	: 4.				cation L		1: 66 4	AM and	EM	114		
Course									miereni	AM and	FWI IIIOC	lulation		
Object							forms on		C	CD O				
Course		1.					emodulat	ted wave	etorms or	n CRO.				
Outcom	mes:		Generat					•				111		
		3.					receiver	and mea	asuremer	nt of its	paramete	ers like		
			sensitivity and selectivity.											
	Mapping of course outcomes with program outcomes    DOI   DO2   DO3   DO4   DO5   DO6   DO7   DO8   DO10   DO11   DO12													
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1			$\sqrt{}$	$\sqrt{}$	$\sqrt{}$									
CO2			$\sqrt{}$											
CO3														
		1. 2. 3. 4. 5. 6. 7. 8.	To obta modula To obse Genera To obse To genera To obse parame To obse detecto To obse	erve ampain Ampation.  erve envertion of I tion of serve free erate a H ly superters vizuerve the	velop de DSB-SC Single si quency i FM Sign heterod sensitiv wavefo	tector for signal de band and and all and all and with and with and arm of d	or demo- using bat signal. tion and measure I receive selective emodula	dulation alanced its wav depth of and notice its.	n of AM modula reform of modula neasured A signal	ne depth signal. tor.	receiver	f ratio		



						EC-217								
					Consu	_	ctronics							
			]	L			T	I	•		Credits			
				3			1	(	)		4			
		Session	al Marl	ks							50			
		End Se	mester l	Examina	tion Ma	arks					50			
Course	e	The co	urse ain	ı is to i	ntroduce	studen	ts about	working	g princi	ole of va	arious t	pes of		
Object	ives:									he studen				
										about sat		_		
			TV is given. At last working principle, block diagram, main features of consumer electronics											
		gadgets/goods/devices have been presented.												
Course	e					•		nicropho	nes and	loudspea	kers.			
Outcor	me:		•		•		ddress sy	•		1				
					•		•		metic ta	pe record	ling and	ontical		
		<b>5.</b>	•		•	•	arious V	_		pe recore	ing and	optical		
		4		•		•				'1 I CD	LED	1 1115		
		4.		and mer	its and d	emerits (	of variou	s Displa	y I V´s I	ike LCD	, LED a	nd HD-		
			TV's.											
	1	T.				1	vith prog	f		1		1		
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1	√	V	V											
CO2		$\sqrt{}$												
CO3			V											
CO4														
	•			Un	it-I		•			•		12 hrs.		

**Audio System:** Microphone, Construction, working, principles and application of microphone: carbon, moving coil. velocity, crystal, condenser type, cordless microphone, loud speakers, direct radiating ,horn loaded, woofer, tweeter, mid-range, multi-speaker system, baffles and enclosures.

Unit-II 12hrs.

**Sound Recorder:** Sound Recording on magnetic tape, its principles, block diagram and tape transport mechanism, Digital sound recording on tape and disc, CD system and DVD Format, CD/DVD players and recorders, Hi-Fi system, pre-amplifiers, amplifiers and equalizers, Stereo amplifiers.

Unit-III 12 hrs.

**Satellite TV and Cable TV:** Principles of satellite TV system ,Frequency allocation of S,C and KV band ,up link and down link frequencies .Block diagram and working principle of TVRO receiver(TV receiving only), Cable TV networks, master distribution amplifier, line amplifier. Distribution component (Tap-off splitter, Termination etc.)



<u>Unit-IV</u>	12 hrs.
VCR: Principle of video recording on magnetic tape, block diagram of VC	R, VHS tape, transport
mechanism, Basic block diagram, working principles and application of Digital v	watch /clock, Calculator,
Washing machine, Microwave ovens, Cordless telephones, Mobile handset, Digital	camera, DTH, Electronic
ignition system for automobiles.	

	RECOMMENDED	BOOKS
Title	Author	Publisher
1. Audio Visual Systems	Sanjay Attri.	BPB Publishers New Delhi
2. Audio Video Systems	R.G.Gupta	TMH, New Delhi India



						EC-218								
			Troub	leshoot	ing of E	Clectron	ic Equi	ipment-	Ι					
			L			T		P			Credits			
			0			0		4			2			
Course			,		_			•		lge of va				
Object	ives:									roduce th				
									_	dgets/goo ems like,				
										ities of a				
					on in a sy				• • • • • • • • • • • • • • • • • • •	01 01	35 <b>411</b> 10111	.8, 14411		
Course	e							eaker, m	icrophon	e and au	dio-amp	lifiers.		
Outcor	mes:									of var				
		equ	ipment/g											
				ng of co	urse out	comes v	vith pro	gram ou	tcomes					
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1	7	<b>V</b>	V	1	,	√								
CO2	V	<b>V</b>	V	V	V									
		List of	Experi											
		1. To plot the directional pattern of a loudspeaker.												
		2. To plot the directional pattern of a moving coil microphone.												
		3. To study the block diagram and working of an audio tape recorder.  4. Foult Finding in Tone Transport Machanism of a Cossette Tone Recorder.												
		<ul><li>4. Fault Finding in Tape Transport Mechanism of a Cassette Tape Recorder.</li><li>5. To study &amp; plot the frequency response of Audio Amplifier.</li></ul>												
		5.		_	_	•	_		_					
		6. To study & plot the frequency response of cross-over network used in stereo-amplifier.												
		7.	•		ne worki	ng of D7	ΓH syste	m.						
		8.			ne worki	_	-							
		9.			ne worki	_								
		10.	To unde	erstand tl	ne worki	ng of dig	gital cloc	k and fir	ne out the	e faults.				
						-		of a VCI						
		12.	To mea	sure the	AC/DC	voltage	at diffe	erent poi	int in Di	ifferent s	section o	of VCR		
			trainer.											
		13.	To mea	sure the	wavefori	m at diff	erent po	int in Dif	ferent se	ection of	VCR tra	iner.		
							_	/CR train						
		15.	To stud	y the blo	ck diagra	am and v	working	principle	of mob	ile traine	r.			
		<ul><li>15. To study the block diagram and working principle of mobile trainer.</li><li>16. To measure the AC/DC voltage and waveform at different point in Different section</li></ul>												
			of mobile trainer.											
		17.	To find	out fault	in differ	rent sect	ions of n	nobile Tı	rainer.					



						EC-211	1								
				Fundan	nentals o	f Electr	ronics E	ngineeri	ng						
				L			T		P		Credits				
				3			0	2	2		4				
		Sessio	nal Ma	rks							50				
		End S	emester	Exami	nation M	<b>Iarks</b>					50				
Course Object		device	The course intends to provide the basic concept and characteristics of the electronics devices such as diode, BJT, FET, etc. Also aims to provide the understanding application of different electronics devices and simple circuits.												
Course	e	1. To acquire knowledge about semiconductor physics for intrinsic and extrinsic													
Outcor	mes:		materia												
		2. This course gives an overview of various semiconductor devices.													
		3. Acquired knowledge about active and passive components, voltage and current													
			sources								_				
		4.					g princij	ples of e	electroni	c circuits	e.g. Re	ctifiers,			
					ed circui										
	T				urse out						T				
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12			
CO1		V	, ,	V											
CO2	√	V	V												
CO3				√											
CO4		$\sqrt{}$		√		$\sqrt{}$									
				Uni	it-I							8 hrs.			

**Introduction:** Classification of materials into conducting and insulating materials through a brief reference to atomic structure, Conducting Materials, Insulating Materials, Semi-conductor Material.

<u>Unit-II</u> 10 hrs.

**Active and Passive Components:** Introduction to active and passive components; fixed and variable resistances, their various types fixed and variable capacitors, their various types and important specifications and color codes.

**Voltage and current sources** – concept of constant voltages and constant current sources, symbol and graphical representation, characteristics of ideal and practical sources.

Unit-III 14 hrs.

**Semiconductor Diodes:** Atomic structure of Germanium and Silicon semi-conductors; intrinsic and extrinsic semiconductors, PN junction, basic principles of operation and VI characteristics of PN junction diode, static and dynamic resistance of a diode. Use of a diode in rectifiers, half wave, full wave and bridge rectifier with shunt capacitor filter, series inductor filter, zener diode and its applications, as a voltage regulator, light emitting diode (LED), liquid crystal display (LCD).



	<u>Unit-IV</u>		14 hrs.								
<b>Transistors:</b> Introduction to a	transistor, working of a PNP and	l NPN tran	nsistor, input and output								
characteristics, transistor configurations, biasing of a transistor, amplifying action of a transistor, comparison											
of different configurations, common emitter amplifier circuit, load line, concept, field effect transistor FET,											
JFET, MOSFET, their characteristics and applications, unijunction transistor (UJT).											
RECOMMENDED BOOKS											
Title	Author	Publisher									
1. Basic Electronics and Linear	N N Bhargava and Kulshreshta	McGraw	Hill								
Circuitis											
2. Electronics Devices and	Miliman and Halkias	McGraw	Hill								
Circuits											



			Fu	ndament	tals of E	lectroni	cs Engin	eering l	Lab				
Course	2	To rei	nforce	learning	through	n hands	on exp	perience	by ex	amining	the el	ectrical	
Object	ives:	characte	eristics of	of variou	is semic	onducto	r device	s, such	as diode	es, BJTs	and FE	Ts. To	
_		provide	the stud	ent with	the capal	bility to	measure	and reco	rd the ex	perimen	tal data,	analyse	
				rious sen						•		•	
Course	9	1.	To unde	erstand tl	he functi	oning of	f various	electron	nic instru	ıments li	ke CRO	, signal	
Outcor	nes:			or and m									
		2.	_				of diode	and BJ	Γ and ve	rify their	response	es.	
	3. To construct various electronic circuits on the bread board and analyses their output.												
	Mapping of course outcomes with program outcomes												
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1	$\sqrt{}$												
CO2			$\sqrt{}$			$\sqrt{}$							
CO3	$\sqrt{}$		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \										
		1. 2. 3. 4. 5. 6. 7. 8.	List of Experiments:  1. To measure values of different resistors by using the color coding chart. 2. To calculate the value of various passive components using multimeter. 3. To observe the front panel of CRO. 4. To observe the front panel of signal generator. 5. To observe the VI characteristics of semiconductor diode in forward bias. 6. To observe the VI characteristics of zener diode in reverse bias.										



	EC-223 Fundamentals of Microprocessor & Microcontroller											
	Fundamentals of Microprocessor & Microcontroller											
		L         T         P         Credits           3         1         4         6										
			3			1		4			6	
		al Mark									50	
	End Se	mester l	Examina	ation Ma	ırks						50	
Course	The obj	ective of	the cou	rse is to	expose to	the stud	dents to t	he evolu	tion of m	icropro	cessors,	
<b>Objectives:</b>	the arcl	e architecture and instruction set of typical 8-bit microprocessor 8085. It also deals with										
	Assemb	Assembly Language Programming and input-output techniques. Next focus is to introduce										
	the architecture, programming and interfacing of 8051 microcontrollers.											
Course 1. Understand the evolution of computers.												
Outcomes:	1											
		•			ic inter (	oos inc	торгосса	ssor and	0031 11110	Tocontic	101	
	its various applications.											
3. Apply the programming techniques in designing simple assembly language programs												
	for solving simple problems by using instruction sets of microprocessor and											
	microcontroller.											
	<b>4.</b> Use							0 1	ogram ef	ficiently	٧.	
		Mappi	ng of co	urse out	comes v	vith pro	gram ou	tcomes				
PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1	V		V	V								
CO2	√	√	√									
CO3	√	V	$\sqrt{}$	V								
CO4	√											
				<u>Unit-I</u>							14 hrs.	
<b>Introduction</b> :							m and f	functions	of its	various	blocks,	
Microprocesso							D 1			005 C		
Introduction												
block diagram	01 8085,	Tunction	s or eacr	Unit-II		cnitectu	re, pin a	etans of	8085 and	related		
Memories and	d I/O Int	orfooina	· Momo			noncont :	of momo	rr, moss	ing next	itioning	8hrs.	
memory space		_		•		•		• • •	~ .	_		
RAM, ROM, I		•	-		, шаррес	ı ı/O alic	i memor	у шарре	u 1/O. D	asic COI	icept of	
107 1171, 100171, 1	ICONI, E	i KOIVI a	nu DDI	Unit-II	T						12hrs.	
Programming using 8085 Microprocessor: 8085: 8085 programming model, brief ideas of machine and												
assembly languages, machines and mnemonic codes, basic idea of instruction format and addressing modes,												
0			-				_					
	uages, ma	chines a	nd mnen	nonic cod	des, basi	c idea of	instructi	on forma	at and add	dressing	modes,	



Introduction: Difference between Microprocessor & Microcontroller, Concept of Embedded System.

Architecture of 8051 Microcontroller: Architecture of 8051, I/O ports in 8051, basic concept of memory in 8051, basic idea of addressing Modes in 8051, basic idea of instructions in 8051, applications of microcontroller.

RECOMMENDED BOOKS

Title

Author
Publisher

1. Microprocessor Architecture- Programming & Ramesh S Gaonkar

5th Edition, Penram

RECOMM	ENDED BOOKS	
Title	Author	Publisher
1. Microprocessor Architecture- Programming &	Ramesh S Gaonkar	5th Edition, Penram
Applications with 8085/8080A		International Publishing
2. Introduction of Microprocessors &	Ram B	4th Edition, Dhanpat Rai
Microcomputers		Publisher (P) Ltd.
3. The 8051 Microcontroller	Kenneth J. Ayala	3rd Edition, Cengage
		Learning, 2004



		]	Fundam	entals of	f Microp	rocesso	r & Mic	rocontr	oller La	b				
Course		The obj	he objective of this lab is to familiarize the students with architecture, pin configuration											
Objective	es:		nd programming of 8085 microprocessor kit. Further students will perform various											
Ū		arithme	ithmetic operations on microprocessor kit. The basic concept of 8051 microcontroller											
		has also	s also been included in this lab.											
Course		1.	1. Understand the architecture and pin configuration of 8085 microprocessor and											
Outcomes	s:		8051 mi	crocontr	oller.									
		2.	Implem	ent vario	us progr	ams on 8	8085 mic	roproces	ssor kit a	nd 8051				
			microco	ntroller	kit.									
			Mappi	ng of co	urse out	comes v	vith prog	gram ou	tcomes					
P	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1		√	V	√ 										
CO2		√			$\sqrt{}$	$\sqrt{}$								
		1. 2. 3. 4. 5. 6. 7. 8. 9.	To get familiarize with Pin Configuration of 8085 Microprocessor. Familiarization of different keys of 8085 microprocessor kit. To familiarize with entering various steps of a program in 8085 kit. Steps to enter, modify data/program and to execute a programme on 8085 kit. Writing and execution of program for addition of two 8 bit numbers. Writing and execution of program for subtraction of two 8 bit numbers. Writing and execution of program for multiplication of two 8 bit numbers. Writing and execution of program for division of two 8 bit numbers.											



						EC-226	•						
	Computer Programming & Applications												
				L			T	]	?		Credits		
				3			0	2	2		4		
		Sessio	nal Ma	rks							50		
		End S	Semester	Exami	nation M	Iarks					50		
Course	<u> </u>	This c	ourse is	useful as	it devel	op the al	bility to v	write cor	nputer p	rograms,	to comp	are	
Object	ives:	values	and per	form alte	ernative o	operation	ns based	upon the	results	of the co	mpariso	n.	
		Stude	nts will a	ılso learr	how to	identify	the prop	er structi	are of lo	ops, the t	ise of ar	rays,	
				strings e									
Course	Course 1. General introduction to the rapidly expanding field of computer science.												
Outcor	<b>Dutcome:</b> 2. To understand the basic terminology used in computer programming												
	3. Students will able to design programs i.e. how to write, compile and debug programs												
	in C language.												
	4. To further develop computer skills.												
	Mapping of course outcomes with program outcomes												
001	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1		1	N	ν								-	
CO2		V	V	1	1								
CO3		V	V	V	V							-	
CO4   V   V   V   V     101													
T4 J	4 4.	<b>C</b>	4 T	<u>Un</u>		C4-	D11	D:		7	C4	12 hrs.	
	uction to			Jenne a	Comput	er Syste	т, вюск	Diagra	m or a C	omputer	System	and its	
	g, associa r <b>ies:</b> Me	•	•	OM Sa	condors	Storaga	dovices	Comput	or coftwa	ore and L	Inrdsvore		
Memo	iles. Mie	mories.	KAWI, N	Uni		Storage	devices,	Comput	er sortwa	are and r	iaiuwait	12hrs.	
Worki	ng Know	ledge o	f Comp			roductio	n to the o	nerating	System	its func	tions an		
	ng knowle												
	ORD, MS												
system	J142, 1116	LITCL	, 1,10 1	0,,,,,,	100	15 101 20	ica iviana	germent,	Dusies	T Duide	ise mane	goment	
•	e <b>t:</b> Evolu	tion of l	Internet a	and its ar	plication	ns and S	ervices						
				Unit								12hrs.	
Proble	m Solvin	g: Nee	d for pro	blem sol	lving of	a progra	m: Metho	ods of Pr	oblem S	olving			
Progra	m Plann	ing: Pl	lanning a	a Progran	n, Progra	am desig	gn tools,	Algorith	ms, flow	charts a	nd pseu	docode:	
	tive exam												
	ew of C		0			0 .							
	iking, ID		its feat	ıres, Ba	sic Tern	ninology	- Chara	cter set,	Tokens	, identif	iers, ke	ywords,	
fundan	ental dat	a types											
					<u>t-IV</u>							12hrs.	
	tors and										expression	ons and	
	aluation,										٦ 1٠،٠	1 1	
_	ing with	_	_	_	_	•		n, Progra	am Stru	ctures: (	Condition	nal and	
	itional br				•			D.	1 4 . 1	141-11			
-	and Point		•				•	0.		nnanze a	rrays,		
romer	s, declara	uon of	a pointei				D BOOL	_	018				
Titla				K			וטטם ע		hlichan				
Title					Au	<u>thor</u>		Pu	<u>blisher</u>				



## Teaching Scheme for Integrated Certificate and Diploma (DEC-CSME) Programme

1. Fundamentals of Computers	V. Rajaraman	Prentice Hall of India
2. Object Oriented Programming with C++	E. balagurusamy	Tata Mcgraw Hill
3. Let us C	Y. P. Kanetkar	BPB Publications



	Computer Programming & Applications Lab												
Course		v	The objective of this lab is to make students familiar with programming of C language. They will perform various programs in C language to learn its practical applications.										
Course		1. 2. 3.	<ol> <li>Understand basics of C language.</li> <li>Implement C language in various practical applications.</li> <li>Perform arithmetic operations on matrices.</li> </ol>										
			Mappi	ng of co	urse out	comes v	vith prog	gram ou	tcomes				
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1		V	V										
CO2			V		V								
CO3			V										
	List of Experiments:  1. Program to find area and circumference of circle. 2. Program to add first 10 natural numbers 3. Program to calculate sum of 5 subjects. 4. Program to find greatest in 3 numbers. 5. Program to print Fibonacci series up to 100. 6. Program to find factorial of a number. 7. Program to find whether given no is a prime no or not. 8. Program to find sum of two matrices. 9. Program to find subtraction of two matrices using arrays.												



						EC-227							
	Service and Maintenance of Computers												
				L			T	I			Credits		
				2			0	4	ļ		4		
		Sessio	nal Ma	rks							50		
		End S	emester	Examir	nation M	Iarks					50		
Course	9	This course is useful as it equips participants with basic knowledge about personal											
Object	ives:												
	PC systems and troubleshooting of common problems.												
Course	2	1. Gain basic knowledge about personal computers											
Outcor	nes:	2. Learn about PC hardware and software											
		<b>3.</b> U	Jndersta:	nding the	e archited	cture of	computer	r systems	S				
		<b>4.</b> U	Jndersta:	nding the	e function	ns of per	ipheral d	levices in	n microc	omputer	systems		
				-		_	_			iters and			
			etworki	_									
			Mappi	ng of co	urse out	comes v	vith prog	gram ou	tcomes				
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1	$\sqrt{}$												
CO2	$\sqrt{}$			$\sqrt{}$									
CO3	V		$\sqrt{}$										
CO4													
CO5	$0.05$ $\sqrt{}$ $\sqrt{}$												
	Unit-I 12 hrs.												

**Repair, Servicing and Maintenance Concepts:** Repair, Servicing and Maintenance Concepts Introduction to servicing and maintenance concepts. Meantime between failure (NTBF) meantime the repair maintenance policy, potential problems preventive maintenance and corrective maintenance. Circuit tracing techniques. Concept of shielding grounding and power supply requirements and considerations of computers and its peripherals.

**Fundamental troubleshooting procedure & installation of OS:** Fault location, Fault finding aids, Service Manuals - Test and measuring instruments, Special tools for troubleshooting, Installation of operating System

Unit-II 12 Hrs.

**Hardware and Software Faults:** Trouble shooting techniques. Different trouble shooting techniques and methods, Functional area approach, Split half method, Divergent, convergent and feedback path circuits, analysis measured techniques.

**Troubleshooting of computer components and Peripherals:** Mother Board, FDD, HDD, CD ROM/DBD, Printers, Modems, Monitors, SMPs



Unit-III 12 Hrs.

**Maintenance and repair of peripherals:** Specification, Maintenance and Repair of CVTs and UPS, Sight preparation and design of computer rooms. Testing specifications and installation of computer systems and peripherals.

**CD Drive and its troubleshooting:** Working Principle and its types. CD-ROM drive:- CD drives mechanism installation of CD drive. Drive technologies: - CD-ROM: SCSI\CD-R, CD-RW, DVD-ROM. Working Principals, IDE controller card

Unit-IV 12 Hrs.

Modem: Fault Finding, Repairing, modem Circuit Diagram, Repairing MODEM.

**Network Components:** Introduction of Network Cable like UTP, STP, Fiber Optics, Hub, Unmanageable Switch, Manageable Switch, Router, Wi-Fi, Access Point, PCI Wireless Card, USB Wireless Device, Print Server, USB Network Sharer, Backup Device, Server Hardware etc. Installation and troubleshooting of Routers

RECOMMENDED BOOKS										
Title	Author	Publisher								
1.Electronic test equipment	RS Khandpur	McGraw Hill								
2. Maintenance and Troubleshooting Guide	SK Chauhan	SK Kataria and Sons, New Delhi								
3. Trouble shooting computer system	Robert C Benner									



			Se	ervice an	d Main	tenance	of Com	puters L	ab				
Course Object		compu	The objective of this lab is to familiarize the students with the basic hardware part of computer. It includes assembly as well as troubleshooting of various components in CPU. Students will also learn the basics of operating systems.										
Course	<u> </u>		1. Identify the various components connected to motherboard.										
Outcor		2.											
			Mapping of course outcomes with program outcomes										
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1	$\sqrt{}$						V						
CO2	V						V						
			11 12 13 14	Study practic Assem Unders To stud Windo To stud To st	and ice connected and standing dy various dy the second dy the tree dy the tree dy the tree dy the dy the dy and pedy softwellerstand	dentificated in a control of the main	ompone able the cations of process installar ad format acting of poting depoting of mup program trouble lems and attenance	f comp nts on n desktop I f RAM a ssors and tion of o ting of H sound ar vice man printer p gram (M eshooting repair th schedule	onents notherboard PC. nd its seal their spaperating ard disk and video ager issuroblems. anually/	on morard.  lection. ecification system  drive usin card in the ues in Win Automatic puter integ y various ktop PC.	ons. Window ng disk m ne system n 7. ically) a	s 7 and nanager. n nd disk devices.	



					NETW	EC-228 ORK T	HEORY	7				
			]	L		T		J	P		Credits	
				3	0		4					
		Session	al Mar	ks							50	
		End Se	mester l	Examina	ation Ma	arks					50	
Course	Course The subject aims to provide the student with an understanding to analyse any given											
Object	Objectives: electrical network with help of fundamental techniques such as Kirchoff's laws, mesh and											
ŭ	node analysis, network theorems, etc. Also aims to provide necessary background for										und for	
understanding of various circuits and networks.												
Course	·											
Outco												
	3. Able to analyze resonant circuits and magnetically coupled circuits.											
		4.	Able to	use Lap	lace trans	sformati	on to sol	ve vario	us circui	ts and us	e of test	signals.
				ng of co	urse out	comes v	vith pro	gram ou	tcomes			
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1		$\sqrt{}$		$\sqrt{}$								
CO2		$\sqrt{}$										
CO3		$\sqrt{}$										
CO4												
				<u>Un</u>	<u>it-I</u>							14 hrs.
Introd	uction:	Voltage	and cur	rent sou	rces, rel	ation be	tween cu	irrent, vo	oltage, p	ower and	d energy	of DC
		e transfo										
		using no								sing loop	current	method
and bra	and branch current method for solving network problems, star-delta conversion.											
	<u>Unit-II</u> 10 hrs.											
		orems: S										
		er transf	er theore	m and T	Cellegens	theoren	n for the	solution	of netw	orks wit	h DC ex	citation
and AC	and AC excitation.											
				Uni	t-III							10 hrs.

Unit-III 10 hrs

**Resonance and Magnetically Coupled Circuits:** Introduction to resonance, Series resonance, Parallel resonance, Concept of self-inductance and mutual inductance, coupling coefficient, magnetically coupled circuits, Simple series and parallel circuits, Dot convention.

Unit-IV 14 hrs.

**Laplace Transformation:** Introduction, important Laplace transform functions, initial and final value theorems, Laplace inverse transform, use of Laplace transform method for series and parallel R-L, R-C, L-C, and R-L-C circuits, **Standard Test Signals:** Unit step, ramp, impulse, gate and shifted functions and their Laplace transforms, **Filters:** Introduction to low pass, high pass, band pass and band elimination filters, prototype LC and RC filters.



RECOM	MENDED BOOKS	
Title	Author	Publisher
1. Fundamentals of Electric Circuits	Charles K. Alexander and Matthew N.O.Sadiku	Tata McGraw Hill
2. Network Analysis	Van Valkenburg	Prentice Hall of India
3. Networks and Systems	D. Roy Choudhary	New Age International
4. Circuit and Networks: Analysis and Synthesis	A.Sudhakar and S.Palli	Tata McGraw Hill



			Troub	leshoot	ing of E	EC-229 Electron		pment-	II			
		L				T		P		Credits		
		0				0		4		2		
Course	9	The cou	ırse prov	ides the	students	with neo	cessary k	nowledg	ge and co	ompetenc	y to diag	gnose
Object	ives:		ts for tro		_	•	tematic 1	epair an	d mainte	enance of	electron	nic
			ent and t									
Course		1.	Find fau								ile teleph	iones,
Outcor	nes:				generato							
		2.	Analyse			ctioning,	fault fin	ding and	l repair o	of UPS a	nd home	
			inverter	•			•41					
	1	T			urse out					1	,	
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	V	V		V	1							
CO2	√	V		V	ν							
		1. Demo 2. Demo 3. Demo 4. Demo 5. Demo 6. To s 7. To m UPS sy 8. To st 9. To m Home i 10. Dem 11. Dem	onstration on tration training the training of training of the training of training	n and prand	actice of actice of actice of actice of agram an C voltag ainer. tice of fa l Circuits	fault fin fault fin fault fin fault fin d working and warking e and warking ault finding (ICs)	ding and ding and ding and and principaveform and read an	I repair of I repa	of C.R.O of Function of Power of Digital PS system of Point of Power of Digital PS system of PS	on Gener supplies I multime em traine in Diffe rter syste in Diffe	rator eter. r. rent secti	er.



						EC-221									
					nentals o										
				L		'	T	I		or intrinsic and exdevices.					
				3			0	2	2		4				
		Sessio	nal Ma	rks							50 50 istics of the electronic aderstanding application intrinsic and extrinsic evices.  et intrinsic and extrinsic evices.  et intrinsic and extrinsic evices.  et intrinsic and extrinsic evices.				
		End S	emester	Exami	nation M	<b>Iarks</b>					50				
Course Object		device	The course intends to provide the basic concept and characteristics of the electronics devices such as diode, BJT, FET, etc. Also aims to provide the understanding application of different electronics devices and simple circuits.												
Course Outcor		2. 3.	materia This co Acquire sources Able to Filters,	ls. urse give ed know unders Regulat	es an over ledge at tand the ed circui	erview o bout acti working ts, etc.	f various ve and j	s semico passive of	nductor of compone	devices. ents, volt	age and	current			
			Mappi	ng of co	urse ou	tcomes v	with pro	gram ot	ıtcomes						
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12			
CO1										-					
CO2															
CO3	V	V		V											
CO4		V	V	V	V	V									
			•	Un	it-T			•		•		8 hrs.			

**Introduction:** Classification of materials into conducting and insulating materials through a brief reference to atomic structure, Conducting Materials, Insulating Materials, Semi-conductor Material.

<u>Unit-II</u> 10 hrs.

**Active and Passive Components:** Introduction to active and passive components; fixed and variable resistances, their various types fixed and variable capacitors, their various types and important specifications and color codes.

**Voltage and current sources** – concept of constant voltages and constant current sources, symbol and graphical representation, characteristics of ideal and practical sources.

Unit-III 14 hrs.

**Semiconductor Diodes:** Atomic structure of Germanium and Silicon semi-conductors; intrinsic and extrinsic semiconductors, PN junction, basic principles of operation and VI characteristics of PN junction diode, static and dynamic resistance of a diode. Use of a diode in rectifiers, half wave, full wave and bridge rectifier with shunt capacitor filter, series inductor filter, zener diode and its applications, as a voltage regulator, light emitting diode (LED), liquid crystal display (LCD).



	<u>Unit-IV</u>		14 hrs.										
<b>Transistors:</b> Introduction to a	transistor, working of a PNP and	l NPN tran	sistor, input and output										
characteristics, transistor configura	characteristics, transistor configurations, biasing of a transistor, amplifying action of a transistor, comparison												
of different configurations, common emitter amplifier circuit, load line, concept, field effect transistor FET,													
JFET, MOSFET, their characteristics and applications, unijunction transistor (UJT).													
RECOMMENDED BOOKS													
Title	Author	Publisher											
1. Basic Electronics and Linear	N N Bhargava and Kulshreshta	McGraw	Hill										
Circuitis													
2. Electronics Devices and	Miliman and Halkias	McGraw	Hill										
Circuits													



	Fundamentals of Electronics Engineering Lab  Course To reinforce learning through hands-on experience by examining the electrical													
Course	e	To rei	nforce	learning	through	h hands	on exp	perience	by ex	amining	the el	ectrical		
Object	tives:			of variou										
		provide	the stud	ent with	the capa	bility to	measure	and reco	ord the ex	kperimen	tal data,	analyse		
				rious sen										
Course	e	1.	To und	erstand tl	he functi	ioning of	f various	electron	nic instru	ıments li	ke CRO	, signal		
Outco	mes:		generate	or and m	ultimete	r.								
	2. To understand the characteristics of diode and BJT and verify their responses.													
	3. To construct various electronic circuits on the bread board and analyses their output.													
	Mapping of course outcomes with program outcomes													
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1														
CO2														
CO3					$\sqrt{}$									
		1. 2. 3. 4. 5. 6. 7. 8.	To calce To obse To obse To obse To verif To cons its rippl To cons its rippl	sure valuulate the erve the ferve the Verve the Verve the Verve the Verve that e factor.	value of ront pandront pandront pandront character of the control	various el of CR el of sign eteristics eteristics p transis ectifier,	passive of O. nal gener of semic of zener tors. wave sha	componerator. conductor diode in the pe of the	ents usin or diode in reverse e electric	n forwar bias. cal signal	eter. d bias. and calo			



				Ele	ectroma	EC-311 gnetic Fi		orv						
				L			T	F	•		Credits			
				3			0	0	)		3			
		Sessio	nal Ma	rks							50 50 cepts in the area of related to a field like a covered. Next focus magnetic field and the field.  The pagation area of the pagation area.			
		End S	emester	Exami	nation M	Iarks					s related to a field like scovered. Next focus magnetic field and the			
Course Object Course Outcon	e	electro potent to give	omagneticial, flux, ethe bridgation. Europe Under Apply Under	c field a charge of ef descrip sasic idea stand fur Maxwel stand fur	and wave density, f ption about a about tr ndamenta d's equat ndamenta	e propage field interpreted into the maxwell of the maxwell terms and terms are terms and terms and terms and terms and terms are terms and terms and terms and terms are terms are terms and terms are terms and terms are terms and terms are terms are terms and terms are terms	nation. Vonsity and well's equipment of the control	arious p d energy uation for is also c electron agnetic v o transmi	aramete density or electro overed. magnetic wave pro ssion lir	rs related is covere omagnetic c field. opagation nes.	I to a fidd. Next is c field and in the control of	eld like focus is		
		-10						gram ou		m meara	••			
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1		V		√	√									
CO2				V	$\sqrt{}$									
CO3		V		1	<b>√</b>									
CO4					$\sqrt{}$									
				Un	it-I							12 hrs.		

**Introduction to Vector Analysis**: Introduction to vectors, addition, subtraction and multiplication of vectors, different co-ordinate systems, Cartesian, cylindrical and spherical systems, transformation between different co-ordinate systems.

**Static Electric Field**: Force between point charges, coulombs law, electric field intensity, electric scalar potential, charge density, gradient of potential, electric flux, gauss law, energy in capacitor, energy density, flux density, divergence, Maxwell's divergence equation, and current density.

Unit-II 12hrs.

**Static Magnetic Field:** Current density in a conductor, force on moving charge and current element, Biotsavart law, magnetic flux, magnetic flux density, amperes law, Maxwell equations, magneto static potential, Maxwell current equation, vector potential.

**Time Varying Fields:** Faraday"s law, moving conductor in a magnetic field, stoke"s theorem, Maxwell equation from Faraday"s law, displacement current, Maxwell"s equation from amperes law, Maxwell equation for free space.

<u>Unit-III</u> 12 hrs.

**Wave Transmission:** Maxwell equations, plane waves, EM wave in a homogeneous medium, uniform plane wave equation for a conducting medium, sinusoidal time variations, reflection coefficient, wave equations for waves in space, plane waves at interfaces, group velocity, phase velocity, power and energy relations, pointing vector, reflection of wave.



Unit-IV 12 hrs.

**Transmission Lines:** Introduction, basic principles, termination lines with load, voltage and current distribution, characteristic impedance, propagation constant attenuation constant, phase constant, reflection coefficient, VSWR, open and short circuited transmission lines and their impedances, stub matching, types of high frequency transmission lines.

	RECOMMENDED	BOOKS
Title	Author	Publisher
1. Electromagnetic Engineering	Hayt	Mcgraw Hill
2. Field theory	Gangadhar	Khanna
3. Electromagnetic	Karus	Mcgraw Hill
4. Electromagnetic Fields and	K.D. Prasad	Satya Prakashan
Waves		



				т	in oon IC	EC-312								
				L L	inear iC	/ S & Ap	plication T	ns F	•		Credits			
				3			1	4	ļ					
		Sessio	nal Ma	rks		•		l .			nd non-linear op-ar er IC's and PLL's. -amps. determine how			
		End S	emester	Exami	nation M	Iarks								
Course  Cobjectives:  Learning op-amp construction and its characteristics. Ability to design different configurations of op-amp and analyse and design linear and non-linear op-amp applications, active filters and detectors. Also analyses of 555 timer IC's and PLL's.														
Course	9	1. Acquired knowledge of fundamental characteristics of op-amps.												
Outcor	mes:	2.	To ana	alyze op-	amps wi	th and w	ithout us	ing feed	back and	d determi	ine how			
			negati	ve feedb	ack effec	ets the pe	rforman	ce of op-	amps.					
		3.	To lea	rn the lir	near appl	ications	of operat	ional an	plifiers.					
		4.	Under	stand va	rious app	olications	s using 5	55 timer	and PLI	 				
		•	Mappi	ng of co	urse out	comes v	vith prog	gram ou	tcomes					
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1		$\sqrt{}$		√ -										
CO2				V	V	V								
CO3		√		V	V	V								
CO4														
				Un	:+ T							12hrc		

**Introduction:** Basic Op-amp and its schematic symbol, Block diagram of a typical Op-Amp, , integrated circuits and their types, IC package types, Pin Identification, Characteristics and performance parameters of Op-Amp, Equivalent circuit of an Op-Amp, Ideal Op-Amp and its characteristics, Ideal voltage transfer curve.

**Practical Op-Amp**: Input offset voltage, Input bias current, Input offset current, total output offset voltage, Thermal drift, Variation of op-amp parameters with supply voltage and temperature, Noise, Common Mode configuration and common mode rejection Ratio.

Unit-II 12 hrs.

**Negative feedback in op-amps:** Block diagram representation of feedback configurations, Voltage-series feedback Amplifier, Voltage shunt feedback amplifier, Differential amplifiers with one op-amp, two op-amps and three op-amps.

**Operational amplifier applications:** Op-amp as inverting amplifier, non-inverting amplifier, unity follower, adder, subtractor, integrator, differentiator, comparator, logarithmic amplifier.

Unit-III 12 hrs.

**Voltage regulator ICs:** Concept of regulation, principal of series and shunt regulator, 723 IC, three terminals voltage regulator ICs (positive, negative and variable voltage) and their applications (78XX and 79XX,317)

**Voltage controlled oscillator:** Concept of voltage controlled oscillator, principle of operation of VCC, study of IC 566 and its applications.



<u>Unit-IV</u>			12 hrs.								
<b>Specialized IC applications:</b> 555 timer IC and	its pin configuration, Block	diagra	am, application of 555 as								
Monostable and Astable Multivibrator, Operating	Monostable and Astable Multivibrator, Operating principles & applications of 565PLL.										
Voltage Regulators: Fixed voltage regulators, Adjustable voltage regulators, Switching Regulators.											
RECOMMENDED BOOKS											
Title	Author	Publ	isher								
1. Op Amps & Linear Integrated circuits	Ramakant Gayakwad	Pea	rson Education								
2. Operational Amplifiers and linear integrated	R.F. Coughlin & F.F.	Prei	ntice Hall								
circuits	Driscol										
3. Design with Operational Amplifiers and	S. Franco	Tata	a Mc-Graw Hill								
Analog Integrated Circuits											



	Linear IC's and Applications Lab													
				Line	ar IC's a	and App	lications	s Lab						
Course	e	This lab	aims to	study t	he basic	principle	es, config	guration	s and pra	actical li	mitations	of op-		
Object	ives:	amp. To	o underst	and the	various 1	inear and	d non-lin	ear appl	ications	of op-an	ıp.	_		
Course	e									various		nd non-		
Outcor	mes:									nverters.				
		2.								cillators		equency		
			generate	ors.		_			_					
		3.								are resul	ts using S	SPICE.		
				ng of co	urse out	comes v	vith prog	gram ou	tcomes					
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12		
CO1		$\sqrt{}$	$\sqrt{}$			$\sqrt{}$								
CO2			$\sqrt{}$	$\sqrt{}$		$\sqrt{}$								
CO3														
		List of	Experi	ments:										
		1.	1. To study differential amplifier configurations.											
		2. To measure the performance parameters of an Op amp.												
		3.	Applica	tion of C	)p amp a	s Inverti	ng and N	Non Inve	rting am	plifier.				
			To study											
			To use t						ging amp	olifier.				
			To use t											
			Design			_	_	•	•					
		8.								ign Low	pass, Hi	gh pass		
						Butterwo			using O	p Amp.				
			Design 1											
			Design '											
			Applica											
					•		_		and wir	ndow det	ector.			
			Applica									c ~		
		14.	_		_			•	•	de an out	•	_		
										specify th				
		1.7					maxımuı	m power	dissipat	ion of th	e transisi	tor.		
			Design a				T 1 ·	1-4	41 C		C			
		16.							ne the fr	ee runnir	ig freque	ency,		
		,	•	_		lock in	_		CE					
			Verificat	ion of ha	araware 1	resuits of	otainea u	ising SP	CE.					



					EC-313										
			L	Digital (		nication T	I	)		Credits					
			<u>L</u>			<u>1</u> 1	2			5					
	Coggio	nal Ma				1		<u>.</u>		50					
				nation M	[onlea					50					
Course				dying th	•	_									
<b>Objectives:</b>			•	U			•		s. The students will understand						
	the procedures and modulation techniques involved in developing digital communication									nication					
system and explore the utility of digital signals for extended applications.															
Course 1. Gain knowledge about the fundamental concepts of digital communication systems.										stems.					
Outcomes: 2. Convert analog signal into digital signal and apply suitable line codes															
3. Identify the benefits of digital signals for applying suitably in multiple access										access					
techniques															
4. Acquire knowledge about digital modulations schemes.															
	Mapping of course outcomes with program outcomes														
PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12				
CO1	√ 	V	V	,											
CO2	√ √	V	√	<b>√</b>											
CO3	√ /	V	,	√	,										
CO4	V		√ 		√						10.1				
			<u>Un</u>								12 hrs.				
Elements of															
representation	of Analog	g signals	, Advant	ages and	l Disadva	antages c	of Digital	l Comm	unication	ı system,					
Concept of a	mount of	Inform	ation a	nd entro	py: Rat	e of info	rmation,	Shanno	on Fano	Source (	Coding,				
Huffman source											Ç.				
			<u>Uni</u>	t-II							14 hrs.				
Sampling The															
Pulse Code M	odulation	n: Block	diagram	of PCM	I system.	Quantiz	ation, D	elta Mod	dulation,	Continu	ously				
variable Slope	Delta Mo	odulator			aptive D	elta Mod	lulation.								
			<u>Uni</u>	t-III							12 hrs.				
<b>Line Coding:</b>				rties. NF	RZ & RZ	types, s	ignaling	format f	or unipo	lar, Pola	r,				
bipolar (AMI)	and Man	chester c	oding.												
Multiplexing	Techniq	ues: Fun	damenta	ls of time	e and fre	quency o	division	multiple	xing.						
Multiple Acce	ess Techn	iques: B	sasics of	TDMA,	FDMA	and CDN	ЛA.								



<u>Unit-IV</u> 10 hrs.

**Digital Carrier Modulation Techniques**: Introduction, Amplitude Shift Keying (ASK), ASK Spectrum, ASK Modulator, Frequency Shift Keying (FSK), PSK.

**Digital Carrier Demodulation Techniques**: Coherent ASK Detector, Non-coherent ASK Detector, Non-coherent FSK Detector, Coherent FSK Detector.

DEC	OMMENDED BOOKS	
Title	Author	Publisher
1 Principles Of Communication Systems	Taub and Schilling	Tata McGraw-Hill Education
2. Introduction to Communication Systems	Gary M. Miller	6 <sup>th</sup> edition, Prentice-Hall, 1999
3. Modern Electronic Communication	D. Roy Choudhary	New Age International
4. Modulation and Coding Techniques in	Evgenii	WILEY, 2011.
Wireless Communications	Krouk, Sergei	
	Semenov	
5. Digital Communication	E.A. Lee and D.G.	Kluwer Academic
	Messerschmitt	Publishers,1994



				Ι	Digital C	Commun	ication	Lab					
Course		This la	b aims to	unders	tand the	building	blocks	of digit	al comm	nunication	system.		
Object													
Course			•						_		ication sy		
Outcomes: 2. Understand and analyze the various data formats used in digital communication.  Mapping of course outcomes with program outcomes													
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1			√	√ /		V							
CO2				√		√							
		1. 2. 3. 4. 5. 6. 7. 8. 9.	Study of Stu	of Sample of Pulse of Delta of difference of ASK 1 of FSK months of TDM	ing and code mo modulate modulation modulation per	dulation and of formats/chniques on and of on and don and done and deceiver ar	and dendered demodulation codes.  demodulation demodulati	modulation. es. lation. ation. ation. mitter.	ion.				



	EC-315												
				Princ	iples of	Microv	vave En	gineeri	ng				
				L			T		P		Credit	S	
				2			1		0		3		
		Sessio	nal Ma	rks							50		
		End S	emester	Exami	xamination Marks 50								
Course													
Object	ives	types Microwave devices, components, their characteristics, their working, and their applications.											
Course	<b>Ourse</b> 1. Acquire knowledge about the microwave frequencies and the waveguides that are used											are used	
Outcor	ne:	to carr	y them.	_				-		_			
			•	owledge	about i	isolator.	circulat	or, cour	oler, mic	crowave s	solid state	devices	
			icrowav	•		,		<sub>I</sub>	,				
						nerforn	nance ar	nalveie o	f Micro	wave Tub	es and Ci	renits	
		J. OH			•	•		rogram			cs and Ci	icuits.	
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1	101	\ \ \ \ \	103	<del>104</del>	105	100	107	100	107	1010	1011	1012	
CO2		V	V	V		V							
CO3		V	V	V	V	,							
203												12 hrs.	

**Introduction:** Microwave frequency spectrum, familiarization with bands and wavelength. **Microwave Components:** Wave guides, wave guide coupling, ferrite devise faraday rotation, isolators and circulators.

Unit-II 12hrs.

**Microwave Couplers:** Directional couplers, loop directional couplers two hole directional couplers, phase Shifters, attenuators, introduction to S parameters.

**Microwave Tubes:** Problem with conventional tubes, limped elements at microwave frequencies, velocity modulations, multi cavity, klystron, two cavity klystrons, performance, and reflex klystron.

Unit-III 12 hrs.

**Magnetrons & TWT:** Magnetrons, constructional features of cavity magnetron cylindrical magnetron oscillations strapping, pushing and pulling traveling wave tube, Performance and application.

**Semiconductor Microwave Devices:** Transistors, integrated circuits, advantages of MICIs varactor diodes, step recovery diode, frequency multipliers.



<u>Unit-IV</u> 12 hrs.

**Semiconductor Microwave Diodes:** Gunn diode application, avalanche effect impatt diode, trappat diode, characteristics and application of avalanche diode, principle of pin diodes & its application, schottky barrier diode, backward diode.

**Microwave Strip Line:** Strip line, micro strip line, strip like transmission line, losses in strip like transmission line.

RECOMMENDED BOOKS										
Title	Author	Publisher								
1.Microwaves	K C Gupta	New Age International								
2.Microwave and Radar Engg.	M Kulkarni	Umesh Publications, Delhi								
3. Microwave Devices and Circuits	Liao S Y	Prentice Hall of India								
4. Foundation of Microwave Engg.	R. E. Collin	McGraw-hill								



	TP-301E												
					Ind	lustrial	Trainin	g					
Cours	e	To pr	To provide hands-on experience in various domains such as hardware, software,										
Objec	tives:		maintenance and testing in Industry / Training Centre's/ Corporate Offices so that they										
		becom	e aware	of the p	ractical	applicati	ion of th	eoretica	l concep	ts studied	in the cla	ss rooms	
		and to	expose	student	ts to the	e 'real'	working	enviror	nment a	nd get ac	quainted	with the	
		organi	zation st	ructure,	business	s operati	ons and	adminis	strative f	functions.			
Cours	e	1.	Genera	ate a rep	ort base	d on the	experie	nces and	l project	s carried	out with th	ne ability	
Outco	mes:		to appl	y know	edge of	Mathen	natics, S	cience, a	and Engi	ineering F	undament	als.	
		2.	Demor	istrate	compete	ency in	releva	ant eng	ineering	fields	through	problem	
			identif	ication,	formula	tion and	solution	1.				-	
		3.	Effecti	vely im	plement	skills i	n comm	unicatio	n, in w	riting and	using m	ultimedia	
			tools.	•	•					C	C		
		4.	Develo	p the al	oility to	work as	an indiv	vidual ar	nd in gro	oup with the	he capacit	y to be a	
				•	•			ve team	_	•	•		
		5.			_					n enginee	er.		
		•	Map	ping of	course o	outcome	es with p	rogran	outcon	nes			
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1		V											
CO2				V	V								
CO3													
CO4													
CO5													



	EC-321 Industrial Electronics												
				L	muust	1	T	I	)		Credits		
				3			1	4	1	6			
		Sessio	nal Ma	rks		•				50			
		End S	emester	Examir	nation M	Iarks				50			
Course Object	<b>bjectives:</b> The objective of this course is to provide in-depth knowledge of the basics of various power semiconductor devices, analyses and design of various power converter circuits using power semiconductor devices and their applications in commercial and industrial areas.											circuits	
Course	9	1. Acquire knowledge about fundamental concepts and techniques used in power											
Outcor	mes:	electro	onics.									_	
			•		gle phas	e and the	ee phase	e power o	converte	r circuits	and und	erstand	
			pplication										
						roublesh							
				ty to und	lerstand	the use of	of power	convert	ers in co	ommercia	ıl and in	dustrial	
		applic											
						tcomes v		ſ					
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1		V	√		√								
CO2		V		V		√							
CO3	$\sqrt{}$			V		$\sqrt{}$							
CO4				$\sqrt{}$									
				Un	it-I							12 hrs.	

**Power Devices:** Symbols, specifications and testing of SCRs, DIACS, TRIACS, UJT. Characteristics of the above devices, Protection circuits for the above devices.

**Introduction to Thyristors:** Thyristor ratings, thyristor construction, principle of operation of an SCR, working of SCR using transistor analogy. Turn on methods-DC gate, AC gate and Pulse gate triggering and R-C trigger circuits. Turn off methods- natural and forced turn off methods. thyristor protection, Circuit for over voltage and over current protection. Internal power dissipation and need for heat sinks in thyristors. Concept of thermal resistance of heat sinks, various types of heat sinks and techniques of mounting device on heat sinks.

<u>Unit-II</u> 12hrs.

**Phase Controlled Rectifiers:** Explanation of the working of three phase half wave and full wave bridge rectifier with the help of wave forms, Explanation of working of following controlled rectifier using SCR (resistive and inductive loads) with help of wave forms and appropriate mathematical expression (no derivations): Single phase controlled half wave, full wave and bridge rectifier. Three phase controlled half wave, full wave and bridge rectifier, Principle of dual converters & their applications.

Unit-III 12 hrs.

**AC Phase Control:** Principle of working of AC phase control circuit using triac and its applications, Application of phase controlled rectification and AC phase control circuit in: Illumination control, Fan speed control, Temperature control.

**Thyristor Applications:** Principal of operation and working of the following switching circuits using SCRs: Automatic battery charger, Voltage regulator, Time delay relay circuit, Emergency, light, Burglar alarm circuit, Light operated alarm.



**Inverters:** Principle of operation of basic inverters circuit, basic series and parallel commutated inverters.

Unit-IV 12 hrs.

**Choppers and Cyclo Converters:** Choppers: Introduction, types of choppers. Step up and step down choppers. Voltage and current commutated type chopper. cyclo converters: principle of operation of converter, input and output characteristics of bridge circuits and applications.

**Solid State Controls**: Advantages of electronic control of devices, Basics of DC motor speed control, speed control of DC and small AC motors using thyristor technology.

RECOMMENDED BOOKS										
Title	Author	Publisher								
1. Industrial Electronics and Control	S K Bhattacharya and S Chatterji	Tata McGraw Hill								
2. Power electronics	P S Bimbhra	Khanna Publishers, New Delhi								
3. Power electronic	M Rama Murthi	New age								



			I	ndustria	l Electr	onics La	b				
Course Objectives:	The objective is to analyses V-I characteristics various power semiconductor devices like SCR, DIAC and TRIAC and to study various basic power control circuit using power semiconductor devices.										
Course Outcomes:	1.	Analyse DIAC, Analyse oscillate	the V-I FRIAC a the wa or, chopp	characte and UJT. veforms per circui	of half t etc.	wave, f	ull wav	e contro	tronic de lled rect		•
		Mappi	ng of co	urse out	comes v	vith prog	gram ou	tcomes			
PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1		√ 	V	,	√ 						
CO2											
	1. 2. 3. 4. 5. 6. 7. 8. 10.	To test a To draw average To draw its avera To draw rectifier To stud time per To trigg To draw average To draw average To draw average To draw	and draw and draw and draw w the difference was a difference which was a difference wh	the char w the char the char fferent was erent was fferent w CR using fferent w	racteristi aracteristi vaveforms veforms vaveform g relaxati vaveform	cs of DL tics of T cs of UJ ns of ha of full was of rela on oscill ns of ha notor usin s of vol	AC and the RIAC and firm of the save midaxation of the save attention of the save attent	find its band find its int controll point controll bridge obscillator voltage	rinsic stated rectiful trolled reconfigurate using Use controlled and cur	r voltage ing and ndoff rat ier and rectifier a ation con JJT and ler and	es. holding tio. find its and find ntrolled find its find its forms.



	EC-322 WIRELESS AND MOBILE COMMUNICATION												
				L	AND WI		T				Credits		
				3			0	(	)	3			
			nal Ma								50		
		End S	emester	Examir	nation M	Iarks					50		
Course										o gives a			
Object	ives:									es modula			
				le comm e commi			y, it intr	oduces t	he CDM	IA and C	iSM tecl	nnıques	
Course	<u>,                                      </u>	1.					ated to w	rireless c	ommuni	cation sy	stem		
Outcor			<ol> <li>Understand the basics terms related to wireless communication system.</li> <li>Understand the basics of cellular communication system.</li> </ol>										
0 444001	-100		3. Understand the modulation techniques used in mobile communication.										
		<u> </u>	Mapping of course outcomes with program outcomes										
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1		√	V	<b>√</b>									
CO2				√									
CO3			_ '										
	<u>Unit-I</u> 12hrs.  Introduction to Wireless Communication Systems: Concept of cellular communication system, basics of												
quality, determi	s cellular , service ining the stem capa	quality, frequer	coverag	e and re	quired g	rade of s	service, o	co-chann	el interf	erence, f	requency	y reuse,	
	•			Uni	t-II							12 hrs.	
propaga	e Radio I ation med pler sprea	chanism	s, reflect	tion, diff	raction,						ding effe	ects due	
Modul	ation Te	chnique	es. Intro			modula	tion tach	nigues	minimur	n chift k		12 hrs.	
	ım shift k											aussian	
		, 8, -	- F F	Unit				~ ~~,		<i>y</i>		12 hrs.	
	Advanced Transceiver Schemes: Cellular code division multiple access systems, GSM, IS–95, IS-2000 and introduction to third generation wireless networks and standards.									S-2000			
T:41 -				RI			D BOOI	KS	p1 1	iala ar-			
Title	less com	municat	ione		Aut	<b>hor</b> S Rappa	nort			isher son Educ	cation 2	003	
1. wire	iess com	munical	10118		1.5	• каррај	ροιι		Pear	SOII EUU	cation, 2	003.	
2. Princ	ciples of l	Mobile (	Commur	nication	Go	rdon L.	Stuber		Spri 200	nger Inte 1.	rnationa	l Ltd.,	
3. Wire	eless Com	ımunica	nunications Andrea Goldsmith Cambridge University Press, 2007										



			Miono	<b></b>	704 P. N.	EC-323		. Annlia	otions				
				proces: L	sor & IV		ntroller T		eauons P		Credits		
				3			1	_	2	5			
		Session	al Mar			<u> </u>	-	-	_	50			
			mester l		tion Ma	arks					50		
Course	e	This co	ourse is a	nimed to	provide	knowle	dge abor	ut the in	terfacing	g of mic	roproces	sor and	
Object	tives:		ontroller		•		_		•	-	•		
Course	e	1. Unde	erstand th	e interfa	cing of o	different	peripher	als with	micropr	ocessor.			
Outcor	me:	2. Write the programs and interfacing of peripherals with microcontroller.											
		3. Design the circuit for interfacing.											
		T					vith prog	7		•		_	
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1		√ ,	V		V	√ ,							
CO2		√ ,	<b>V</b>		V	<b>V</b>							
CO3													
	<u>Unit-I</u> 12 hrs.  Memory Interfacing: Details of interfacing of PIC8259, Interfacing of memory chips ROM (2732, 2764)												
and RA Genera	AM (611 al Purp		rammak	ole Perip	herals I	nterfaci			•	-			
				Uni	t-II							12hrs.	
		MC 1488 ata Conv			•		809.						
				Uni	t-III							12 hrs.	
Interfa	cing of	Microco	ntroller	with M	emory:	Interfaci	ng of ext	ternal me	emory R	AM & E	PROM.		
	ncing o	f Micro		er with	Externa		_		•		per mot		
				<u>Uni</u>	t-IV							12 hrs.	
with 82	Interfacing: Interfacing of Microcontroller Data Converters, Interfacing to enhance 8051 capabilities with 8255 & Timer chips and LCD, ADC, DAC, Microcontroller Ports: Serial Communication, connection to RS 232.									ies			
						MENDE	D BOOI						
Title		Author Publisher											
1 Mics	ronrocas	cor Arch	V	Willey on	ctorn Itd	Mary Da	lh;						

RECOMMENDED BOOKS											
Title	Author	Publisher									
1. Microprocessor Architecture, programming and application with 8080/8085	Ramesh s Gaonker,	Willey eastern ltd New Delhi									
2. The 8051 Microcontroller and Embedded Systems	Ali Mazidi	Pearson Education									



	Microprocessor & Microcontroller Applications Lab												
Course		It inclu	This lab aims to design various applications based on microprocessor and microcontroller. It includes the interfacing of microcontroller with dc stepper motor, ADC, LED, LCD display, relays <i>etc</i> .										
Course		<ol> <li>Design various real time applications based on microprocessor and microcontroller such as traffic light, temperature control <i>etc</i>.</li> <li>Control the speed as well as step size of DC stepper motor.</li> <li>Interface various peripheral devices to microprocessor and microcontroller.</li> <li>Mapping of course outcomes with program outcomes</li> </ol>											
	1 -	T					`			T	T	T	
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1		V	V	V	V	V	V						
CO2			V	V	N	V	.,						
CO3													
		1. Write micropn 2. Write 3. Write 8255 Pl 4. Write and 825 5. Write 8255 Pl 6. Write 7. Write 8. Write 9. Write 10. Write 10. Write 10. Write 11.	rocessors e a progr e a progr PI. e a progr S5 PPI. e a progr PI. e a progr te a progr te a progr	am to co am for fi am to co am to co am to co am of Fl am to ge am to in am to co gram to co	55 PPI. Inding squartrol the entrol speciashing Lenerate a sterface the entrol a steriontrol the	traffic leed of Do. ED contract Ramp when ADC. tepper must speed	a number a number a number ture using the system of the contraction of	r using long 8085/em using 80 port 1 or using Direction, octor.	98085/8086 g 8085/8086 g 85/8086 f the Michael AC with	8085/80 able and croproces 086 micr microproces cro Contra micro cond numbers, LED, 1	verify. ssors and oprocess ocessors roller ontroller er of step	ors and	



	EC-324												
	Antenna Wave Propagation												
			]	L			T	F	•		Credits		
		3 0 0 3											
Sessional Marks 50											50		
		End Semester Examination Marks 50											
Course	Course The objective of this course is to provide students with opportunities to learn different types												
Object	<b>Objectives:</b> of antenna. This course provides an introduction to the basic antenna parameters, antenna												
		arrays, aperture type antennas and wave propagation.											
Course	e		<b>1.</b> Unc	lerstand	fundame	ntal tern	ns related	l to anter	na para	meters.			
Outcor	mes:		<b>2.</b> Unc	lerstandi	ng of dif	ferent ty	pes of ar	ntenna st	ructure f	for differ	ent		
			app	lications									
			<b>3.</b> Unc	lerstand	fundame	entals of	wave pro	pagation	1.				
			Mappi	ng of co	urse out	comes v	vith prog	gram ou	tcomes				
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1	V	V	V	V		V							
CO2													
CO3		V	V	V		V							
	Unit-I 12 hrs.												

**Basic Antenna Parameters:** Radiation patterns, antenna beam area, antenna beam width, radiation intensity, gain, directive gain, power gain, directivity (D), antenna bandwidth, effective height, reciprocity theorem, self-impedance, mutual impedance, radiation resistance, front to back ratio, radiation power density, isotropic radiators, near field and far field concept, radiation from a half wavelength dipole and its radiation resistance.

Unit-II 12hrs.

**Wire Radiators:** Voltage and current distribution, hertz dipole antenna, monopole radiators, loop antenna.

**Antenna Arrays:** introduction, linear uniform array of two & isotropic sources, principles of pattern multiplication, broadside arrays, end fire arrays, antenna for receiving and transmitting TV signals e.g. Yagi-Uda and turnstile antennas.

Unit-III 12 hrs.

**Aperture Type Antennas:** Aperture antennas, E & H -plane horns, pyramidal horn, reflector antennas, log periodic antenna, antenna measurements, microstrip antennas & their advantages.

Unit-IV 12 hrs.

**Propagation of Radio Waves:** structure of ionosphere, different modes of propagation: ground waves, space waves, space wave propagation over flat and curved earth, optical and radio horizons, surface waves and troposphere waves, wave propagation in the ionosphere, critical frequency, maximum usable frequency (MUF), skip distance and virtual height, radio noise of terrestrial and extra-terrestrial origin. effect of earth's curvature, duct propagation, troposphere scatter propagation.



RECOMMENDED BOOKS										
Title	Author	Publisher								
1. Antennas	Kraus	Mc Graw Hill								
2. Antenna and Wave	K D Parsad	Parkash Publications								
Propagation										



						EC-325	5					
					Mi	croelecti	ronics					
		L T P								Credits		
				3			0	2	2	4		
		Sessional Marks								50		
		End So	emester		50							
Course	e	The ob	jective o	f the subj	ject micr	oelectro	nics is to	discuss t	he desig	n and fab	rication	process
Object	tives:	The objective of the subject microelectronics is to discuss the design and fabrication process of thick film, thin film and hybrid IC's. It also aims at understanding each and every step of										
		fabrication from crystal growth to photolithography to manufacturing and to have a brief										
			knowledge of fabrication process flow and learning design and fabrication of BJT, diode,									
		FET, MOS etc.										
Course	e	1. Understand the physical and electrical properties of semiconductor materials and										
Outco	mes:	their use in microelectronic circuits.										
		2.	2. Develop an understanding about key aspects of the microelectronics industry, from									
		device design, to processing, to photolithography, to manufacturing and packaging.										
		3. Learn in brief many of the core problems involved with MOSFET technology.										
			Mapp	ing of co	urse ou	tcomes v	with pro	gram ou	tcomes			
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1				V								
$\sim$	1	1.1		V								
CO2		V										
		\ \ \		V								
CO2		<u> </u>		√ Un	it-I							12 hrs.
CO2 CO3	uction:	1	iges of I			ssification	n of IC's	(Linear/	Digital I	C's Mot		
CO2 CO3		Advanta		C's, Gen		ssification	n of IC's	(Linear/	Digital I	C's, Moi		
CO2 CO3 Introd	Basic IC	Advanta: fabricat	ion steps	C's, Gen	eral clas					-	nolithic/	Hybrid
CO2 CO3 Introd IC's), I	Basic IC fication	Advanta: fabricat	ion steps	C's, Gen	eral clas		n of IC's			-	nolithic/	Hybrid
CO2 CO3 Introd	Basic IC fication	Advanta: fabricat	ion steps	C's, Gen	eral clas					-	nolithic/	Hybrid

and fabrication.

Monolithic Techniques: Process on silicon crystals, line growth, refining, substrate slicing, polishing, chemical vapour deposition, thermal oxidation, photolithography, diffusion, impurities, diffusion system, ion implantation, metallization, isolation.

> 12 hrs. **Unit-III**

Thermal oxidation: Thermal oxidation process (kinetics of growth, thin oxide growth), effect of impurities on the oxidation rate.

Photolithography: Pattern generation/Mask making, Contact and Proximity printing, photo resist, photolithography, process (lift off technology, fine line photolithography).

> **Unit-IV** 12 hrs.

Diffusion: Basic diffusion process(diffusion equation, diffusion profiles), extrinsic diffusion, lateral diffusion.

Ion Implantation: Ion implantation process (ion distribution, ion stopping), implant damage and annealing process (furnace and rta)

RECOMMENDED BOOKS									
Title	Author	Publisher							
1. Integrated Circuits	Millman and Halkias	Mc Graw Hill							
2. Integrated Circuits	K R Botkar	TMH							



Microelectronics Lab													
Course		The objective of micro-electronics lab is to make the students familiar with PCB designing processes. It also includes layout designing using different software followed by etching and mounting the components on final prepared PCB.											
Course Outcomes:  1. Understand the working of PCB making CNC Mill-15. 2. Design the layout of electronic circuit with the help of various software. 3. Perform various steps involved in the design of PCB.									ware.				
	Mapping of course outcomes with program outcomes												
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	
CO1					,	,	V						
CO2					V	√	√						
CO3					$\sqrt{}$								
	List of Experiments:  1. To Study and observe the PCB making Machine CNC Mill- 15.  2. To Study and observe the Mach Mill 3 Software.  3. To Study and observe the Sprint Layout Software.  4. To Study and observe the Copper CAM Software.  5. To design and Implement the PCB using PCB making Machine.  6. To prepare Negative of a given artwork using Vertical process Camera.  7. To perform Dip Coating Operation on Copper Clad Board by using dip Coating machine.  8. To perform etching operation on given Printed Copper Clad Board.  9. To mount the Components on prepared PCB.  10. To test or Inspect the PCB.												



	EC-327												
	Project												
		L T P Credits									lits		
			Project Work aims at developing innovative skills in the students whereby they apply										
Course													
Object	tives:	totality the knowledge and skills gained through the course work in the solu											
		particular problem or by undertaking a project. In addition, the project work is intended											
		place students for project oriented practical training in actual work situation for the											
		_	•	iod with									
		1.									e classroo		
											nce and co		
											nic/institu		
											world of v		
		2.		iop abili alues etc		interper	sonai sk	ilis, con	nmunica	tion skills	s, positive	attitudes	
		3.					lina notu	ro of fi	ald more	in which	students	oro going	
		3.					ting the				students	are going	
Course	Ω	1.									the techr	ical and	
Outco		<b>1.</b> Refine and complete the selected project making use of the technical and engineering knowledge which meets the expected outcome.											
Outcol	iiics.	2. Work with the modern tools required for the implementation of the project.											
		3. Achieve the results within in the stipulated time.											
		4. Acquire problem solving, system integration, project management, documentation,											
							tion skill		<b>.</b>		.,	,	
		ı					s with p		outcon	ies			
	PO1	PO2 PO3 PO4 PO5 PO6 PO7 PO8 PO9 PO10 PO11 PO12											
CO1	V	V	V	$\sqrt{}$	V	V							
CO2	V						$\sqrt{}$						
CO3											V		
CO4											V	V	